

IGBT – Field Stop, Trench

650 V, 75 A

FGH75T65SHD

Description

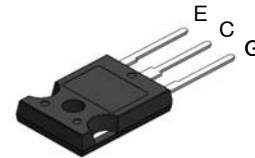
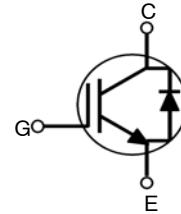
Using novel field stop IGBT technology, onsemi's new series of field stop 3rd generation IGBTs offer the optimum performance for solar inverter, UPS, welder, telecom, ESS and PFC applications where low conduction and switching losses are essential.

Features

- Maximum Junction Temperature: $T_J = 175\text{ }^{\circ}\text{C}$
- Positive Temperature Co-efficient for Easy Parallel Operating
- High Current Capability
- Low Saturation Voltage: $V_{CE(sat)} = 1.6\text{ V (Typ.) @ } I_C = 75\text{ A}$
- 100% of the Parts Tested for I_{LM} (Note 1)
- High Input Impedance
- Fast Switching
- Tighten Parameter Distribution
- This Device is Pb-Free and is RoHS Compliant

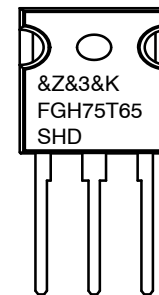
Applications

- Solar Inverter, UPS, Welder, PFC, Telecom, ESS, PFC



TO-247-3LD
CASE 340CH

MARKING DIAGRAM



&Z	= Assembly Plant Code
&3	= Numeric Date Code
&K	= Lot Code
FGH75T65SHD	= Specific Device Code

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

FGH75T65SHD

ABSOLUTE MAXIMUM RATINGS

Description		Symbol	FGH75T65SHD-F155	Unit
Collector to Emitter Voltage		V _{CES}	650	V
Gate to Emitter Voltage		V _{GES}	±20	V
Transient Gate to Emitter Voltage			±30	V
Collector Current	T _C = 25 °C	I _C	150	A
Collector Current	T _C = 100 °C		75	A
Pulsed Collector Current (Note 1)	T _C = 25 °C	I _{LM}	225	A
Pulsed Collector Current (Note 2)		I _{CM}	225	A
Diode Forward Current	T _C = 25 °C	I _F	75	A
Diode Forward Current	T _C = 100 °C		50	A
Pulsed Diode Maximum Forward Current (Note 2)		I _{FM}	225	A
Maximum Power Dissipation	T _C = 25 °C	P _D	455	W
Maximum Power Dissipation	T _C = 100 °C		227	W
Operating Junction Temperature		T _J	−55 to +175	°C
Storage Temperature Range		T _{stg}	−55 to +175	°C
Maximum Lead Temp. for Soldering Purposes, 1/8” from Case for 5 Seconds		T _L	300	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. $V_{CC} = 400\text{ V}$, $V_{GE} = 15\text{ V}$, $I_C = 225\text{ A}$, $R_G = 20\text{ }\Omega$, Inductive Load
2. Repetitive Rating: Pulse width limited by max. junction temperature.

THERMAL CHARACTERISTICS

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction to Case (IGBT)	$R_{\theta JC}$	0.33	$^{\circ}\text{C/W}$
Thermal Resistance, Junction to Case (Diode)	$R_{\theta JC}$	0.65	$^{\circ}\text{C/W}$
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	40	$^{\circ}\text{C/W}$

PACKAGE MARKING AND ORDERING INFORMATION

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGH75T65SHD	FGH75T65SHD-F155	TO-247-3LD	–	–	30

ELECTRICAL CHARACTERISTICS OF THE IGBT ($T_C = 25\text{ }^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Collector to Emitter Breakdown Voltage	BV_{CES}	$V_{GE} = 0\text{ V}$, $I_C = 1\text{ mA}$	650	–	–	V
Temperature Coefficient of Breakdown Voltage	$\Delta BV_{CES}/\Delta T_J$	$I_C = 1\text{ mA}$, Referenced to $25\text{ }^{\circ}\text{C}$	–	0.6	–	$\text{V}/^{\circ}\text{C}$
Collector Cut-Off Current	I_{CES}	$V_{CE} = V_{CES}$, $V_{GE} = 0\text{ V}$	–	–	250	μA
G–E Leakage Current	I_{GES}	$V_{GE} = V_{GES}$, $V_{CE} = 0\text{ V}$	–	–	± 400	nA

ON CHARACTERISTICS

G–E Threshold Voltage	$V_{GE(th)}$	$I_C = 75\text{ mA}$, $V_{CE} = V_{GE}$	4.0	5.5	7.5	V
Collector to Emitter Saturation Voltage	$V_{CE(sat)}$	$I_C = 75\text{ A}$, $V_{GE} = 15\text{ V}$	–	1.6	2.1	V
		$I_C = 75\text{ A}$, $V_{GE} = 15\text{ V}$, $T_C = 175\text{ }^{\circ}\text{C}$	–	2.28	–	V

FGH75T65SHD

ELECTRICAL CHARACTERISTICS OF THE IGBT ($T_C = 25^\circ\text{C}$ unless otherwise noted) (continued)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
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DYNAMIC CHARACTERISTICS

Input Capacitance	C_{ies}	$V_{CE} = 30\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$	–	3680	–	pF
Output Capacitance	C_{oes}		–	179	–	pF
Reverse Transfer Capacitance	C_{res}		–	43	–	pF

SWITCHING CHARACTERISTICS

Turn-On Delay Time	$t_{d(on)}$	$V_{CC} = 400\text{ V}, I_C = 75\text{ A},$ $R_G = 3\ \Omega, V_{GE} = 15\text{ V},$ Inductive Load, $T_C = 25^\circ\text{C}$	–	28	–	ns
Rise Time	t_r		–	56	–	ns
Turn-Off Delay Time	$t_{d(off)}$		–	80	–	ns
Fall Time	t_f		–	14.4	–	ns
Turn-On Switching Loss	E_{on}		–	2.4	–	mJ
Turn-Off Switching Loss	E_{off}		–	0.72	–	mJ
Total Switching Loss	E_{ts}		–	3.12	–	mJ
Turn-On Delay Time	$t_{d(on)}$	$V_{CC} = 400\text{ V}, I_C = 75\text{ A},$ $R_G = 3\ \Omega, V_{GE} = 15\text{ V},$ Inductive Load, $T_C = 175^\circ\text{C}$	–	26.4	–	ns
Rise Time	t_r		–	58.4	–	ns
Turn-Off Delay Time	$t_{d(off)}$		–	86.4	–	ns
Fall Time	t_f		–	13.6	–	ns
Turn-On Switching Loss	E_{on}		–	3.7	–	mJ
Turn-Off Switching Loss	E_{off}		–	0.98	–	mJ
Total Switching Loss	E_{ts}		–	4.68	–	mJ
Total Gate Charge	Q_g	$V_{CC} = 400\text{ V}, I_C = 75\text{ A},$ $V_{GE} = 15\text{ V}$	–	123	–	nC
Gate to Emitter Charge	Q_{ge}		–	22.6	–	nC
Gate to Collector Charge	Q_{gc}		–	44.9	–	nC

ELECTRICAL CHARACTERISTICS OF THE DIODE ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Conditions		Min	Typ	Max	Unit
Diode Forward Voltage	V_{FM}	$I_F = 50\text{ A}$	$T_C = 25\text{ }^{\circ}\text{C}$	–	2.2	2.7	V
			$T_C = 175\text{ }^{\circ}\text{C}$	–	1.8	–	
Reverse Recovery Energy	E_{rec}	$I_F = 50\text{ A},$ $di_F/dt = 200\text{ A}/\mu\text{s}$	$T_C = 175\text{ }^{\circ}\text{C}$	–	60	–	μJ
Diode Reverse Recovery Time	t_{rr}		$T_C = 25\text{ }^{\circ}\text{C}$	–	43.4	–	ns
			$T_C = 175\text{ }^{\circ}\text{C}$	–	207	–	
Diode Reverse Recovery Charge	Q_{rr}		$T_C = 25\text{ }^{\circ}\text{C}$	–	87.9	–	nC
			$T_C = 175\text{ }^{\circ}\text{C}$	–	1243	–	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.



TYPICAL PERFORMANCE CHARACTERISTICS

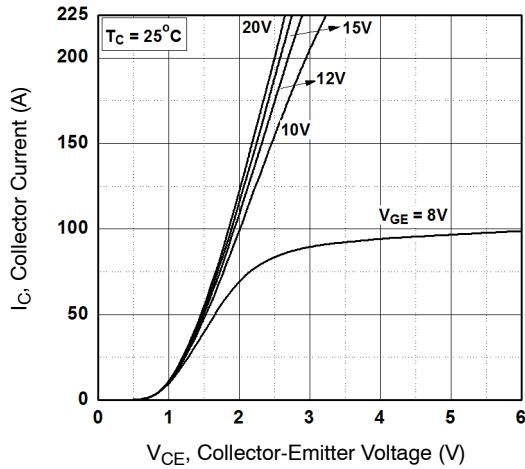


Figure 1. Typical Output Characteristics

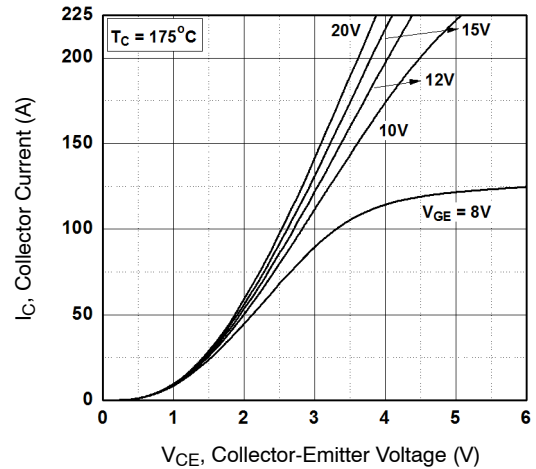


Figure 2. Typical Output Characteristics

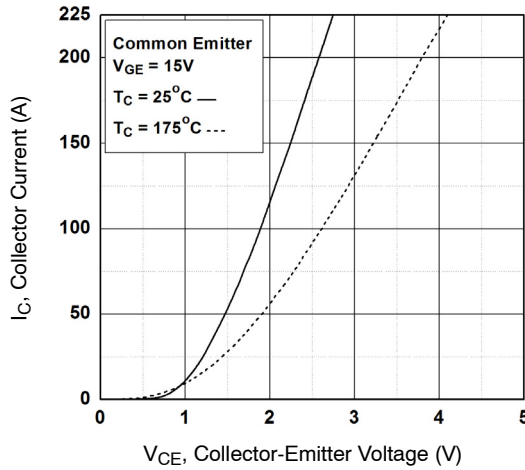


Figure 3. Typical Saturation Voltage Characteristics

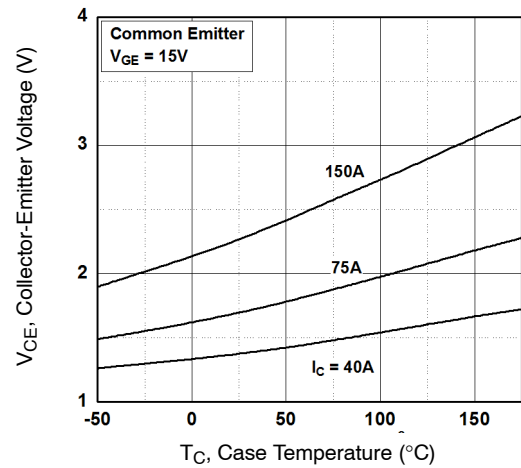


Figure 4. Saturation Voltage vs. Case Temperature at Variant Current Level

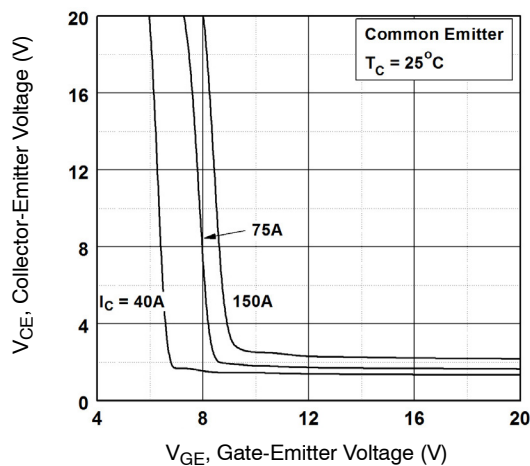


Figure 5. Saturation Voltage vs V_{GE}

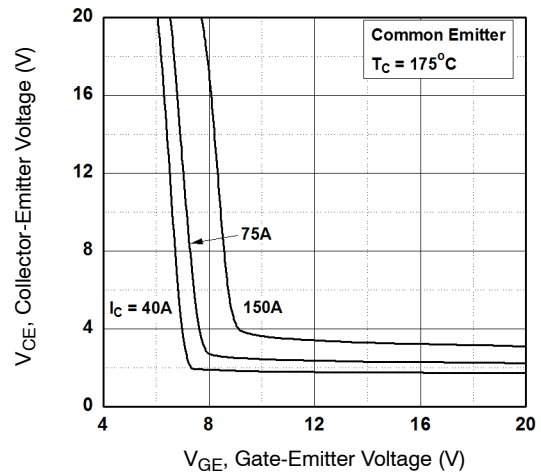


Figure 6. Saturation Voltage vs V_{GE}

TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

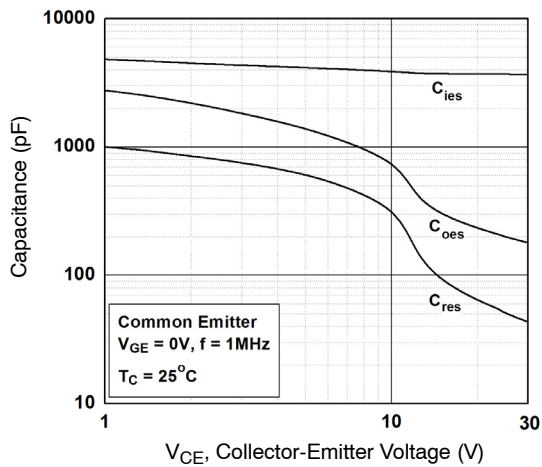


Figure 7. Capacitance Characteristics

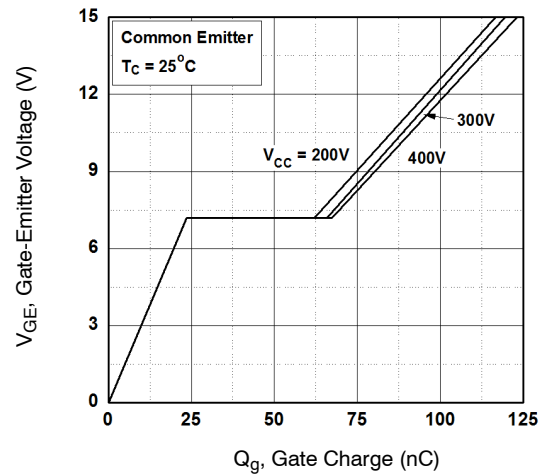


Figure 8. Gate Charge Characteristics

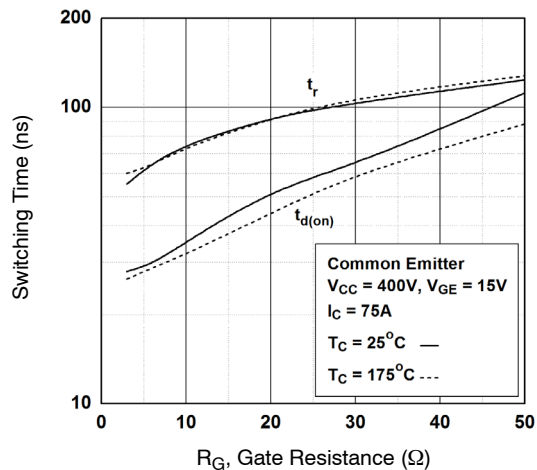


Figure 9. Turn-On Characteristics vs. Gate Resistance

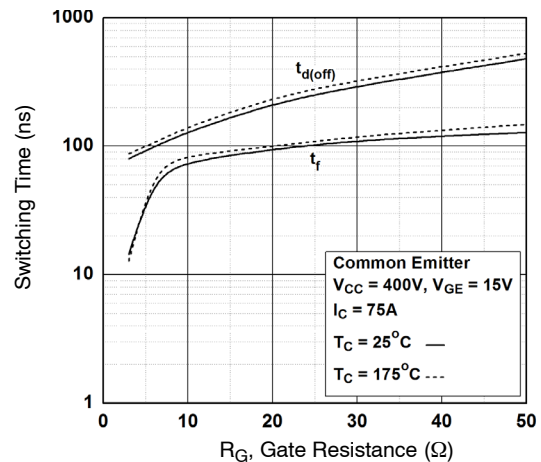


Figure 10. Turn-Off Characteristics vs. Gate Resistance

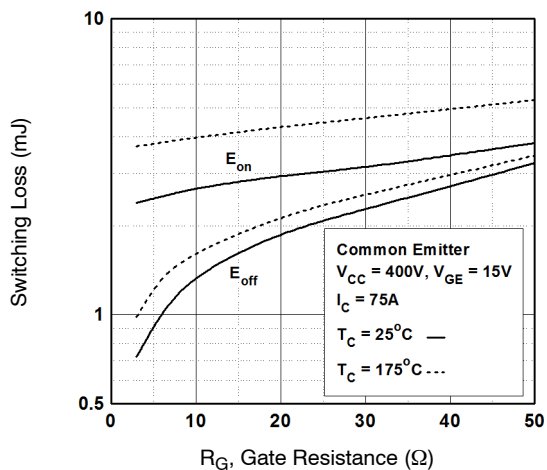


Figure 11. Switching Loss vs. Gate Resistance

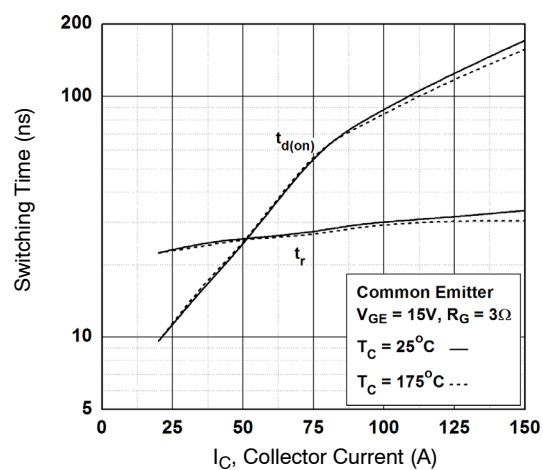


Figure 12. Turn-On Characteristics vs. Collector Current

TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

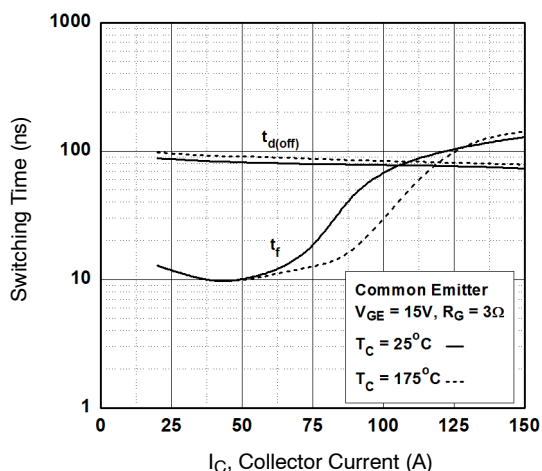


Figure 13. Turn-Off Characteristics vs. Collector Current

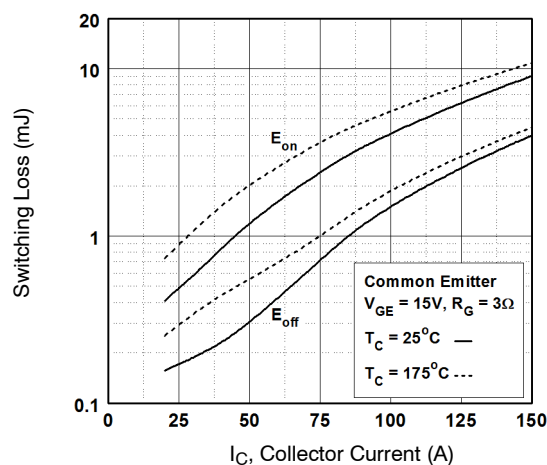


Figure 14. Switching Loss vs. Collector Current

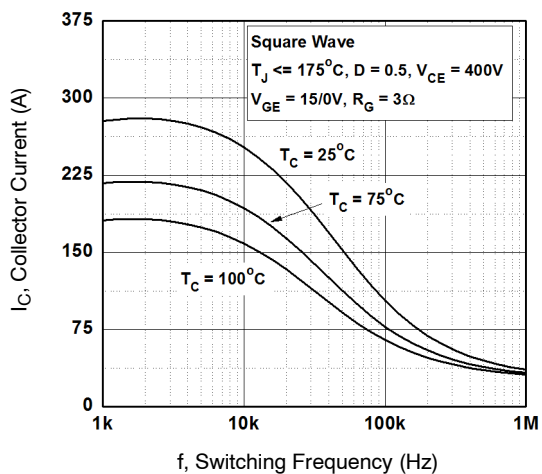


Figure 15. Load Current vs. Frequency

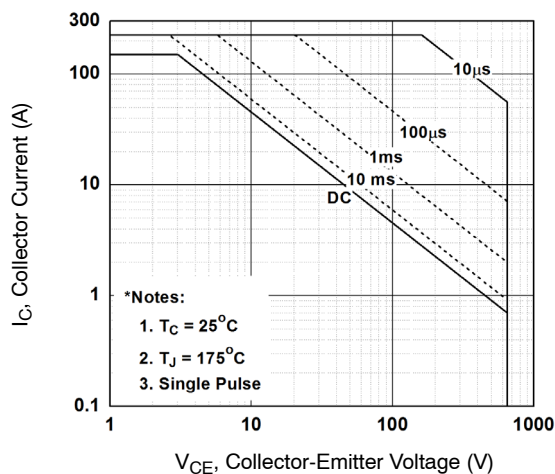


Figure 16. SOA Characteristics

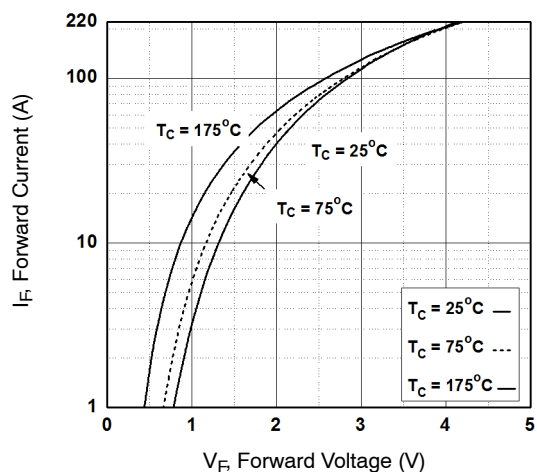


Figure 17. Forward Characteristics

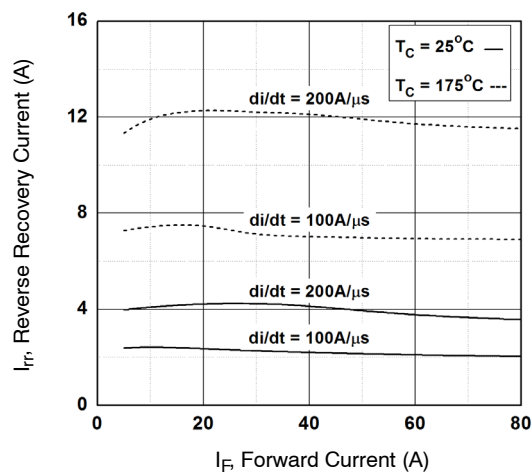


Figure 18. Reverse Recovery Current

TYPICAL PERFORMANCE CHARACTERISTICS (CONTINUED)

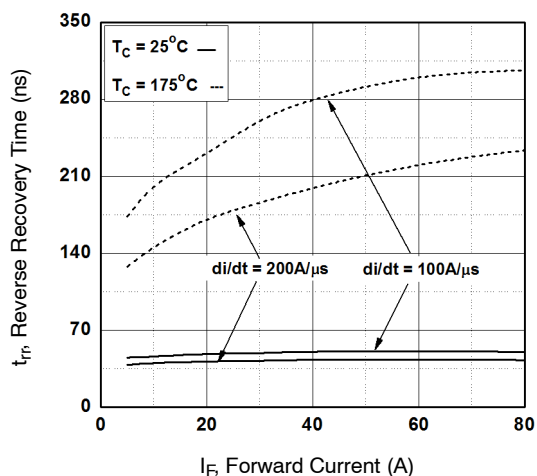


Figure 19. Reverse Recovery Time

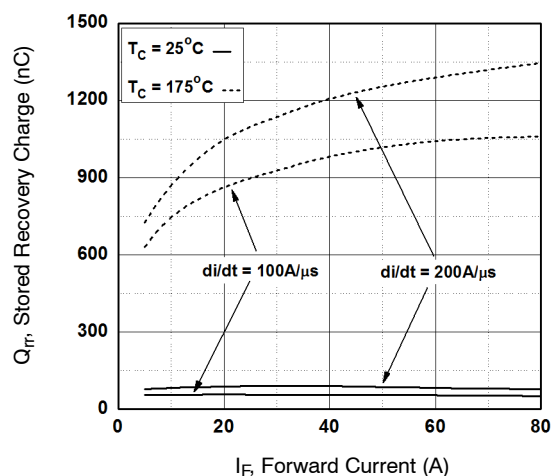


Figure 20. Stored Charge

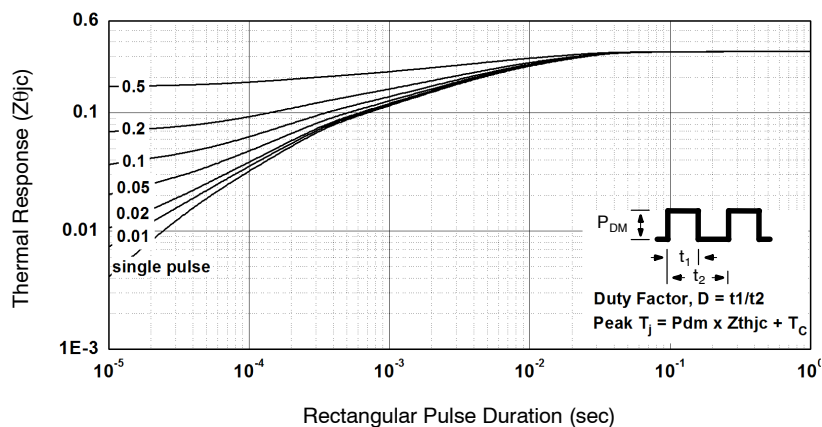


Figure 21. Transient Thermal Impedance of IGBT

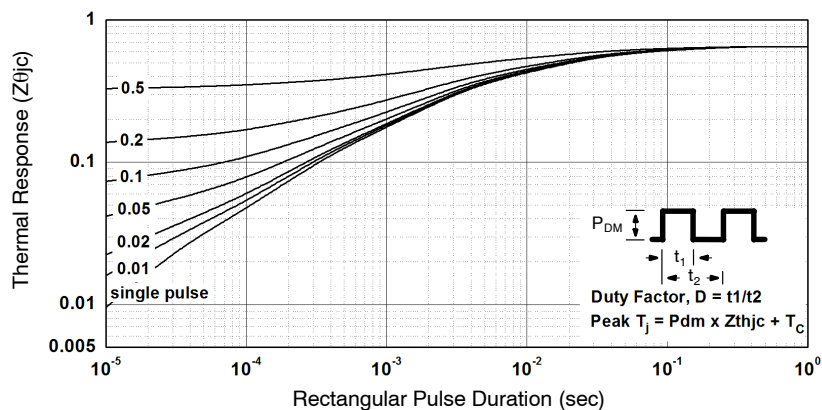


Figure 22. Transient Thermal Impedance of Diode

FGH75T65SHD

REVISION HISTORY

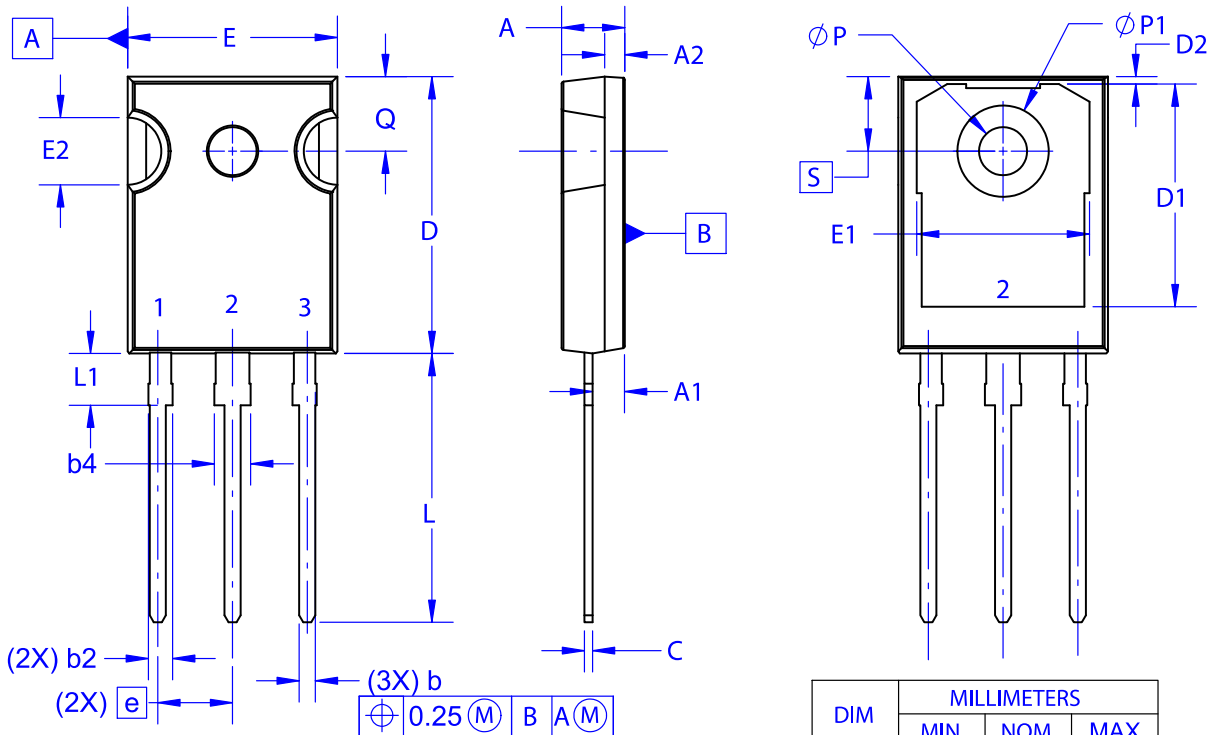
Revision	Description of Changes	Date
5	Document rebranded to onsemi format.	4/22/2026

This document has undergone updates prior to the inclusion of this revision history table. The changes tracked here only reflect updates made on the noted approval dates.



TO-247-3LD
CASE 340CH
ISSUE A

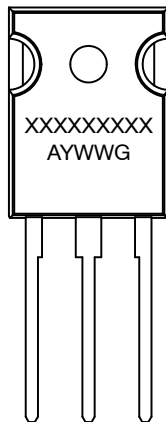
DATE 09 OCT 2019



NOTES: UNLESS OTHERWISE SPECIFIED.

- A. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR EXTRUSIONS.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. DRAWING CONFORMS TO ASME Y14.5 - 2009.
- D. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.
- E. LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

GENERIC
MARKING DIAGRAM*



XXXX = Specific Device Code
A = Assembly Location
Y = Year
WW = Work Week
G = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	TO-247-3LD	PAGE 1 OF 1

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