

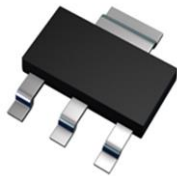
Features

- $BV_{CEO} > 300V$
- $BV_{CBO} > 300V$
- $I_C = 0.5A$ High Continuous Current
- $I_{CM} = 1A$ Peak Pulse Current
- Complementary PNP Type: [FZT757](#)
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/automotive-products/>.**
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. <https://www.diodes.com/quality/product-definitions/>

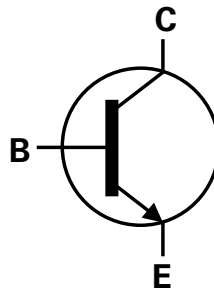
Mechanical Data

- Package: SOT223
- Package Material: Molded Plastic, "Green" Molding Compound; UL Flammability Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin Plated Leads; Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.112 grams (Approximate)

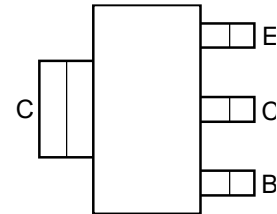
SOT223 (Type DN)



Top View



Device Symbol



Top View Pinout

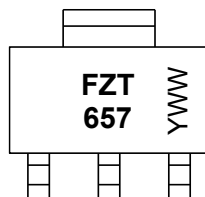
Ordering Information (Note 4)

Orderable Part Number	Package	Marking	Reel Size (inches)	Tape Width (mm)	Packing	
					Qty.	Carrier
FZT657TA	SOT223 (Type DN)	FZT657	7	12	1,000	Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

SOT223 (Type DN)



FZT 657 = Product Type Marking Code
 YWW = Date Code Marking
 Y or \bar{Y} = Last Digit of Year (ex: 6 = 2026)
 WW or $\bar{W}W$ = Week Code (01 to 53)

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	300	V
Collector-Emitter Voltage	V _{CEO}	300	V
Emitter-Base Voltage	V _{EBO}	7	V
Continuous Collector Current	I _C	0.5	A
Peak Pulse Current	I _{CM}	1	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation	P _D	(Note 5)	3
		(Note 6)	2
		(Note 7)	1.6
		(Note 8)	1.2
Thermal Resistance, Junction to Ambient	R _{θJA}	(Note 5)	41.7
		(Note 6)	62.5
		(Note 7)	78.1
		(Note 8)	104
Thermal Resistance, Junction to Lead	R _{θJL}	12.9	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

ESD Ratings (Note 10)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	C

- Notes:
5. For a device mounted with the collector lead on 50mm x 50mm 2oz copper that is on a single-sided 1.6mm FR-4 PCB; device is measured under still air conditions whilst operating in a steady state.
 6. Same as Note 5, except the device is mounted on 25mm x 25mm 2oz copper.
 7. Same as Note 5, except the device is mounted on 25mm x 25mm 1oz copper.
 8. Same as Note 5, except the device is mounted on minimum recommended pad layout.
 9. Thermal resistance from junction to solder-point (at the end of the collector lead).
 10. Refer to JEDEC specifications JESD22-A114 and JESD22-A115.

Thermal Characteristics and Derating Information

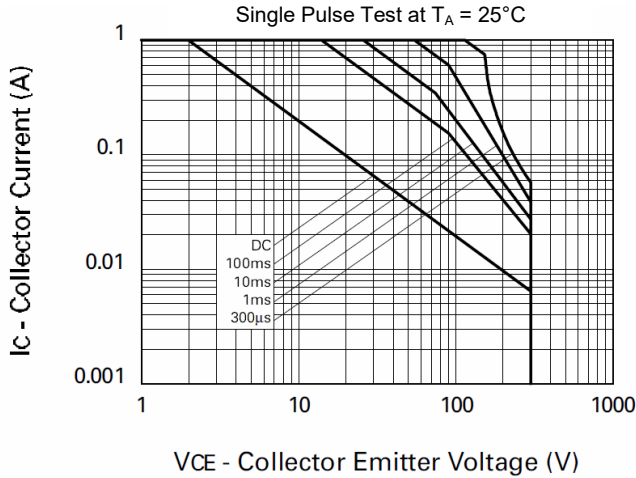


Figure 1. Safe Operating Area

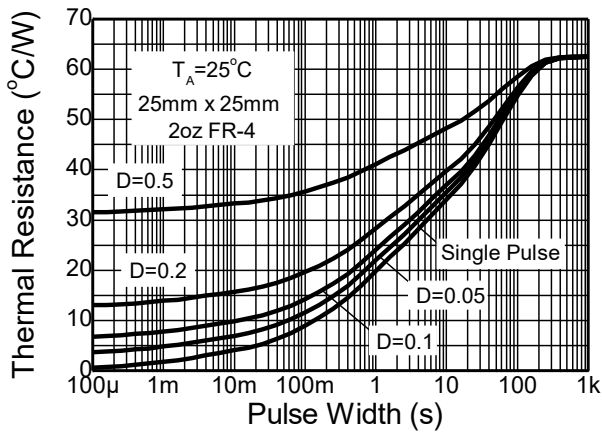


Figure 2. Transient Thermal Impedance

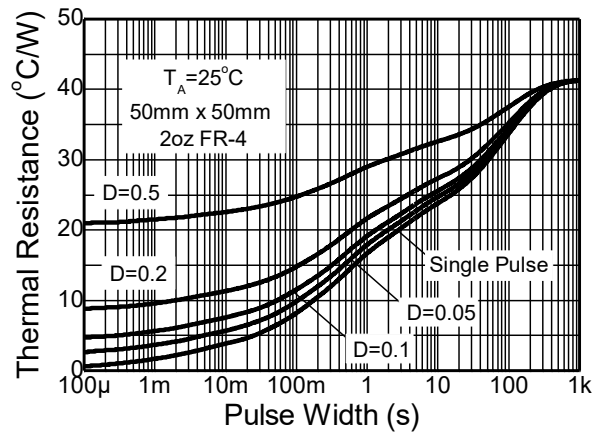


Figure 3. Transient Thermal Impedance

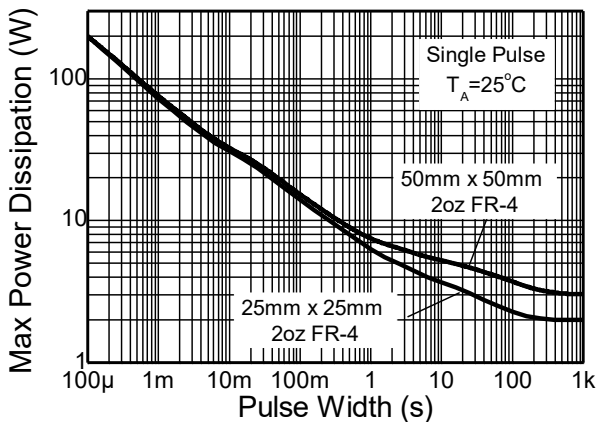


Figure 4. Pulse Power Dissipation

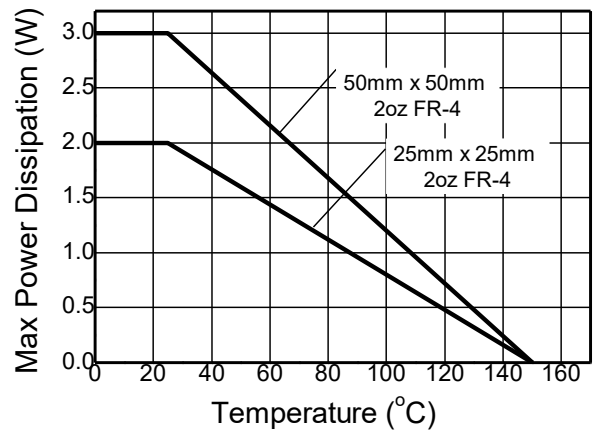


Figure 5. Derating Curve

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV _{CB0}	300	—	—	V	I _C = 100μA
Collector-Emitter Breakdown Voltage (Note 11)	BV _{CEO}	300	—	—	V	I _C = 10mA
Emitter-Base Breakdown Voltage	BV _{EB0}	7	—	—	V	I _E = 100μA
Collector-Base Cutoff Current	I _{CB0}	—	—	100	nA	V _{CB} = 200V
Emitter Cutoff Current	I _{EB0}	—	—	100	nA	V _{EB} = 5.6V
DC Current Gain (Note 11)	h _{FE}	40 50	— —	— —	—	I _C = 10mA, V _{CE} = 5V I _C = 100mA, V _{CE} = 5V
Collector-Emitter Saturation Voltage (Note 11)	V _{CE(sat)}	—	—	0.5	V	I _C = 100mA, I _B = 10mA
Base-Emitter Saturation Voltage (Note 11)	V _{BE(sat)}	—	—	1	V	I _C = 100mA, I _B = 10mA
Base-Emitter Turn-On Voltage (Note 11)	V _{BE(on)}	—	—	1	V	I _C = 100mA, V _{CE} = 5V
Output Capacitance	C _{obo}	—	—	20	pF	V _{CB} = 20V, f = 1MHz
Current Gain-Bandwidth Product	f _T	30	—	—	MHz	V _{CE} = 20V, I _C = 10mA f = 20MHz

Note: 11. Measured under pulsed conditions. Pulse width ≤ 300μs. Duty cycle ≤ 2%.

Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

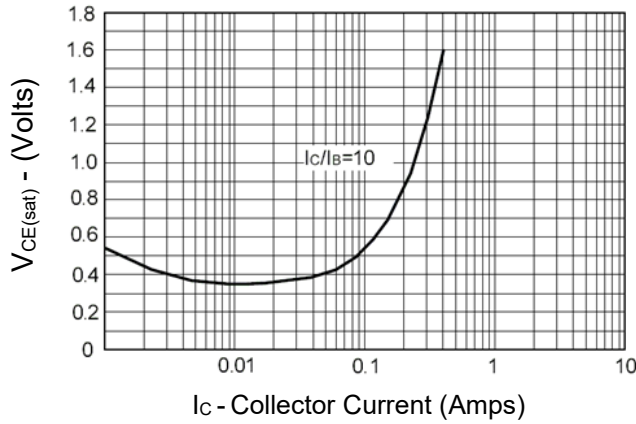


Figure 6. $V_{CE(sat)}$ vs. I_c

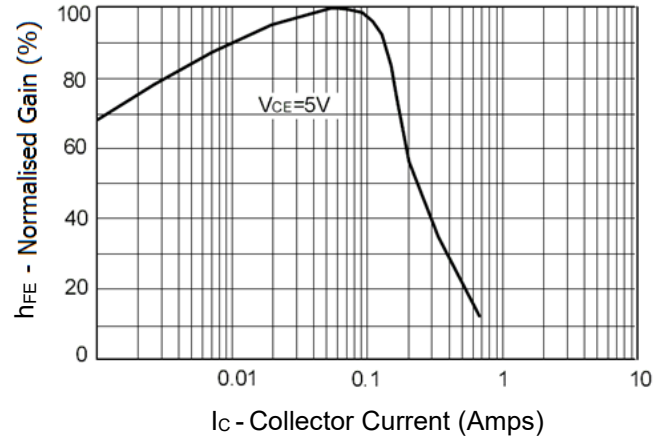


Figure 7. h_{FE} vs. I_c

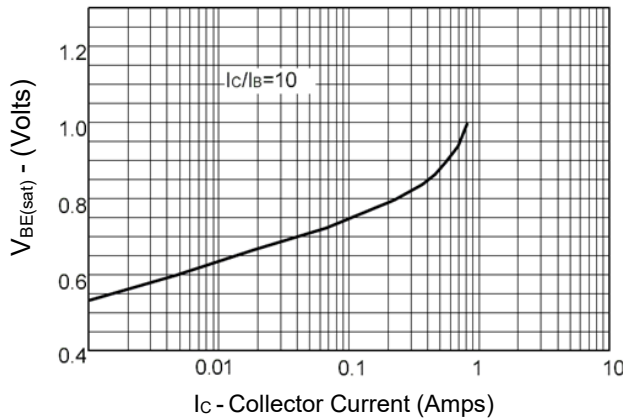


Figure 8. $V_{BE(sat)}$ vs. I_c

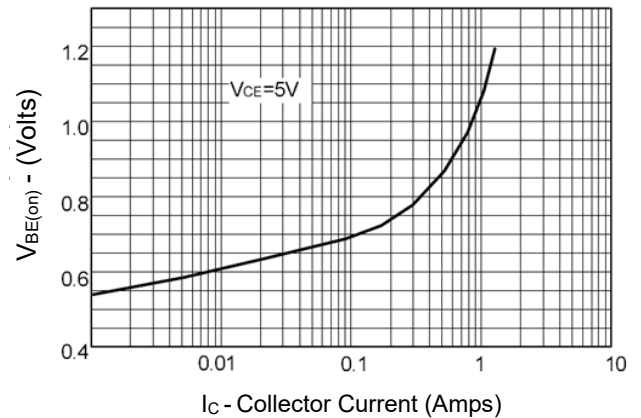


Figure 9. $V_{BE(on)}$ vs. I_c

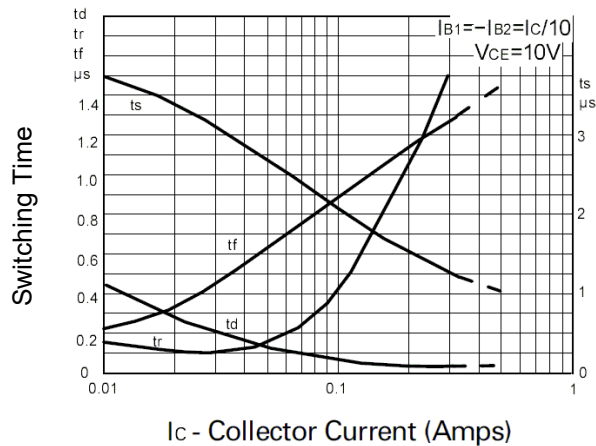


Figure 10. Switching Speeds

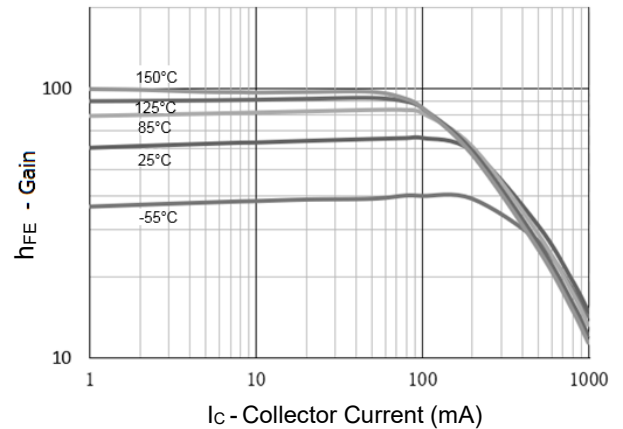
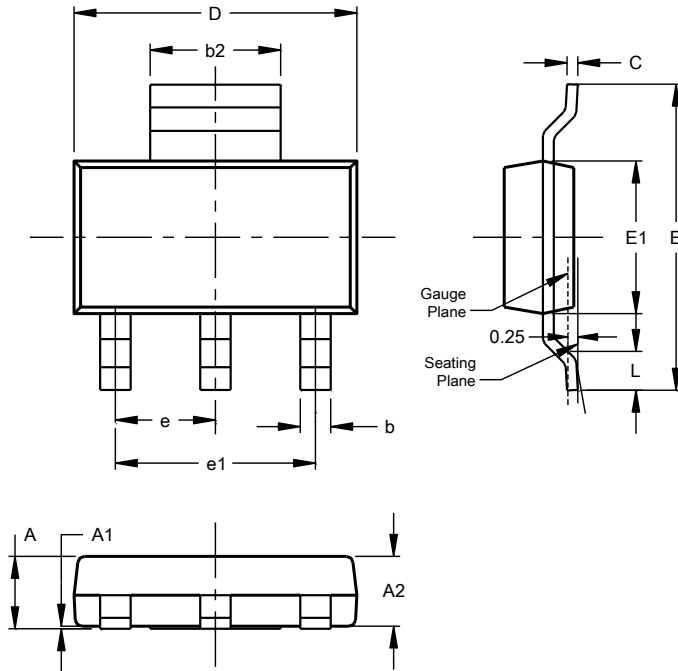


Figure 11. h_{FE} vs. I_c

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT223 (Type DN)

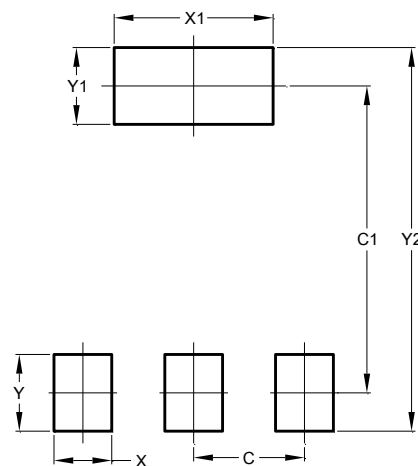


SOT223 (Type DN)			
Dim	Min	Max	Typ
A	--	1.70	--
A1	0.01	0.15	--
A2	1.50	1.68	1.60
b	0.60	0.80	0.70
b2	2.90	3.10	--
c	0.20	0.32	--
D	6.30	6.70	--
E	6.70	7.30	--
E1	3.30	3.70	--
e	--	--	2.30
e1	--	--	4.60
L	0.85	--	--
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

SOT223 (Type DN)



Dimensions	Value (in mm)
C	2.30
C1	6.40
X	1.20
X1	3.30
Y	1.60
Y1	1.60
Y2	8.00

Note: 12. For high-voltage applications, the appropriate industry sector guidelines should be considered with regards to voltage spacing between terminals.

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