

# Constant Current Regulator & LED Driver for A/C off-line Applications

120 V, 50 mA  $\pm$  15%, 3 W Package

## NSIC2050JBT3G

The linear constant current regulator (CCR) is a simple, economical and robust device designed to provide a cost-effective solution for regulating current in LEDs (similar to Constant Current Diode, CCD). The CCR is based on Self-Biased Transistor (SBT) technology and regulates current over a wide voltage range. It is designed with a negative temperature coefficient to protect LEDs from thermal runaway at extreme voltages and currents.

The CCR turns on immediately and is at 20% of regulation with only 0.5 V  $V_{AK}$ . It requires no external components allowing it to be designed as a high or low-side regulator.

The 120 V anode-cathode voltage rating is designed to withstand the high peak voltage incurred in A/C offline applications. The high anode-cathode voltage rating withstands surges common in Automotive, Industrial and Commercial Signage applications.

### Features

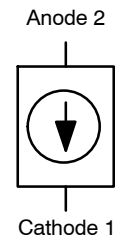
- Robust Power Package: 2.3 W
- Wide Operating Voltage Range
- Immediate Turn-On
- Voltage Surge Suppressing – Protecting LEDs
- UL94–V0 Certified
- SBT (Self-Biased Transistor) Technology
- Negative Temperature Coefficient
- Also available in 30 mA (NSIC2030JBT1G) and 20 mA (NSIC2020JBT1G)
- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC–Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

### Typical Applications and Reference/Design Documents

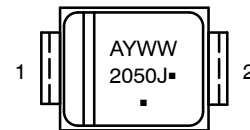
- Automobile: Chevron Side Mirror Markers, Cluster, Displays & Instruments Backlighting, CHMSL, Map Light
- AC Lighting Panels, Display Signage, Decorative Lighting, Channel Lettering
- Application Note [AND8349/D](#) – Automotive CHMSL
- Application Notes [AND8391/D](#), [AND9008/D](#) – Power Dissipation Considerations
- Application Note [AND8433/D](#) – A/C Application
- Application Note [AND8492/D](#) – A/C Capacitive Drop Design
- Application Note [AND9098/D](#) – Protecting a CCR from ISO 7637–2 Pulse 2A and Reverse Pulses

$$I_{reg(SS)} = 50 \text{ mA}$$

$$@ V_{AK} = 7.5 \text{ V}$$



### MARKING DIAGRAM



2050J = Specific Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
▪ = Pb–Free Package

(Note: Microdot may be in either location)

### ORDERING INFORMATION

| Device        | Package          | Shipping <sup>†</sup>  |
|---------------|------------------|------------------------|
| NSIC2050JBT3G | SMB<br>(Pb–Free) | 2,500 /<br>Tape & Reel |
| NSVC2050JBT3G | SMB<br>(Pb–Free) | 2,500 /<br>Tape & Reel |

<sup>†</sup> For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, [BRD8011/D](#).

Continued

- Design Note DN05013 – A/C Design
- Design Note DN06065 – A/C Design with PFC

**MAXIMUM RATINGS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

| Symbol              | Rating                                           | Value                                | Unit             |
|---------------------|--------------------------------------------------|--------------------------------------|------------------|
| $V_{AK\text{ Max}}$ | Anode-Cathode Voltage                            | 120                                  | V                |
| $V_R$               | Reverse Voltage                                  | 500                                  | mV               |
| $T_J, T_{stg}$      | Operating Junction and Storage Temperature Range | -55 to +175                          | $^\circ\text{C}$ |
| ESD                 | ESD Rating: Human Body Model<br>Machine Model    | Class 3A (4000 V)<br>Class C (400 V) |                  |

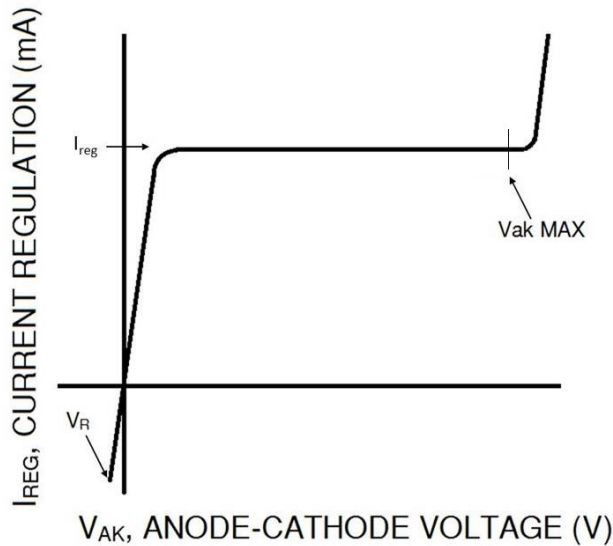
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

**ELECTRICAL CHARACTERISTICS** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

| Symbol         | Characteristic                                          | Min  | Typ  | Max  | Unit |
|----------------|---------------------------------------------------------|------|------|------|------|
| $I_{reg(SS)}$  | Steady State Current @ $V_{AK} = 7.5\text{ V}$ (Note 1) | 42.5 | 50   | 57.5 | mA   |
| $V_{overhead}$ | Voltage Overhead (Note 2)                               |      | 1.8  |      | V    |
| $I_{reg(P)}$   | Pulse Current @ $V_{AK} = 7.5\text{ V}$ (Note 3)        | 48.1 | 57.4 | 66.7 | mA   |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1.  $I_{reg(SS)}$  steady state is the voltage ( $V_{AK}$ ) applied for a time duration  $\geq 80\text{ sec}$ , using  $100\text{ mm}^2$ , 1 oz. Cu (or equivalent), in still air.
2.  $V_{overhead} = V_{in} - V_{LEDs}$ .  $V_{overhead}$  is typical value for 80%  $I_{reg(SS)}$ .
3.  $I_{reg(P)}$  non-repetitive pulse test. Pulse width  $t \leq 360\text{ }\mu\text{sec}$ .



**Figure 1. CCR Voltage-Current Characteristic**

# THERMAL CHARACTERISTICS

| Symbol          | Characteristic                                                                                 | Max          | Unit                       |
|-----------------|------------------------------------------------------------------------------------------------|--------------|----------------------------|
| $P_D$           | Total Device Dissipation (Note 1) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$  | 1210<br>8.0  | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 1)                                               | 124          | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 1)                                                    | 17.5         | $^\circ\text{C/W}$         |
| $P_D$           | Total Device Dissipation (Note 2) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$  | 1282<br>8.5  | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 2)                                               | 117          | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 2)                                                    | 18.2         | $^\circ\text{C/W}$         |
| $P_D$           | Total Device Dissipation (Note 3) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$  | 1667<br>11.1 | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 3)                                               | 90           | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 3)                                                    | 16.4         | $^\circ\text{C/W}$         |
| $P_D$           | Total Device Dissipation (Note 4) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$  | 1765<br>11.8 | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 4)                                               | 85           | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 4)                                                    | 16.7         | $^\circ\text{C/W}$         |
| $P_D$           | Total Device Dissipation (Note 5) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$  | 1948<br>13   | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 5)                                               | 77           | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 5)                                                    | 15.5         | $^\circ\text{C/W}$         |
| $P_D$           | Total Device Dissipation (Note 6) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$  | 2055<br>12.7 | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 6)                                               | 73           | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 6)                                                    | 15.6         | $^\circ\text{C/W}$         |
| $P_D$           | Total Device Dissipation (Note 7) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$  | 2149<br>14.3 | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 7)                                               | 69.8         | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 7)                                                    | 14.8         | $^\circ\text{C/W}$         |
| $P_D$           | Total Device Dissipation (Note 8) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$  | 2269<br>15.1 | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 8)                                               | 66.1         | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 8)                                                    | 14.8         | $^\circ\text{C/W}$         |
| $P_D$           | Total Device Dissipation (Note 9) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$  | 2609<br>17.4 | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 9)                                               | 57.5         | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 9)                                                    | 13.9         | $^\circ\text{C/W}$         |
| $P_D$           | Total Device Dissipation (Note 10) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | 2500<br>16.7 | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 10)                                              | 60           | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 10)                                                   | 16           | $^\circ\text{C/W}$         |
| $P_D$           | Total Device Dissipation (Note 11) $T_A = 25^\circ\text{C}$<br>Derate above $25^\circ\text{C}$ | 3000<br>20   | mW<br>mW/ $^\circ\text{C}$ |
| $R_{\theta JA}$ | Thermal Resistance, Junction-to-Ambient (Note 11)                                              | 50           | $^\circ\text{C/W}$         |
| $R_{\psi JL}$   | Thermal Reference, Junction-to-Tab (Note 11)                                                   | 16           | $^\circ\text{C/W}$         |

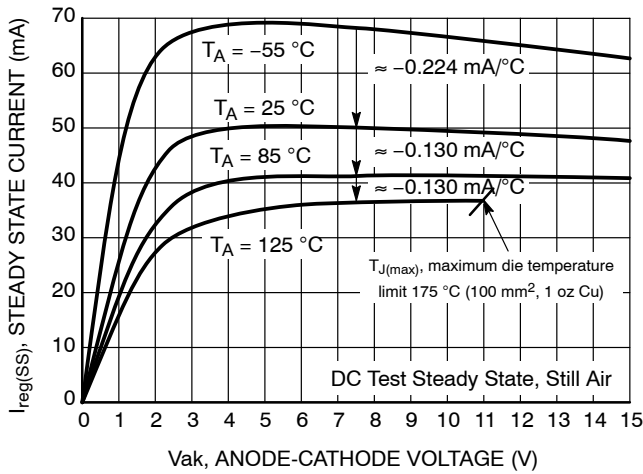
NOTE: Lead measurements are made by non-contact methods such as IR with treated surface to increase emissivity to 0.9.

Lead temperature measurement by attaching a T/C may yield values as high as 30% higher  $^\circ\text{C/W}$  values based upon empirical measurements and method of attachment.

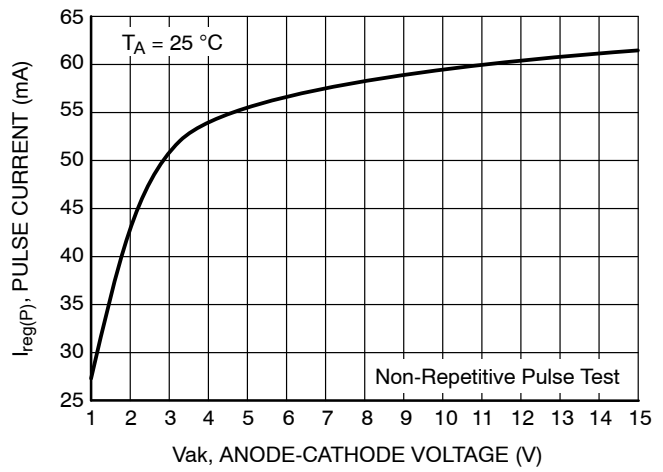
1. 100 mm<sup>2</sup>, 1 oz. Cu, still air.
2. 100 mm<sup>2</sup>, 2 oz. Cu, still air.
3. 300 mm<sup>2</sup>, 1 oz. Cu, still air.
4. 300 mm<sup>2</sup>, 2 oz. Cu, still air.
5. 500 mm<sup>2</sup>, 1 oz. Cu, still air.
6. 500 mm<sup>2</sup>, 2 oz. Cu, still air.
7. 700 mm<sup>2</sup>, 1 oz. Cu, still air.
8. 700 mm<sup>2</sup>, 2 oz. Cu, still air.
9. 1000 mm<sup>2</sup>, 3 oz. Cu, still air.
10. 400 mm<sup>2</sup>, PCB is DENKA K1, 1.5 mm Al, 2kV Thermally conductive dielectric, 2 oz. Cu, or equivalent, still air.
11. 900 mm<sup>2</sup>, PCB is DENKA K1, 1.5 mm Al, 2kV Thermally conductive dielectric, 2 oz. Cu, or equivalent, still air.



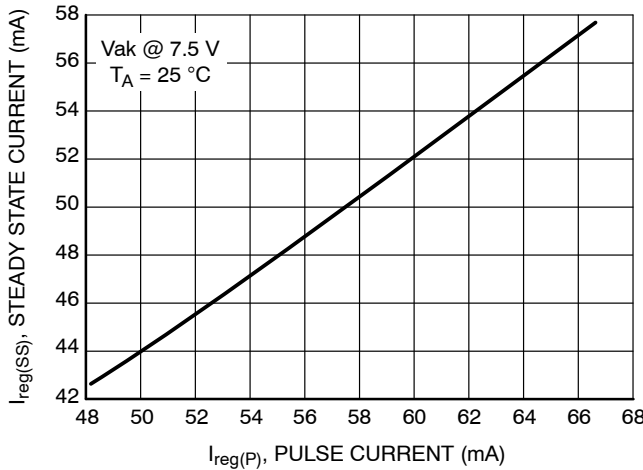
**TYPICAL PERFORMANCE CURVES**  
(MINIMUM FR-4 @ 100 MM<sup>2</sup>, 1 OZ. COPPER TRACE, STILL AIR)



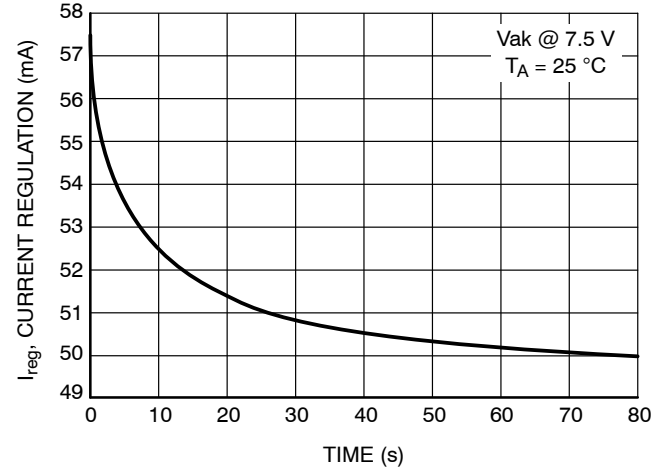
**Figure 2. Steady State Current ( $I_{reg(SS)}$ ) vs. Anode-Cathode Voltage ( $V_{AK}$ )**



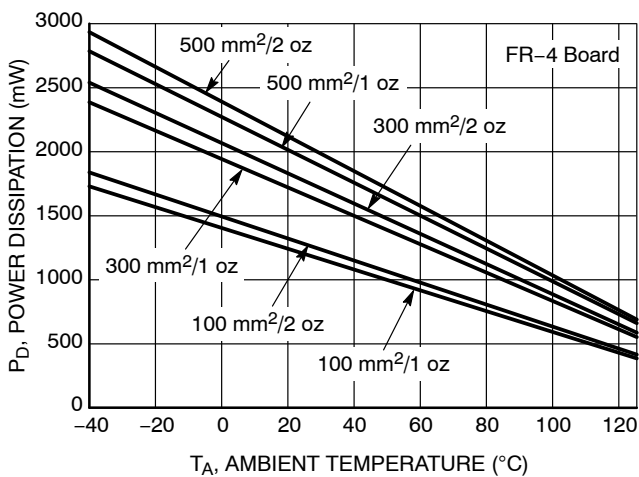
**Figure 3. Pulse Current ( $I_{reg(P)}$ ) vs. Anode-Cathode Voltage ( $V_{AK}$ )**



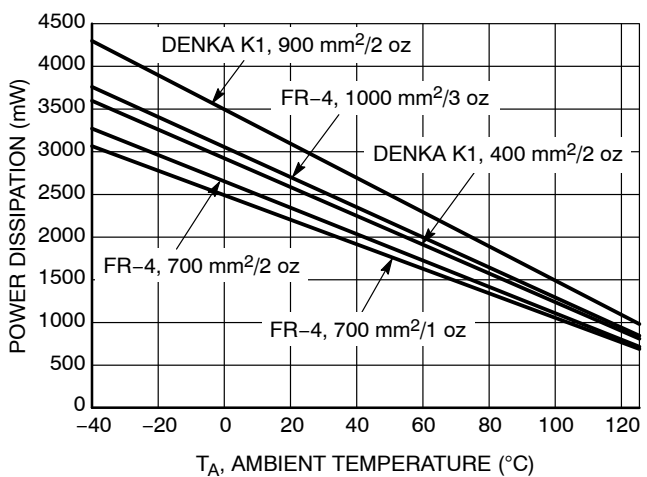
**Figure 4. Steady State Current vs. Pulse Current Testing**



**Figure 5. Current Regulation vs. Time**



**Figure 6. Power Dissipation vs. Ambient Temperature @  $T_J = 175^\circ\text{C}$ : Small Footprint**



**Figure 7. Power Dissipation vs. Ambient Temperature @  $T_J = 175^\circ\text{C}$ : Large Footprint**

## APPLICATIONS INFORMATION

The CCR is a self biased transistor designed to regulate the current through itself and any devices in series with it. The device has a slight negative temperature coefficient, as shown in Figure 2 – Tri Temp. (i.e. if the temperature increases the current will decrease). This negative temperature coefficient will protect the LEDs by reducing the current as temperature rises.

The CCR turns on immediately and is typically at 20% of regulation with only 0.5 V across it.

The device is capable of handling voltage for short durations of up to 120 V so long as the die temperature does

not exceed 175 °C. The determination will depend on the thermal pad it is mounted on, the ambient temperature, the pulse duration, pulse shape and repetition.

### AC Applications

The CCR is a DC device; however, it can be used with full wave rectified AC as shown in application notes [AND8433/D](#) and [AND8492/D](#) and design notes [DN05013/D](#) and [DN06065/D](#). Figure 8 shows the basic circuit configuration.

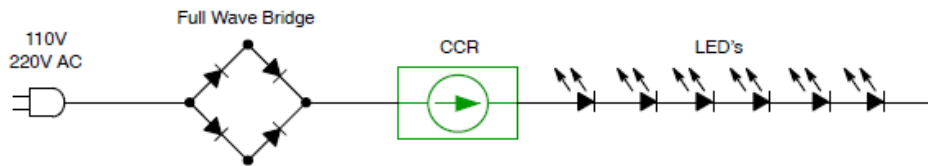


Figure 8. Basic AC Application

### Single LED String

The CCR can be placed in series with LEDs as a High Side or a Low Side Driver. The number of the LEDs can vary from one to an unlimited number. The designer needs to calculate the maximum voltage across the CCR by taking the maximum input voltage less the voltage across the LED string (Figures 9 and 10).

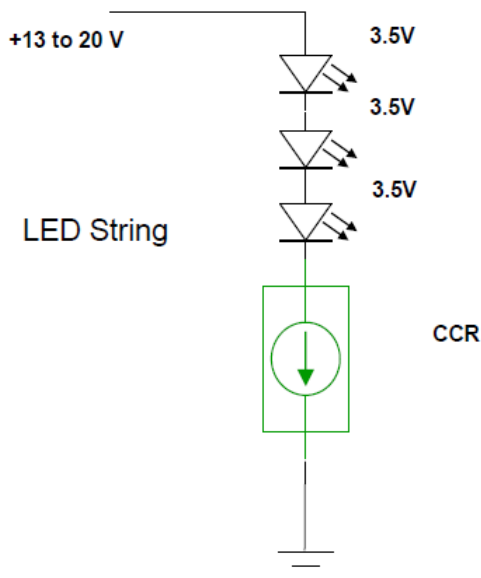


Figure 9.

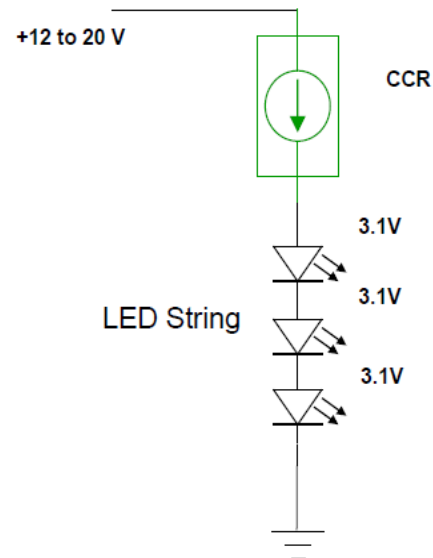


Figure 10.

### Higher Current LED Strings

Two or more fixed current CCRs can be connected in parallel. The current through them is additive (Figure 11).

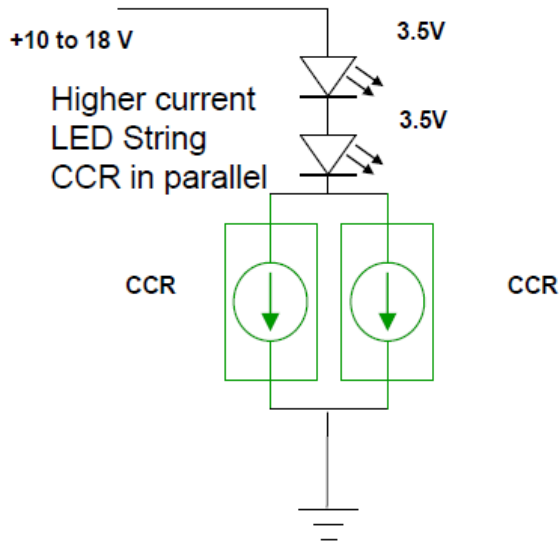


Figure 11.

### Other Currents

The adjustable CCR can be placed in parallel with any other CCR to obtain a desired current. The adjustable CCR provides the ability to adjust the current as LED efficiency increases to obtain the same light output (Figure 12).

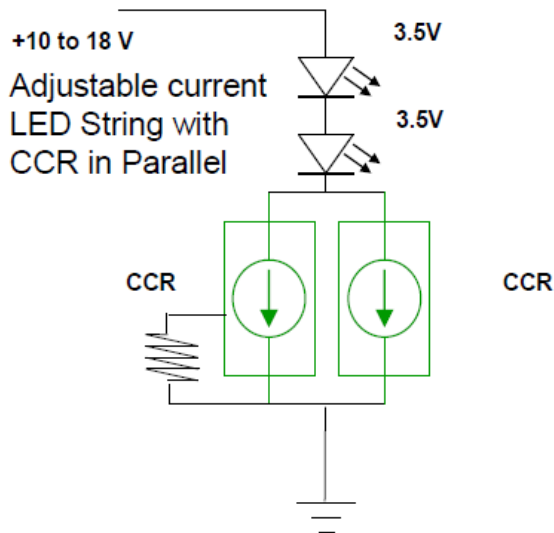


Figure 12.

### Dimming using PWM

The dimming of an LED string can be easily achieved by placing a BJT in series with the CCR (Figure 13).

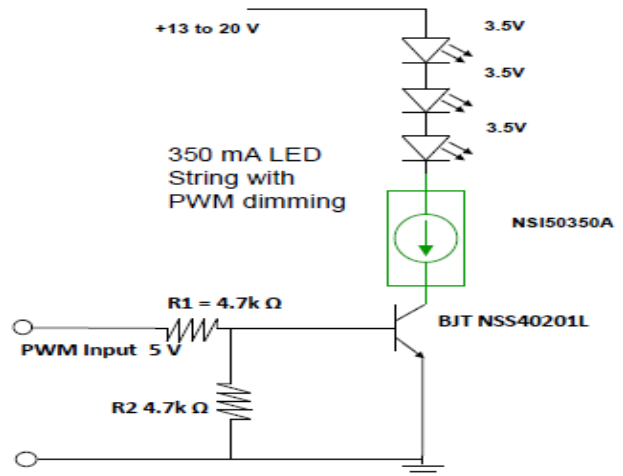


Figure 13.

The method of pulsing the current through the LEDs is known as Pulse Width Modulation (PWM) and has become the preferred method of changing the light level. LEDs being a silicon device, turn on and off rapidly in response to the current through them being turned on and off. The switching time is in the order of 100 nanoseconds, this equates to a maximum frequency of 10 Mhz, and applications will typically operate from a 100 Hz to 100 kHz. Below 100 Hz the human eye will detect a flicker from the light emitted from the LEDs. Between 500 Hz and 20 kHz the circuit may generate audible sound. Dimming is achieved by turning the LEDs on and off for a portion of a single cycle. This on/off cycle is called the Duty cycle (D) and is expressed by the amount of time the LEDs are on ( $T_{ON}$ ) divided by the total time of an on/off cycle ( $T_s$ ) (Figure 14).

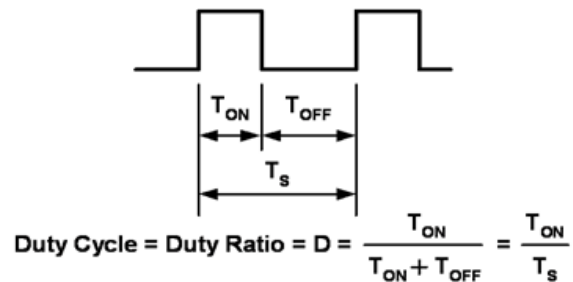


Figure 14.

The current through the LEDs is constant during the period they are turned on resulting in the light being consistent with no shift in chromaticity (color). The brightness is in proportion to the percentage of time that the LEDs are turned on.

Figure 15 is a typical response of Luminance vs Duty Cycle.

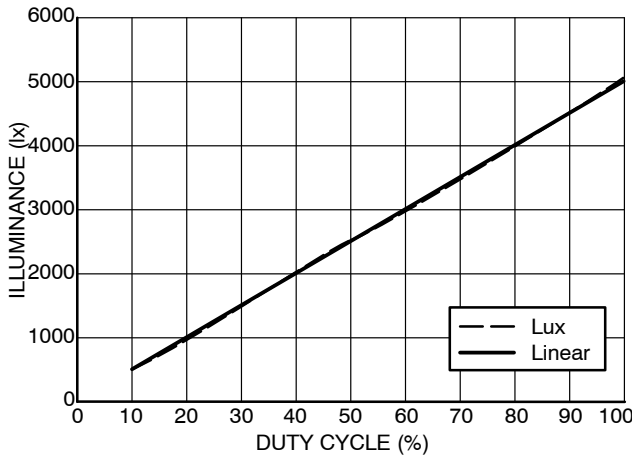


Figure 15. Luminous Emittance vs. Duty Cycle

### Reducing EMI

Designers creating circuits switching medium to high currents need to be concerned about Electromagnetic Interference (EMI). The LEDs and the CCR switch extremely fast, less than 100 nanoseconds. To help eliminate EMI, a capacitor can be added to the circuit across R2. (Figure 13) This will cause the slope on the rising and falling

edge on the current through the circuit to be extended. The slope of the CCR on/off current can be controlled by the values of R1 and C1.

The selected delay / slope will impact the frequency that is selected to operate the dimming circuit. The longer the delay, the lower the frequency will be. The delay time should not be less than a 10:1 ratio of the minimum on time. The frequency is also impacted by the resolution and dimming steps that are required. With a delay of 1.5 microseconds on the rise and the fall edges, the minimum on time would be 30 microseconds. If the design called for a resolution of 100 dimming steps, then a total duty cycle time (Ts) of 3 milliseconds or a frequency of 333 Hz will be required.

### Thermal Considerations

As power in the CCR increases, it might become necessary to provide some thermal relief. The maximum power dissipation supported by the device is dependent upon board design and layout. Mounting pad configuration on the PCB, the board material, and the ambient temperature affect the rate of junction temperature rise for the part. When the device has good thermal conductivity through the PCB, the junction temperature will be relatively low with high power applications. The maximum dissipation the device can handle is given by:

$$P_{D(MAX)} = \frac{T_{J(MAX)} - T_A}{R_{\theta JA}}$$

Referring to the thermal table on page 2 the appropriate  $R_{\theta JA}$  for the circuit board can be selected.

# NSIC2050JBT3G

## REVISION HISTORY

| Revision | Description of Changes                            | Date      |
|----------|---------------------------------------------------|-----------|
| 2        | Rebranded the Data Sheet to <b>onsemi</b> format. | 9/10/2025 |

This document has undergone updates prior to the inclusion of this revision history table. The changes tracked here only reflect updates made on the noted approval dates.





SCALE 1:1

Polarity Band

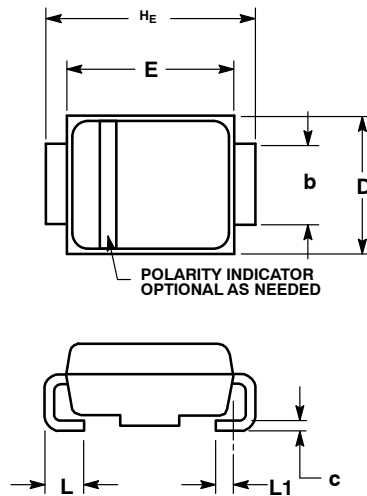


SCALE 1:1

Non-Polarity Band

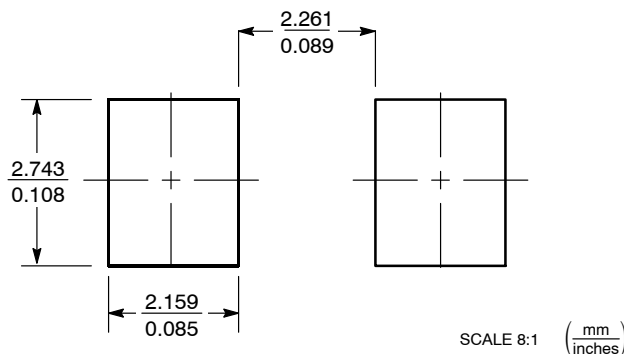
**SMB**  
**CASE 403A-03**  
**ISSUE J**

DATE 19 JUL 2012



POLARITY INDICATOR  
OPTIONAL AS NEEDED

**SOLDERING FOOTPRINT\***

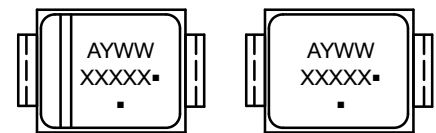


**NOTES:**

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION b SHALL BE MEASURED WITHIN DIMENSION L1.

| DIM | MILLIMETERS |      |      | INCHES    |       |       |
|-----|-------------|------|------|-----------|-------|-------|
|     | MIN         | NOM  | MAX  | MIN       | NOM   | MAX   |
| A   | 1.95        | 2.30 | 2.47 | 0.077     | 0.091 | 0.097 |
| A1  | 0.05        | 0.10 | 0.20 | 0.002     | 0.004 | 0.008 |
| b   | 1.96        | 2.03 | 2.20 | 0.077     | 0.080 | 0.087 |
| c   | 0.15        | 0.23 | 0.31 | 0.006     | 0.009 | 0.012 |
| D   | 3.30        | 3.56 | 3.95 | 0.130     | 0.140 | 0.156 |
| E   | 4.06        | 4.32 | 4.60 | 0.160     | 0.170 | 0.181 |
| HE  | 5.21        | 5.44 | 5.60 | 0.205     | 0.214 | 0.220 |
| L   | 0.76        | 1.02 | 1.60 | 0.030     | 0.040 | 0.063 |
| L1  | 0.51 REF    |      |      | 0.020 REF |       |       |

**GENERIC  
MARKING DIAGRAM\***



Polarity Band

Non-Polarity Band

XXXXX = Specific Device Code  
A = Assembly Location  
Y = Year  
WW = Work Week  
▪ = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

\*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

|                         |                    |                                                                                                                                                                                  |
|-------------------------|--------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| <b>DOCUMENT NUMBER:</b> | <b>98ASB42669B</b> | Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. |
| <b>DESCRIPTION:</b>     | <b>SMB</b>         | <b>PAGE 1 OF 1</b>                                                                                                                                                               |

**onsemi** and **onsemi** are trademarks of Semiconductor Components Industries, LLC dba **onsemi** or its subsidiaries in the United States and/or other countries. **onsemi** reserves the right to make changes without further notice to any products herein. **onsemi** makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. **onsemi** does not convey any license under its patent rights nor the rights of others.

**onsemi**, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## ADDITIONAL INFORMATION

### TECHNICAL PUBLICATIONS:

Technical Library: [www.onsemi.com/design/resources/technical-documentation](http://www.onsemi.com/design/resources/technical-documentation)  
onsemi Website: [www.onsemi.com](http://www.onsemi.com)

### ONLINE SUPPORT: [www.onsemi.com/support](http://www.onsemi.com/support)

For additional information, please contact your local Sales Representative at  
[www.onsemi.com/support/sales](http://www.onsemi.com/support/sales)