

N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^a	Q_g (Typ.)
100	0.0088 at $V_{GS} = 10$ V	20	18.3 nC
	0.012 at $V_{GS} = 4.5$ V	17	

FEATURES

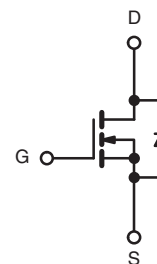
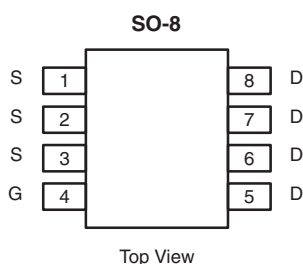
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE

APPLICATIONS

- DC/DC Primary Side Switch
- Telecom/Server
- Industrial



Ordering Information: Si4190DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	$T_C = 25$ °C	20	A
	$T_C = 70$ °C	16	
	$T_A = 25$ °C	13.4 ^{b, c}	
	$T_A = 70$ °C	10.6 ^{b, c}	
Pulsed Drain Current	I_{DM}	70	mJ
Continuous Source-Drain Diode Current	$T_C = 25$ °C	7.0	
	$T_A = 25$ °C	3.1 ^{b, c}	
Single Pulse Avalanche Current	I_{AS}	30	
Avalanche Energy	E_{AS}	45	W
Maximum Power Dissipation	$T_C = 25$ °C	7.8	
	$T_C = 70$ °C	5.0	
	$T_A = 25$ °C	3.5 ^{b, c}	
	$T_A = 70$ °C	2.2 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R_{thJA}	29	35	°C/W
Maximum Junction-to-Foot (Drain)	R_{thJF}	13	16	

Notes:

- Based on $T_C = 25$ °C.
- Surface mounted on 1" x 1" FR4 board.
- $t = 10$ s.
- Maximum under steady state conditions is 80 °C/W.

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	100			V
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		47		mV/°C
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			- 5.8		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.2		2.8	V
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V			1	μA
		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 55 °C			10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	30			A
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 15 A		0.0073	0.0088	Ω
		V _{GS} = 4.5 V, I _D = 10 A		0.0093	0.0120	
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 15 A		58		S
Dynamic ^b						
Input Capacitance	C _{iss}	V _{DS} = 50 V, V _{GS} = 0 V, f = 1 MHz		2000		pF
Output Capacitance	C _{oss}			1120		
Reverse Transfer Capacitance	C _{rss}			56		
Total Gate Charge	Q _g	V _{DS} = 50 V, V _{GS} = 10 V, I _D = 10 A		38.6	58	nC
Gate-Source Charge	Q _{gs}	V _{DS} = 50 V, V _{GS} = 4.5 V, I _D = 10 A		18.3	28	
Gate-Drain Charge	Q _{gd}			5.4		
Gate Resistance	R _g			7.3		
Turn-On Delay Time	t _{d(on)}	f = 1 MHz	0.6	2.7	5.4	Ω
Rise Time	t _r					
Turn-Off Delay Time	t _{d(off)}					
Fall Time	t _f					
Turn-On Delay Time	t _{d(on)}	V _{DD} = 50 V, R _L = 5 Ω I _D ≅ 10 A, V _{GEN} = 7.5 V, R _g = 1 Ω		12	24	ns
Rise Time	t _r			13	26	
Turn-Off Delay Time	t _{d(off)}			40	70	
Fall Time	t _f			11	22	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 50 V, R _L = 5 Ω I _D ≅ 10 A, V _{GEN} = 10 V, R _g = 1 Ω		10	20	
Rise Time	t _r			10	20	
Turn-Off Delay Time	t _{d(off)}			40	70	
Fall Time	t _f			11	22	
Drain-Source Body Diode Characteristics						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			7.0	A
Pulse Diode Forward Current ^a	I _{SM}				70	
Body Diode Voltage	V _{SD}	I _S = 5 A		0.75	1.1	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = 10 A, di/dt = 100 A/μs, T _J = 25 °C		51	100	ns
Body Diode Reverse Recovery Charge	Q _{rr}			51	100	nC
Reverse Recovery Fall Time	t _a			24		ns
Reverse Recovery Rise Time	t _b			27		

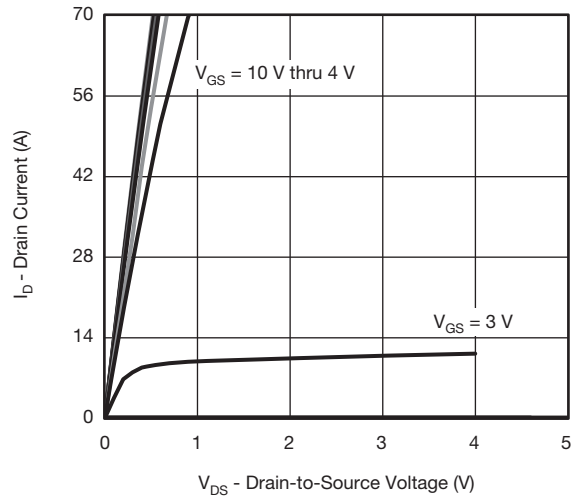
Notes:

a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$

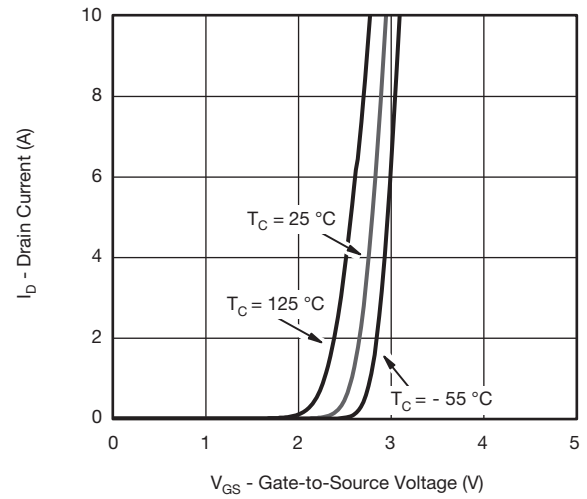
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

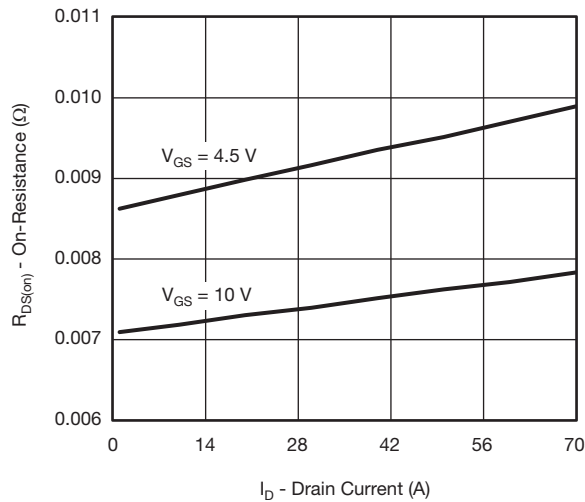
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



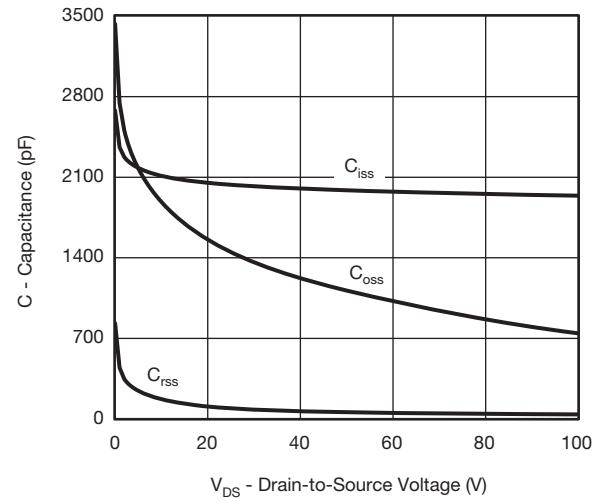
Output Characteristics



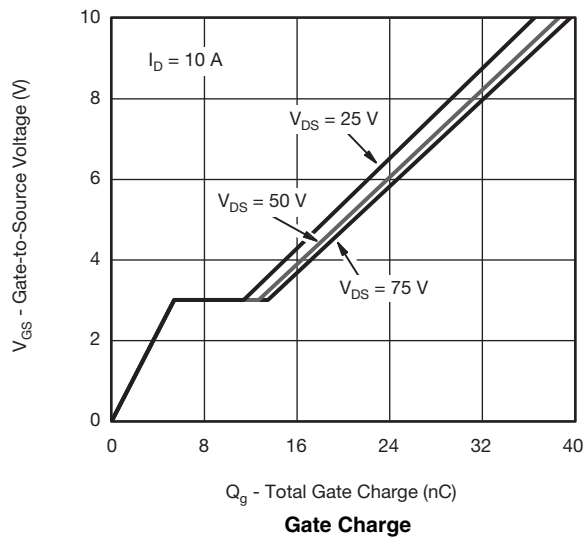
Transfer Characteristics



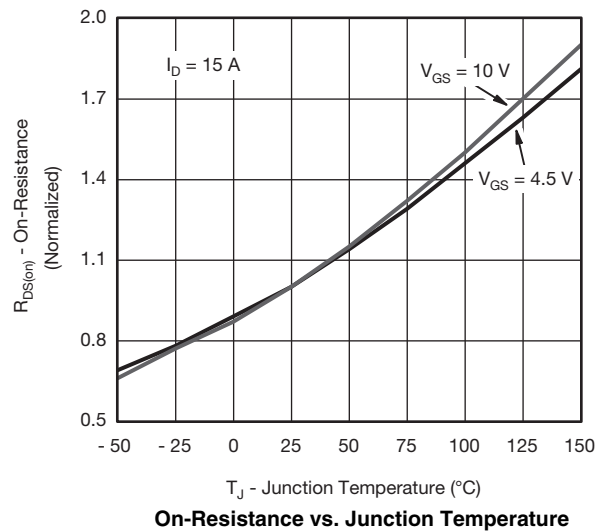
On-Resistance vs. Drain Current



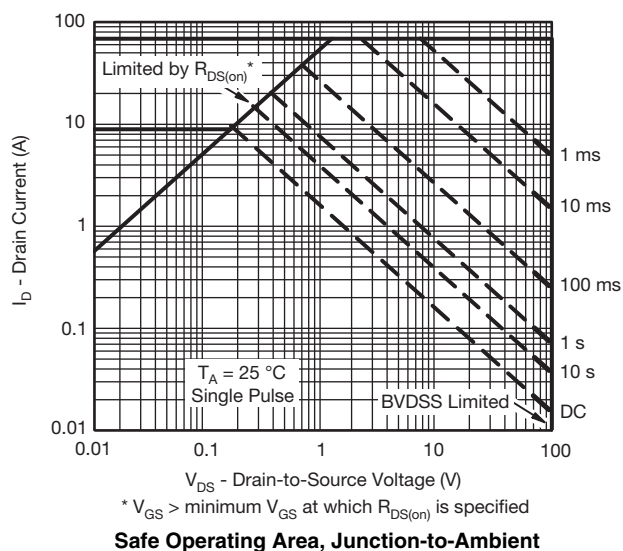
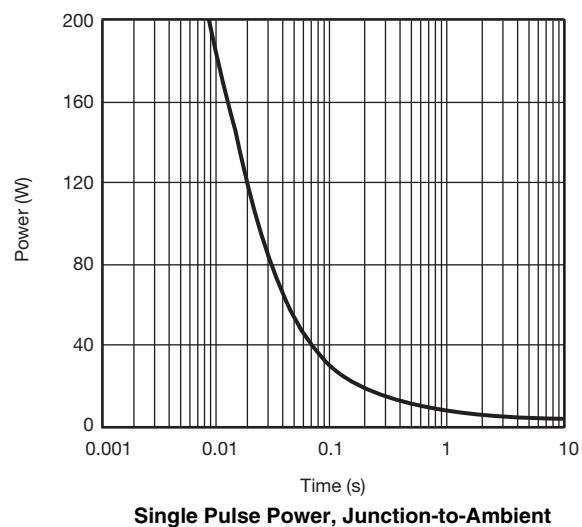
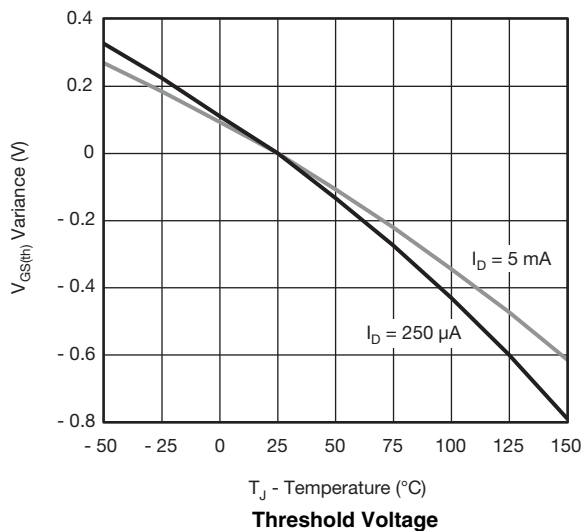
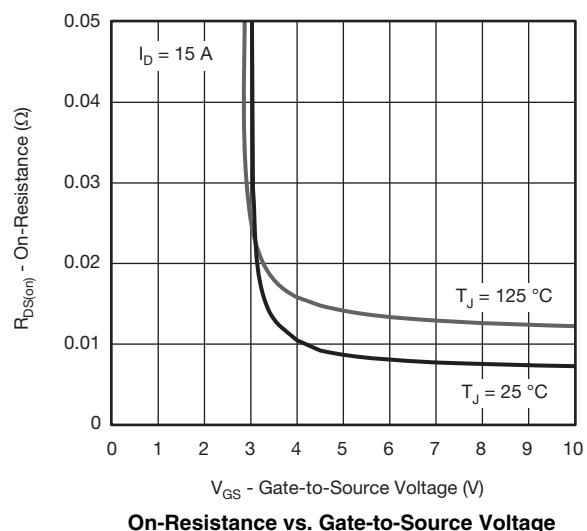
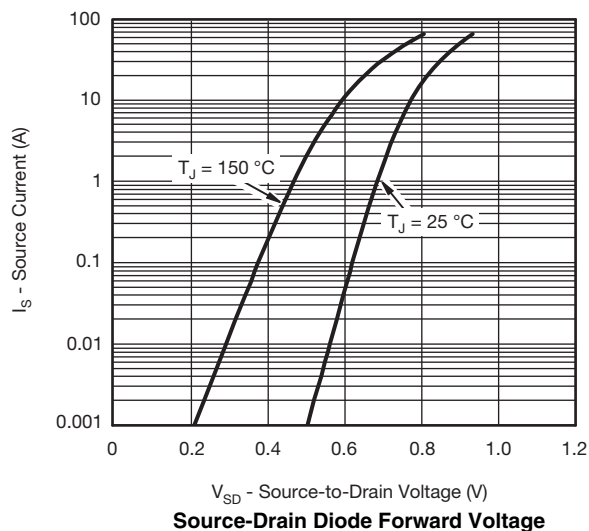
Capacitance



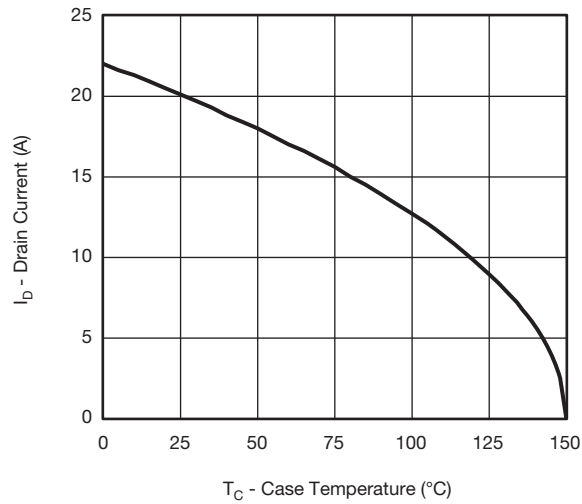
Gate Charge



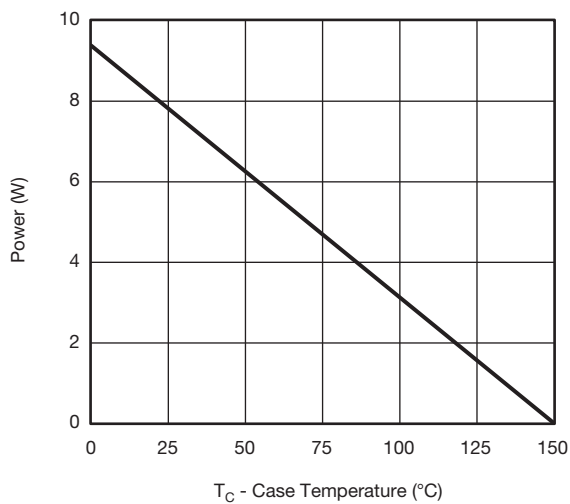
On-Resistance vs. Junction Temperature

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

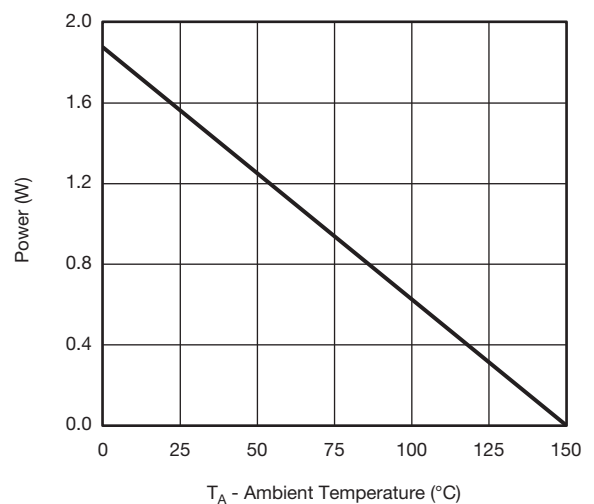
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Current Derating*

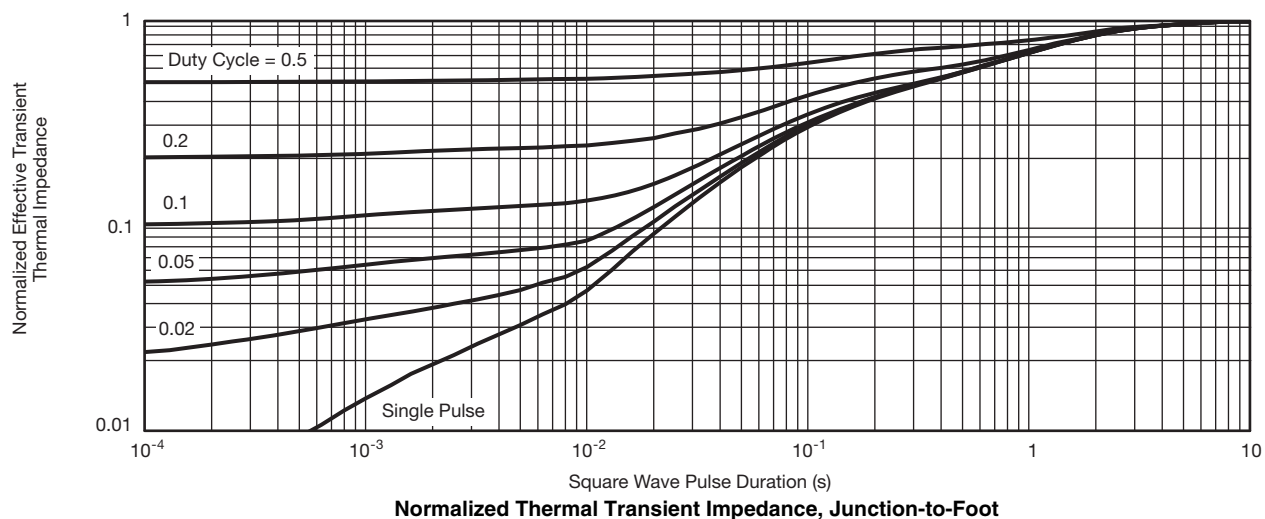
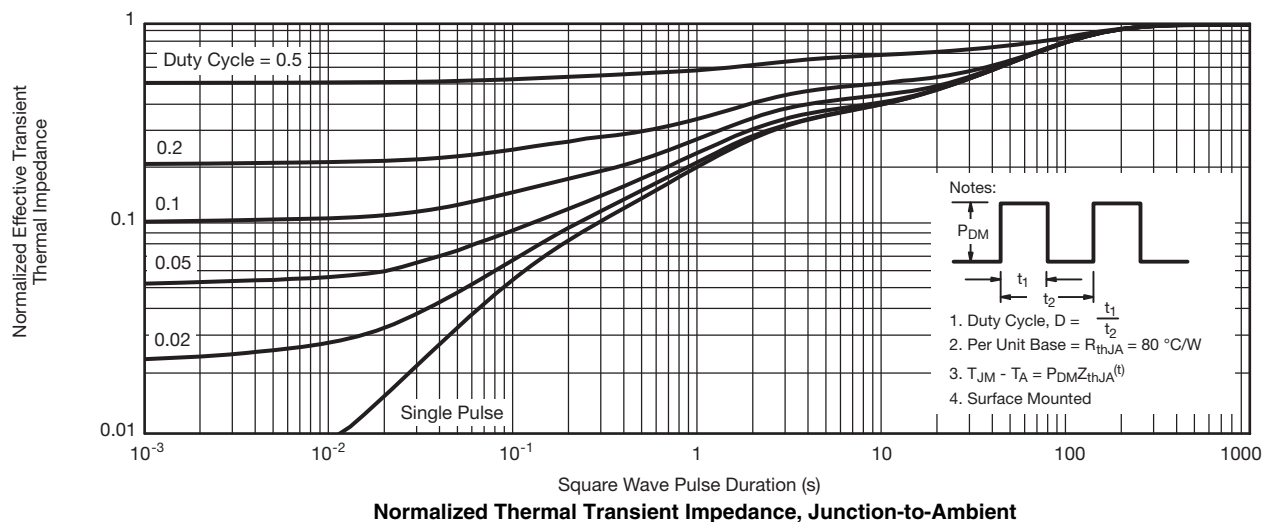


Power, Junction-to-Foot



Power, Junction-to-Ambient

* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

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