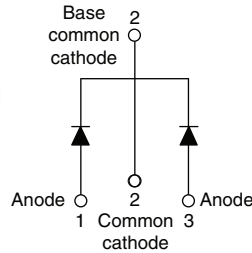


High Performance Schottky Rectifier, 2 x 15 A



FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912



RoHS
COMPLIANT
HALOGEN
FREE

PRIMARY CHARACTERISTICS

I _{F(AV)}	2 x 15 A
V _R	25 V, 30 V
V _F at I _F	0.40 V
I _{RM} typ.	97 mA at 125 °C
T _J max.	150 °C
E _{AS}	13 mJ
Package	TO-220AB 3L
Circuit configuration	Common cathode

DESCRIPTION

The VS-32CTQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS

SYMBOL	CHARACTERISTICS	VALUES	UNITS
I _{F(AV)}	Rectangular waveform	30	A
V _R		25/30	V
I _{FSM}	t _p = 5 µs sine	900	A
V _F	15 A _{pk} , T _J = 125 °C	0.40	V
T _J	Range	-55 to +150	°C

VOLTAGE RATINGS

PARAMETER	SYMBOL	VS-32CTQ025-M3	VS-32CTQ030-M3	UNITS
Maximum DC reverse voltage	V _R	25	30	V
Maximum working peak reverse voltage	V _{RWM}			

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum average forward current, see fig. 5	I _{F(AV)}	50 % duty cycle at T _C = 115 °C, rectangular waveform	30	A
Maximum peak one cycle non-repetitive surge current, see fig. 7	I _{FSM}	5 µs sine or 3 µs rect. pulse	900	
		10 ms sine or 6 ms rect. pulse	250	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1.20 A, L = 11.10 mH	13	mJ
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 µs Frequency limited by T _J maximum V _A = 1.5 x V _R typical	3	A

**ELECTRICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop See fig. 1	$V_{FM}^{(1)}$	15 A	$T_J = 25\text{ }^{\circ}\text{C}$	0.49	V
		30 A		0.58	
		15 A	$T_J = 125\text{ }^{\circ}\text{C}$	0.40	
		30 A		0.53	
Maximum reverse leakage current	$I_{RM}^{(1)}$	$T_J = 25\text{ }^{\circ}\text{C}$	$V_R = \text{Rated } V_R$	1.75	mA
		$T_J = 125\text{ }^{\circ}\text{C}$		145	
Typical reverse leakage current	$I_{RM}^{(1)}$	$T_J = 125\text{ }^{\circ}\text{C}$	$V_R = \text{Rated } V_R$	97	mA
Threshold voltage	$V_{F(TO)}$	$T_J = T_J \text{ maximum}$		0.233	V
Forward slope resistance	r_t			9.09	m Ω
Maximum junction capacitance per leg	C_T	$V_R = 5\text{ }V_{DC}$ (test signal range 100 kHz to 1 MHz) $25\text{ }^{\circ}\text{C}$		1300	pF
Typical series inductance per leg	L_S	Measured lead to lead 5 mm from package body		8.0	nH
Maximum voltage rate of change	dV/dt	Rated V_R		10 000	V/ μ s

Note(1) Pulse width < 300 μ s, duty cycle < 2 %**THERMAL - MECHANICAL SPECIFICATIONS**

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T_J, T_{Stg}		-55 to 150	$^{\circ}\text{C}$
Maximum thermal resistance, junction to case per leg	R_{thJC}	DC operation See fig. 4	3.25	$^{\circ}\text{C/W}$
Typical thermal resistance, case to heatsink	R_{thCS}	Mounting surface, smooth and greased	0.50	
Approximate weight			2	g
			0.07	oz.
Mounting torque	minimum		6 (5)	kgf · cm
	maximum		12 (10)	(lbf · in)
Marking device		Case style TO-220AB 3L	32CTQ025	
			32CTQ030	

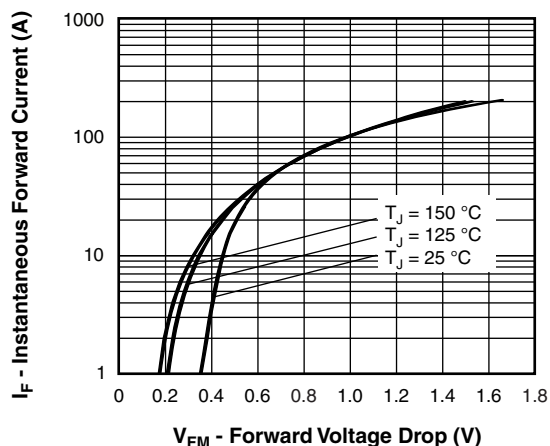


Fig. 1 - Maximum Forward Voltage Drop Characteristics

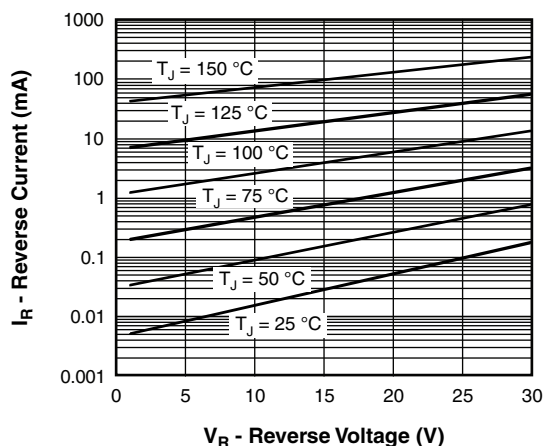


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

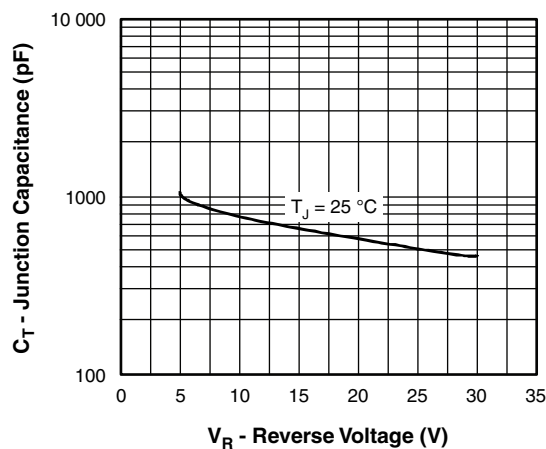
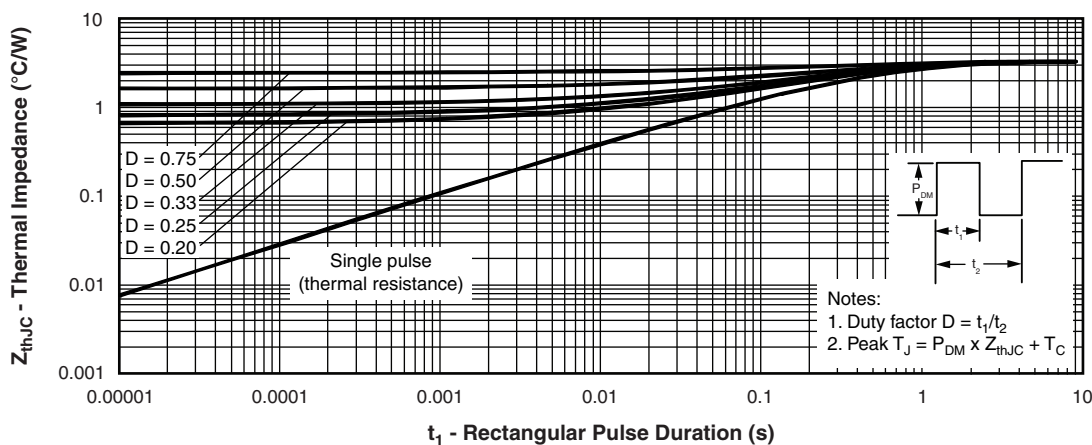


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

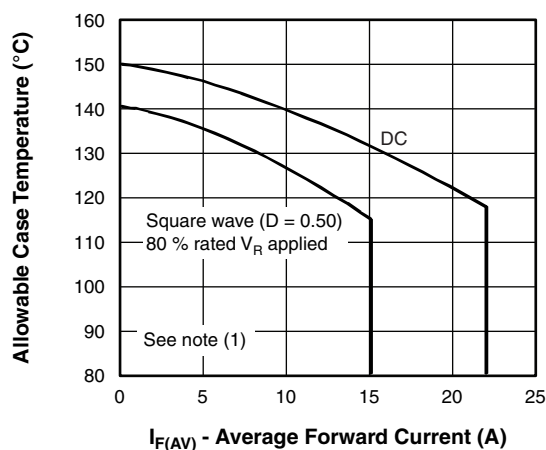


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

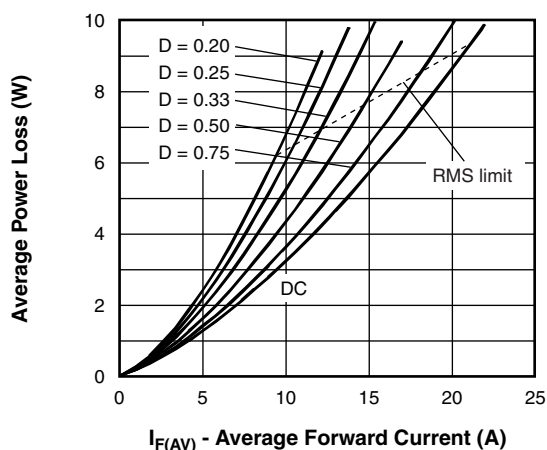


Fig. 6 - Forward Power Loss Characteristics

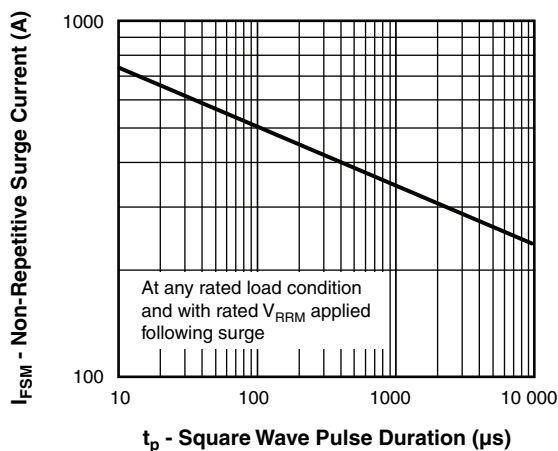


Fig. 7 - Maximum Non-Repetitive Surge Current

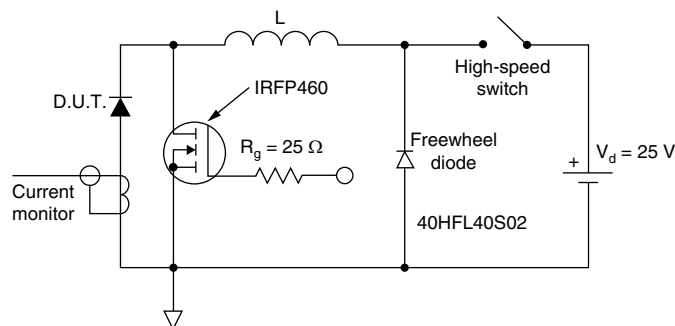


Fig. 8 - Unclamped Inductive Test Circuit

Note

- (1) Formula used: $T_C = T_J - (P_d + P_{dREV}) \times R_{thJC}$;
 P_d = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6);
 P_{dREV} = inverse power loss = $V_{R1} \times I_R (1 - D)$; I_R at $V_{R1} = 80\%$ rated V_R



ORDERING INFORMATION TABLE

Device code	VS-	32	C	T	Q	030	-M3
	1	2	3	4	5	6	7

- 1** - Vishay Semiconductors product
- 2** - Current rating (30 A)
- 3** - Circuit configuration:
C = common cathode
- 4** - Package:
T = TO-220
- 5** - Schottky "Q" series
- 6** - Voltage ratings

025 = 25 V
030 = 30 V
- 7** - Environmental digit
-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)		
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION
VS-32CTQ025-M3	50	Antistatic plastic tubes
VS-32CTQ030-M3	50	Antistatic plastic tubes

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?96154
Part marking information	www.vishay.com/doc?95028



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