

N-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$R_{DS(on)}$ (Ω)	I_D (A) ^a	Q_g (Typ.)
30	0.0094 at $V_{GS} = 10$ V	16	11 nC
	0.0135 at $V_{GS} = 4.5$ V	13	

FEATURES

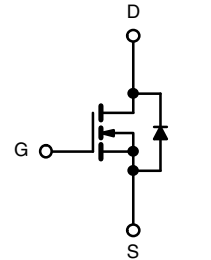
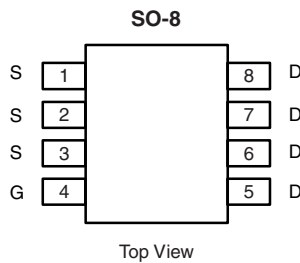
- Halogen-free According to IEC 61249-2-21 Definition
- Extremely Low Q_{gd} for Low Switching Losses
- TrenchFET[®] Power MOSFET
- 100 % R_g Tested
- Compliant to RoHS Directive 2002/95/EC



RoHS
COMPLIANT
HALOGEN
FREE
Available

APPLICATIONS

- High-Side DC/DC Conversion
 - Notebook
 - Server



N-Channel MOSFET

Ordering Information: Si4682DY-T1-E3 (Lead (Pb)-free)
Si4682DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS ($T_A = 25$ °C, unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 150$ °C)	$T_C = 25$ °C	16	A
	$T_C = 70$ °C	12.9	
	$T_A = 25$ °C	12 ^{b, c}	
	$T_A = 70$ °C	9.5 ^{b, c}	
Pulsed Drain Current	I_{DM}	50	A
Continuous Source-Drain Diode Current	$T_C = 25$ °C	4.0	
	$T_A = 25$ °C	2.3 ^{b, c}	
Single Pulse Avalanche Current	I_{AS}	20	
Avalanche Energy	E_{AS}	20	mJ
Maximum Power Dissipation	$T_C = 25$ °C	4.45	W
	$T_C = 70$ °C	2.85	
	$T_A = 25$ °C	2.50 ^{b, c}	
	$T_A = 70$ °C	1.6 ^{b, c}	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^{b, d}	R_{thJA}	36	50	°C/W
Maximum Junction-to-Foot (Drain)	R_{thJF}	22	28	

Notes:

a. Based on $T_C = 25$ °C.

b. Surface mounted on 1" x 1" FR4 board.

c. $t = 10$ s.

d. Maximum under steady state conditions is 90 °C/W.

MOSFET SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA	30			V	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	I _D = 250 μA		35		mV/°C	
V _{GS(th)} Temperature Coefficient	ΔV _{GS(th)} /T _J			6.5			
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.4		2.5	V	
		V _{DS} = V _{GS} , I _D = 5 mA		2.2			
Gate-Source Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			1	μA	
		V _{DS} = 30 V, V _{GS} = 0 V, T _J = 55 °C			10		
On-State Drain Current ^a	I _{D(on)}	V _{DS} ≥ 5 V, V _{GS} = 10 V	30			A	
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 10 V, I _D = 16 A		0.0078	0.0094	Ω	
		V _{GS} = 4.5 V, I _D = 9.5 A		0.0113	0.0135		
Forward Transconductance ^a	g _{fs}	V _{DS} = 15 V, I _D = 16 A		35		S	
Dynamic ^b							
Input Capacitance	C _{iss}	V _{DS} = 15 V, V _{GS} = 0 V, f = 1 MHz		1595		pF	
Output Capacitance	C _{oss}			375			
Reverse Transfer Capacitance	C _{rss}			150			
Total Gate Charge	Q _g	V _{DS} = 15 V, V _{GS} = 10 V, I _D = 11 A		24	38	nC	
		V _{DS} = 15 V, V _{GS} = 4.5 V, I _D = 11 A		11	17		
Gate-Source Charge			Q _{gs}		4		
Gate-Drain Charge			Q _{gd}		3.1		
Gate Resistance	R _g	f = 1 MHz	0.2	0.55	0.9	Ω	
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 1.87 Ω I _D ≡ 8 A, V _{GEN} = 4.5 V, R _g = 1 Ω		18	30	ns	
Rise Time	t _r			82	130		
Turn-Off Delay Time	t _{d(off)}			18	30		
Fall Time	t _f			10	16		
Turn-On Delay Time	t _{d(on)}	V _{DD} = 15 V, R _L = 1.87 Ω I _D ≡ 8 A, V _{GEN} = 10 V, R _g = 1 Ω		11	18		
Rise Time	t _r			55	85		
Turn-Off Delay Time	t _{d(off)}			23	35		
Fall Time	t _f			8	15		
Drain-Source Body Diode Characteristics							
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			4	A	
Pulse Diode Forward Current ^a	I _{SM}				50		
Body Diode Voltage	V _{SD}	I _S = 2.3 A		0.76	1.1	V	
Body Diode Reverse Recovery Time	t _{rr}	I _F = 9.5 A, dI/dt = 100 A/μs, T _J = 25 °C		30	45	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			24	40	nC	
Reverse Recovery Fall Time	t _a			15.5		ns	
Reverse Recovery Rise Time	t _b			14.5			

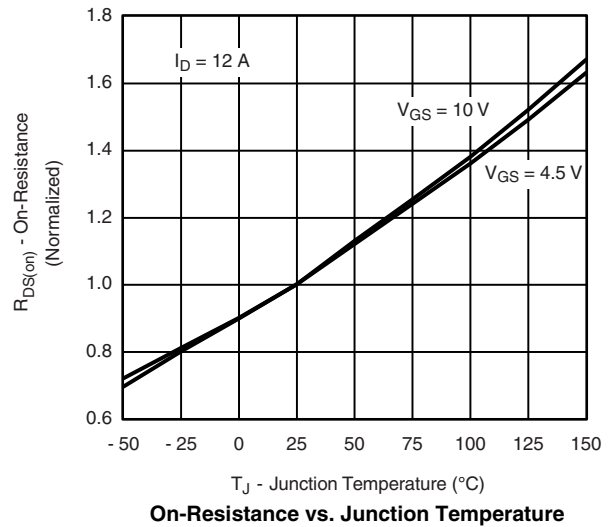
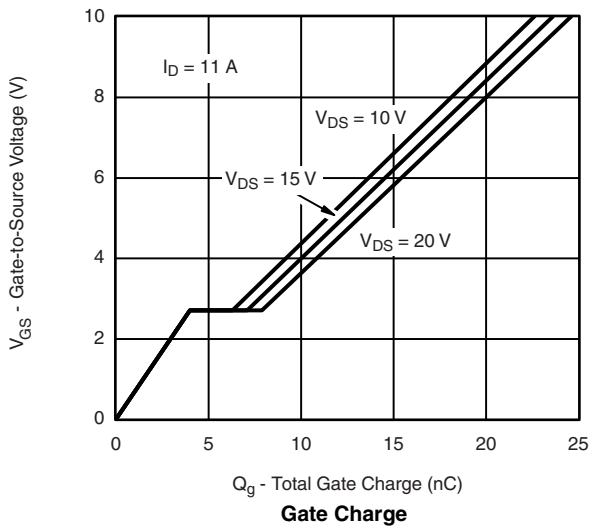
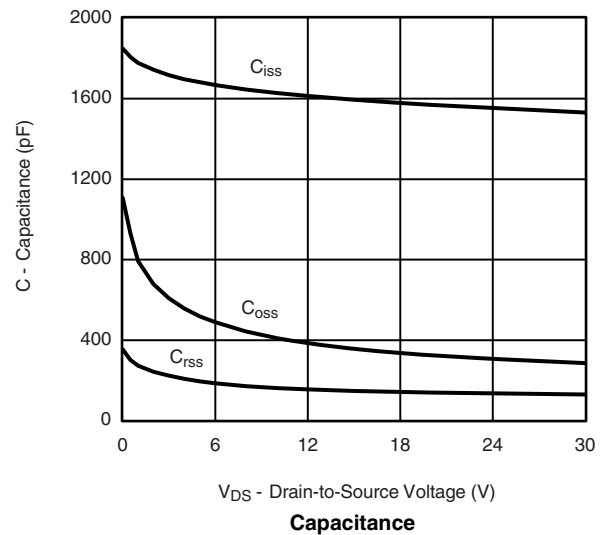
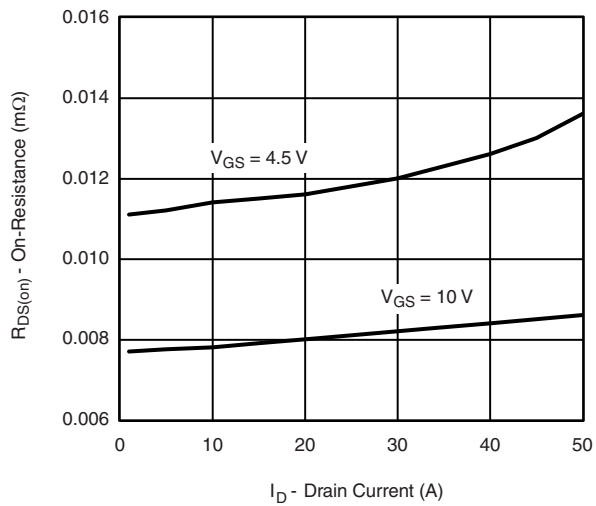
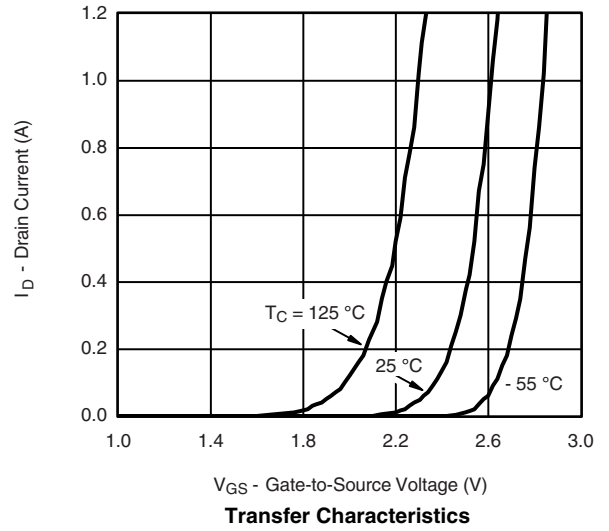
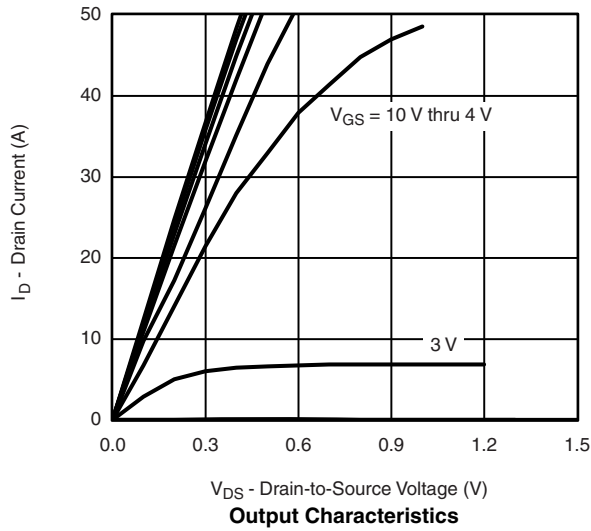
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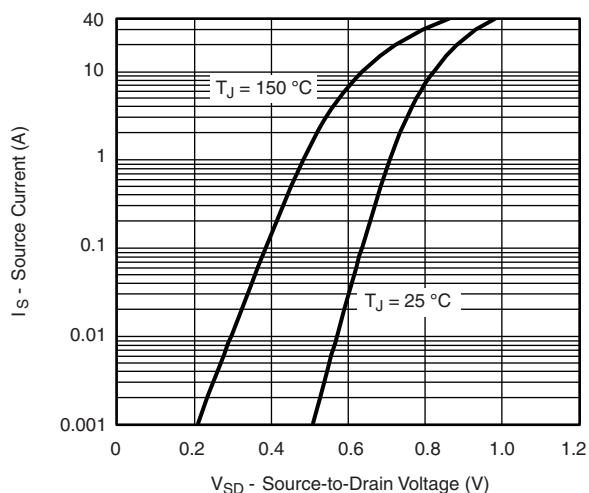
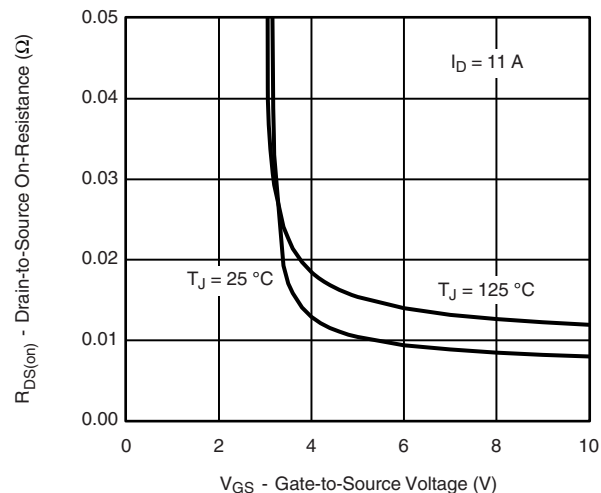
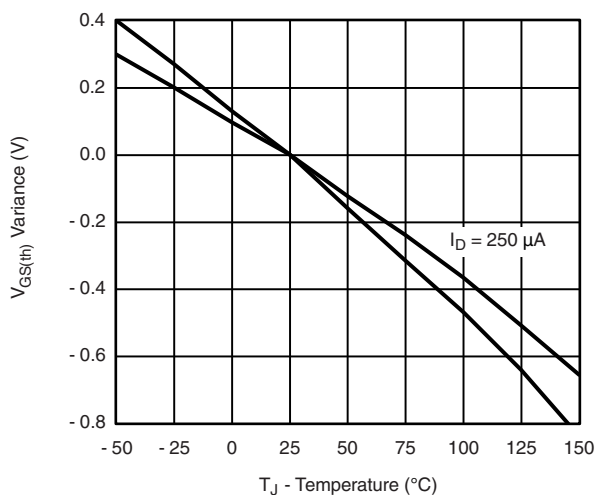
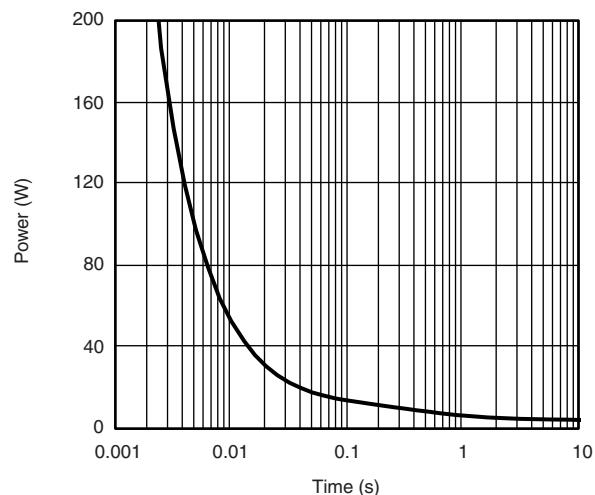
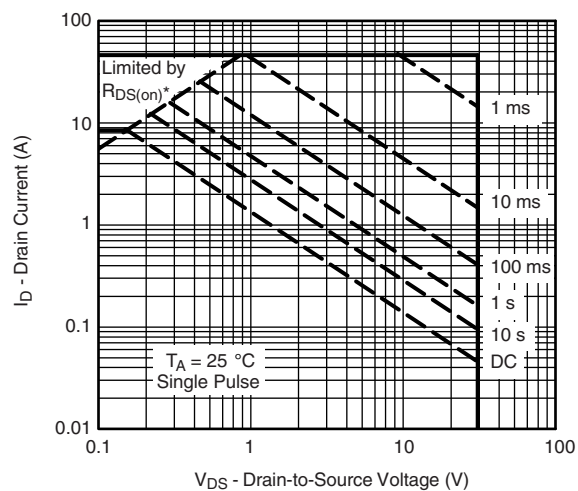
a. Pulse test; pulse width $\leq 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$.

b. Guaranteed by design, not subject to production testing.

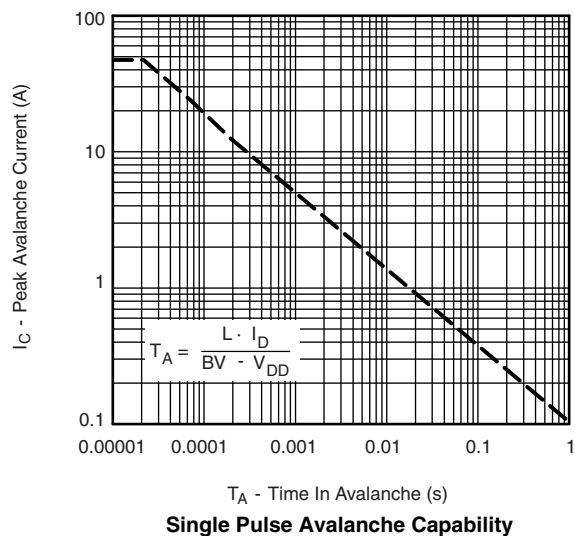
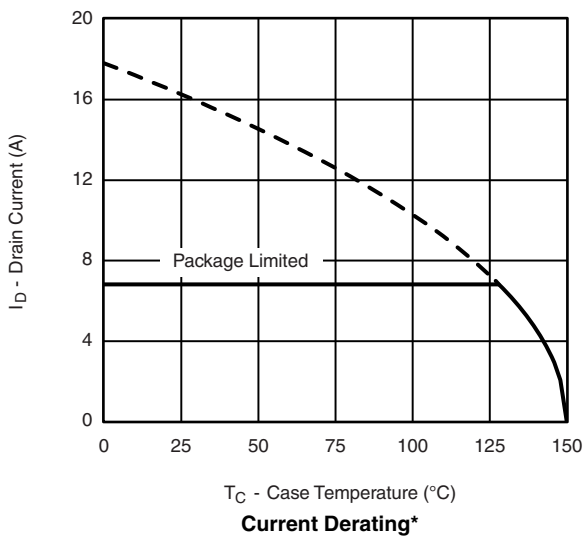
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

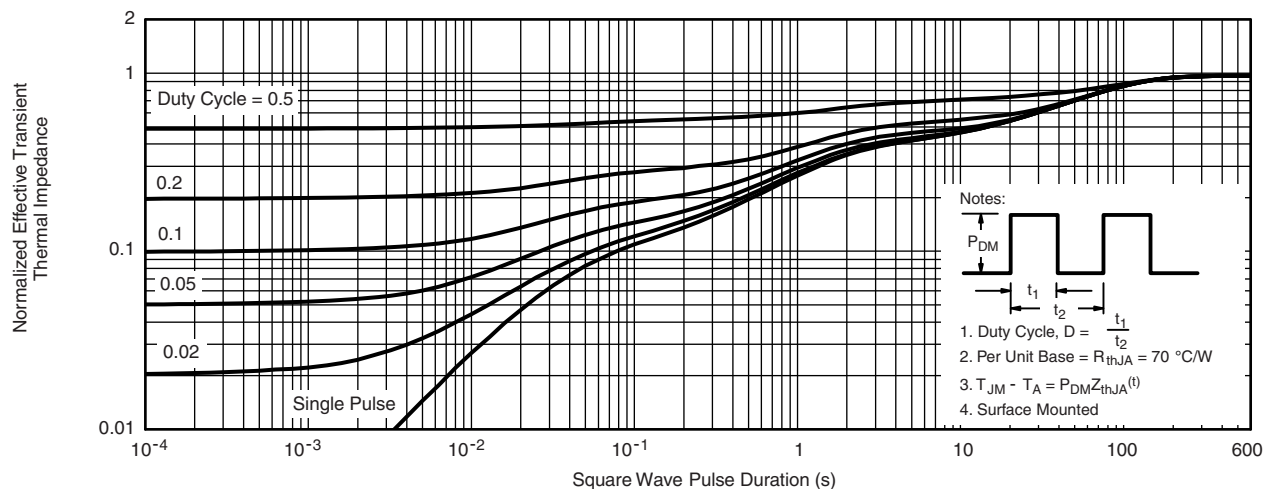
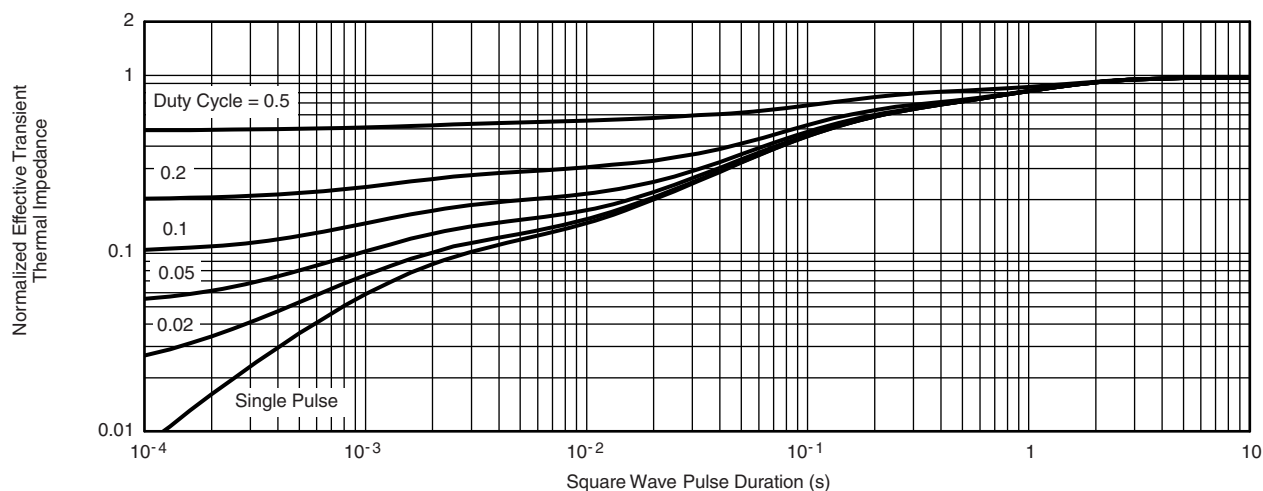


TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**Source-Drain Diode Forward Voltage****On-Resistance vs. Gate-to-Source Voltage****Threshold Voltage****Single Pulse Power, Junction-to-Ambient*** $V_{GS} >$ minimum V_{GS} at which $R_{DS(on)}$ is specified**Safe Operating Area, Junction-to-Ambient**

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)**Normalized Thermal Transient Impedance, Junction-to-Ambient****Normalized Thermal Transient Impedance, Junction-to-Case**

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