

30V COMPLEMENTARY ENHANCEMENT MODE MOSFET

Product Summary

Device	V _{(BR)DSS}	R _{DS(ON)} max	Package	I _D MAX T _A = +25 °C
N-Channel	30V	20mΩ @ V _{GS} = 10V	SO-8	8.5A
		32mΩ @ V _{GS} = 4.5V		7.0A
P-Channel	-30V	45mΩ @ V _{GS} = -10V		-5.5A
		85mΩ @ V _{GS} = -4.5V		-4.1A

Description

This MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

Applications

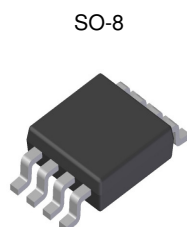
- DC Motor Control
- DC-AC Inverters

Features

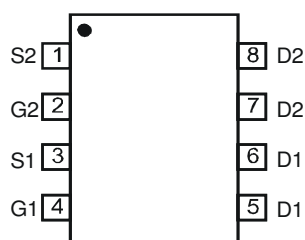
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

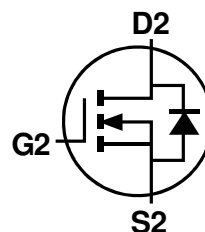
- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound.
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — Matte Tin Annealed Over Copper Leadframe.
Solderable per MIL-STD-202, Method 208 (e3)
- Weight: 0.008 grams (Approximate)



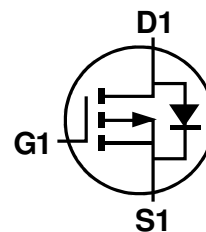
Top View



Pin Configuration



Q2 N-CHANNEL MOSFET



Q1 P-CHANNEL MOSFET

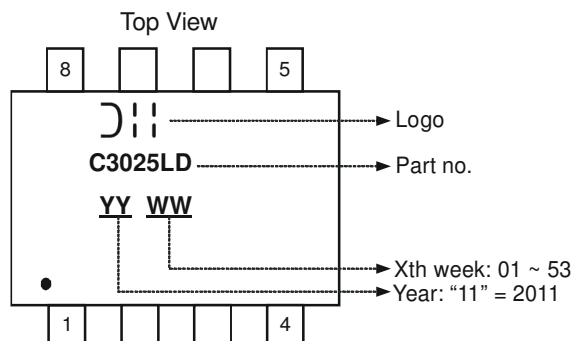
Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMC3025LSD-13	SO-8	2,500/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>

Marking Information



Maximum Ratings N-CHANNEL– Q2 (@T_A = +25 °C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	I _D	6.5 5.1	A
	t<10s	T _A = +25°C T _A = +70°C	I _D	8.5 6.8	A
Continuous Drain Current (Note 5) V _{GS} = 4.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	5.3 4.1	A
	t<10s	T _A = +25°C T _A = +70°C	I _D	7.0 5.5	A
Maximum Continuous Body Diode Forward Current (Note 5)			I _S	2	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	60	A
Pulsed Body Diode Current (10µs pulse, duty cycle = 1%)			I _{SM}	60	A
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	14	A
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	10	mJ

Maximum Ratings P-CHANNEL– Q1 (@T_A = +25 °C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V _{DSS}	-30	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) V _{GS} = -10V	Steady State	T _A = +25°C T _A = +70°C	I _D	-4.2 -3.2	A
	t<10s	T _A = +25°C T _A = +70°C	I _D	-5.5 -4.3	A
Continuous Drain Current (Note 5) V _{GS} = -4.5V	Steady State	T _A = +25°C T _A = +70°C	I _D	-3.5 -2.3	A
	t<10s	T _A = +25°C T _A = +70°C	I _D	-4.1 -3.2	A
Maximum Continuous Body Diode Forward Current (Note 5)			I _S	-2	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	-30	A
Pulsed Body Diode Current (10µs pulse, duty cycle = 1%)			I _{SM}	-30	A
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	-14	A
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	10	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 6)	T _A = +25 °C	P _D	1.2	W
	T _A = +70 °C		0.77	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{θJA}	104	°C/W
	t<10s		62	
Total Power Dissipation (Note 5)	T _A = +25 °C	P _D	1.5	W
	T _A = +70 °C		0.95	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _{θJA}	83	°C/W
	t<10s		49	
Thermal Resistance, Junction to Case (Note 5)		R _{θJC}	15	
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Electrical Characteristics N-CHANNEL– Q2 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	—	—	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±1	μA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	1.0	—	2.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	15	20	mΩ	V _{GS} = 10V, I _D = 7.4A
		—	23	32		V _{GS} = 4.5V, I _D = 6A
Forward Transfer Admittance	Y _{fs}	—	8	—	S	V _{DS} = 5V, I _D = 10A
Diode Forward Voltage	V _{SD}	—	0.70	1.2	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	501	—	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	72	—		
Reverse Transfer Capacitance	C _{rss}	—	57	—		
Gate Resistance	R _g	—	1.84	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	4.6	—	nC	V _{DS} = 15V, I _D = 10A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	9.8	—		
Gate-Source Charge	Q _{gs}	—	1.6	—		
Gate-Drain Charge	Q _{gd}	—	2.0	—		
Turn-On Delay Time	t _{D(on)}	—	3.9	—	ns	V _{DD} = 15V, V _{GS} = 10V, R _G = 6Ω, I _D = 1A
Turn-On Rise Time	t _r	—	4.2	—		
Turn-Off Delay Time	t _{D(off)}	—	16.6	—		
Turn-Off Fall Time	t _f	—	5.8	—	ns	I _F = 12A, di/dt = 500A/μs
Reverse Recovery Time	t _{rr}	—	5.5	—		
Reverse Recovery Charge	Q _{rr}	—	2.6	—	nC	

Electrical Characteristics P-CHANNEL – Q1 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	-30	—	—	V	V _{GS} = 0V, I _D = -250μA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	-1	μA	V _{DS} = -30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(th)}	-1.0	—	-2.0	V	V _{DS} = V _{GS} , I _D = -250μA
Static Drain-Source On-Resistance	R _{DS(on)}	—	38	45	mΩ	V _{GS} = -10V, I _D = -5.2A
		—	65	85		V _{GS} = -4.5V, I _D = -4A
Forward Transfer Admittance	Y _{fs}	—	5	—	S	V _{DS} = -5V, I _D = -5.2A
Diode Forward Voltage	V _{SD}	—	-0.7	-1.2	V	V _{GS} = 0V, I _S = -1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	—	590	—	pF	V _{DS} = -25V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	—	69	—	pF	
Reverse Transfer Capacitance	C _{rss}	—	53	—	pF	
Gate Resistance	R _g	—	11	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1.0MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	—	5.1	—	nC	V _{DS} = -15V, I _D = -6A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	10.5	—	nC	
Gate-Source Charge	Q _{gs}	—	1.8	—	nC	
Gate-Drain Charge	Q _{gd}	—	1.9	—	nC	
Turn-On Delay Time	t _{D(on)}	—	6.8	—	ns	V _{DD} = -15V, V _{GS} = -10V, R _G = 6Ω, I _D = -1A
Turn-On Rise Time	t _r	—	4.9	—	ns	
Turn-Off Delay Time	t _{D(off)}	—	28.4	—	ns	
Turn-Off Fall Time	t _f	—	12.4	—	ns	I _F = 12A, di/dt = 500A/μs
Reverse Recovery Time	t _{rr}	—	14	—	ns	
Reverse Recovery Charge	Q _{rr}	—	11	—	nC	

- Notes: 7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

N-CHANNEL

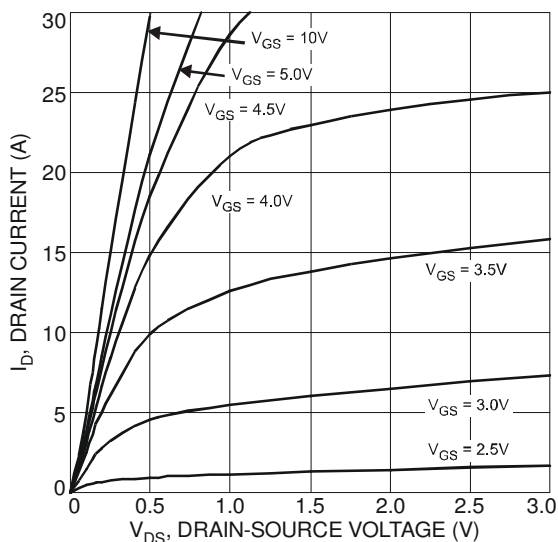


Figure 1. Typical Output Characteristic

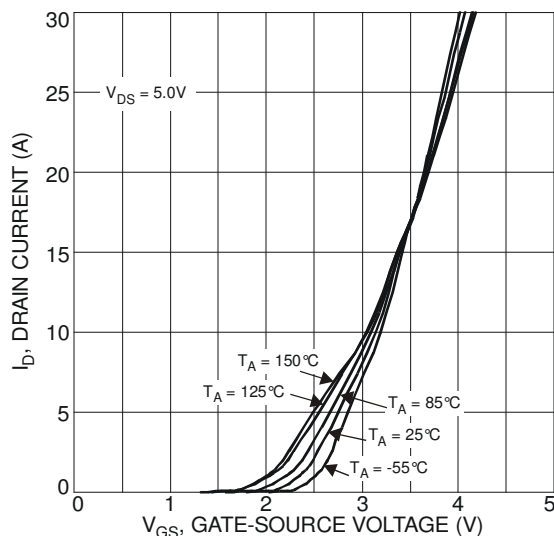


Figure 2. Typical Transfer Characteristics

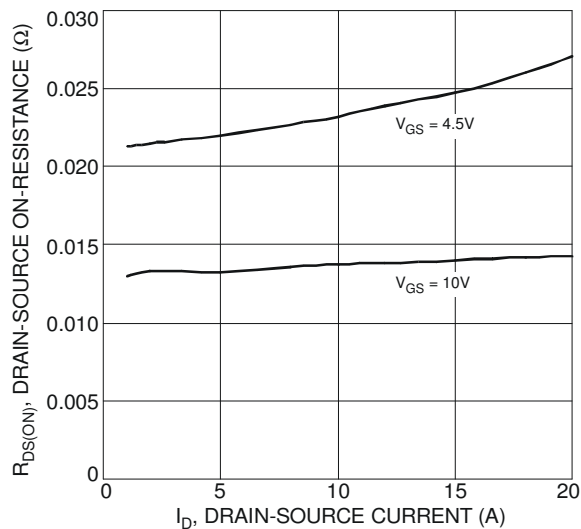


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

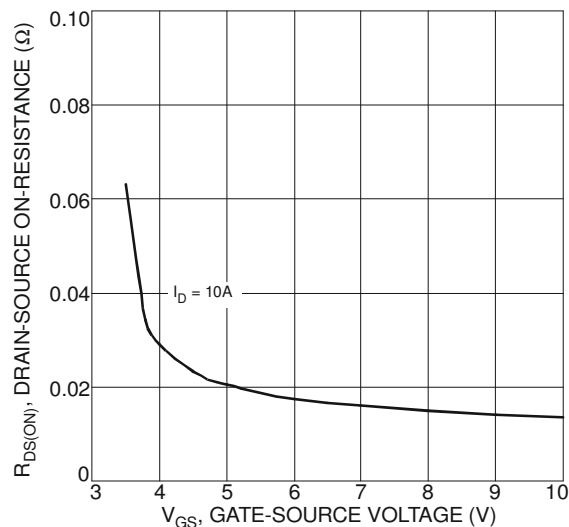


Figure 4. Typical On-Resistance vs. Drain Current and Gate Voltage

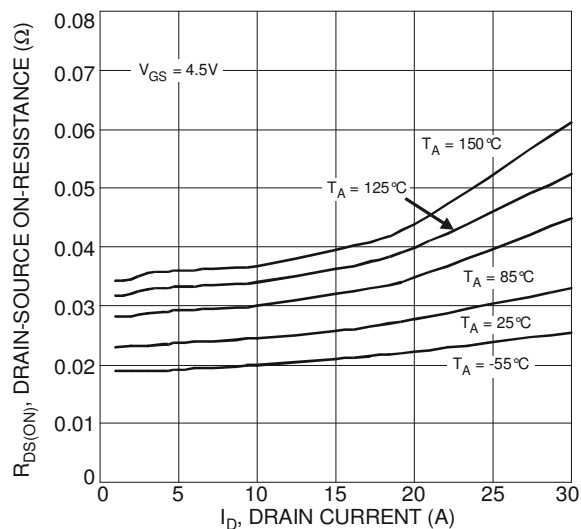


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

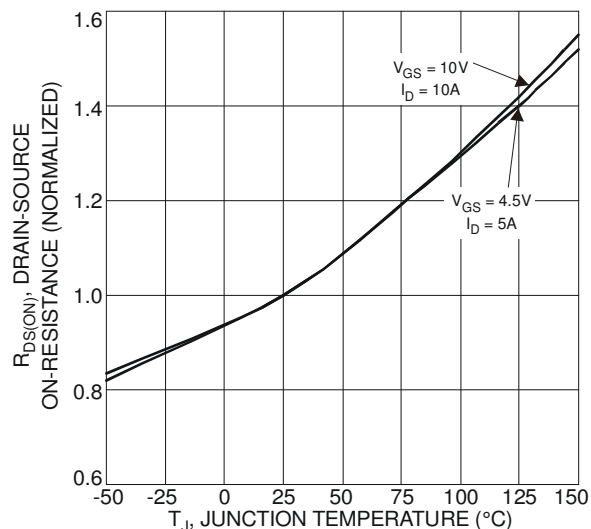


Figure 6. On-Resistance Variation with Temperature

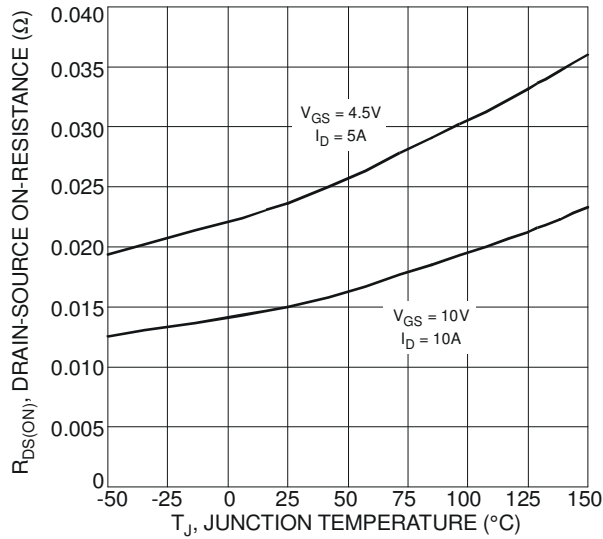


Figure 7. On-Resistance Variation with Temperature

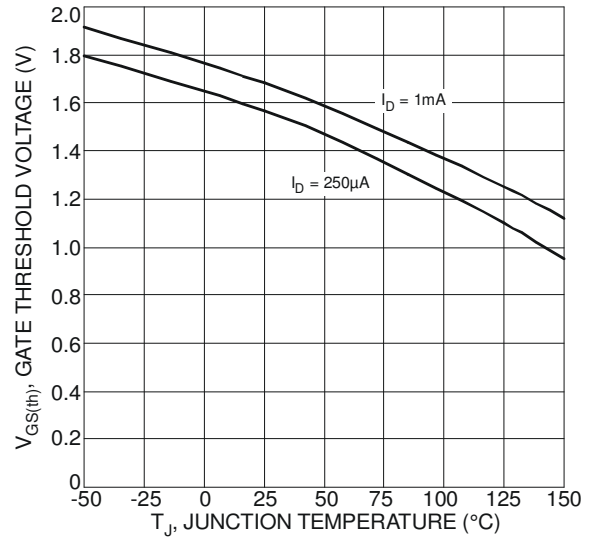


Figure 8. Gate Threshold Variation vs. Ambient Temperature

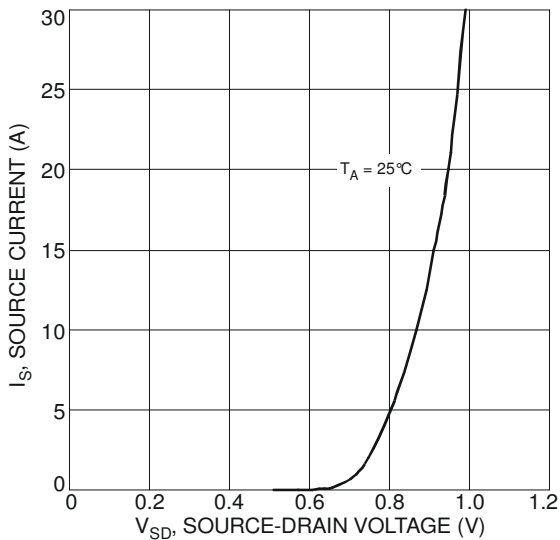


Figure 9. Diode Forward Voltage vs. Current

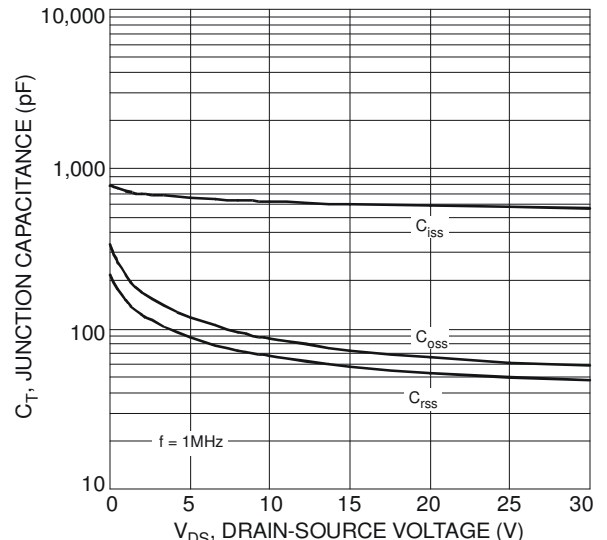


Figure 10. Typical Junction Capacitance

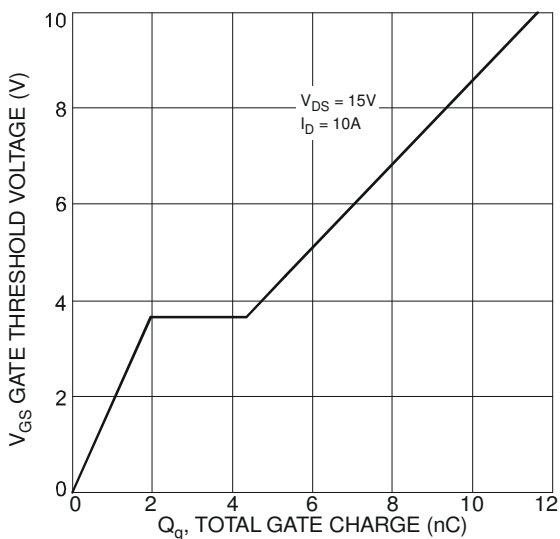


Figure 11. Gate Charge

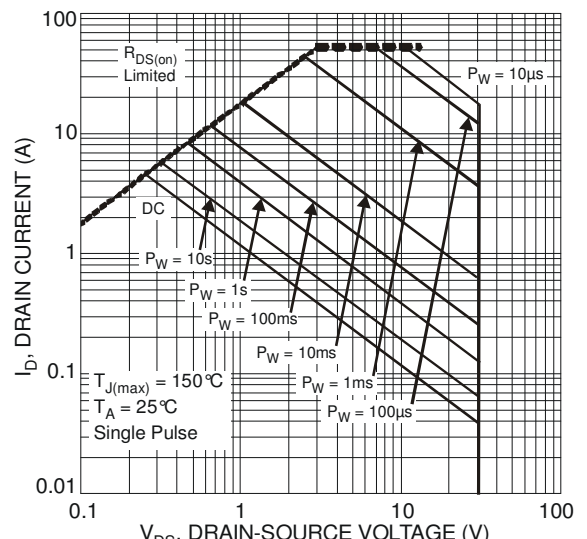


Figure 12. SOA, Safe Operation Area

P-CHANNEL

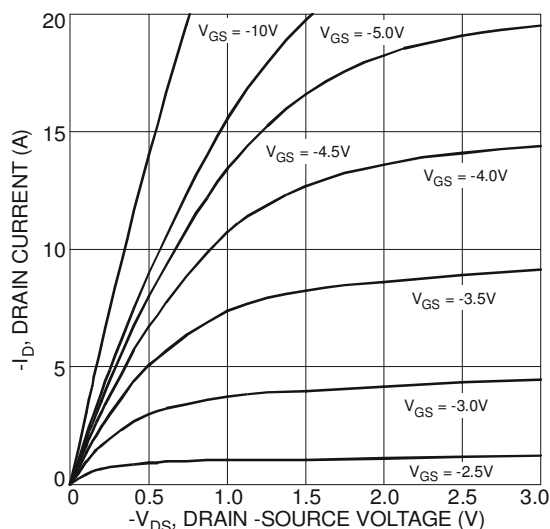


Figure 13. Typical Output Characteristics

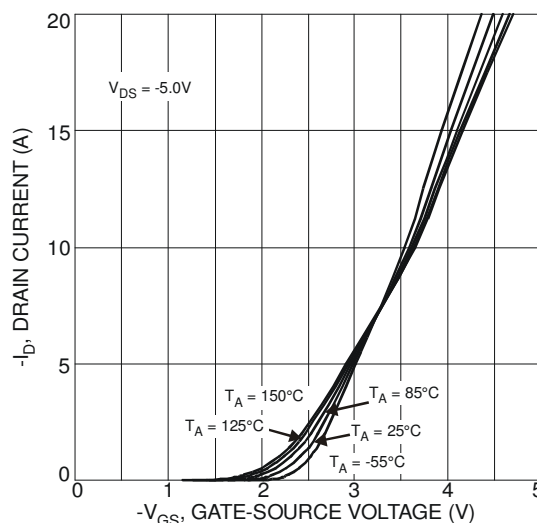


Figure 14. Typical Transfer Characteristics

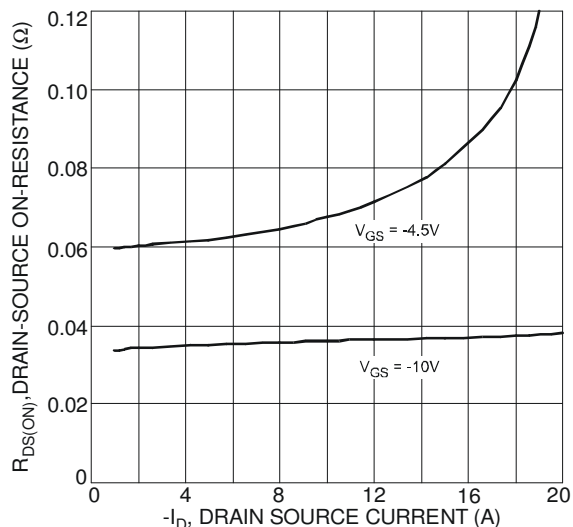


Figure 15. Typical On-Resistance vs. Drain Current and Gate Voltage

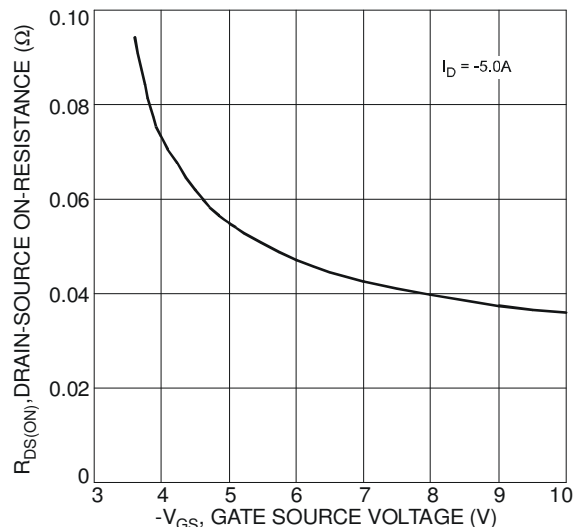


Figure 16. Typical On-Resistance vs. Drain Current and Gate Voltage

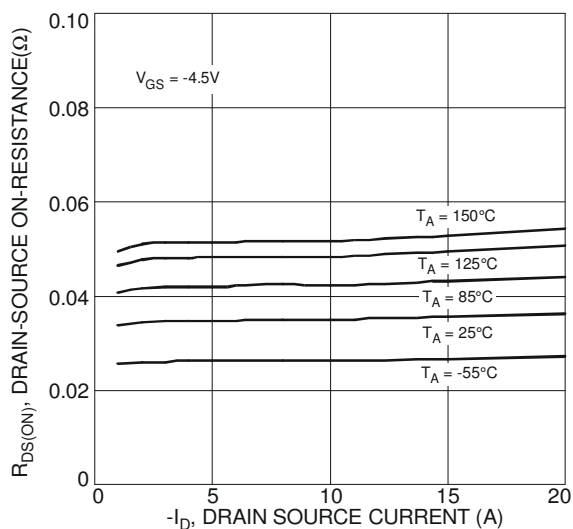


Figure 17. Typical On-Resistance vs. Drain Current and Temperature

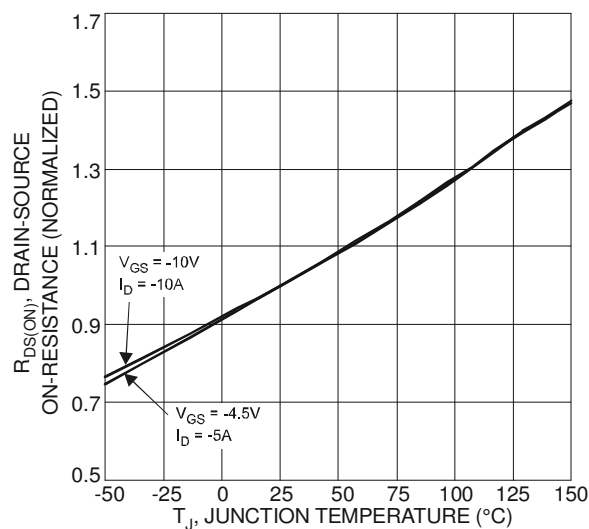


Figure 18. On-Resistance Variation with Temperature

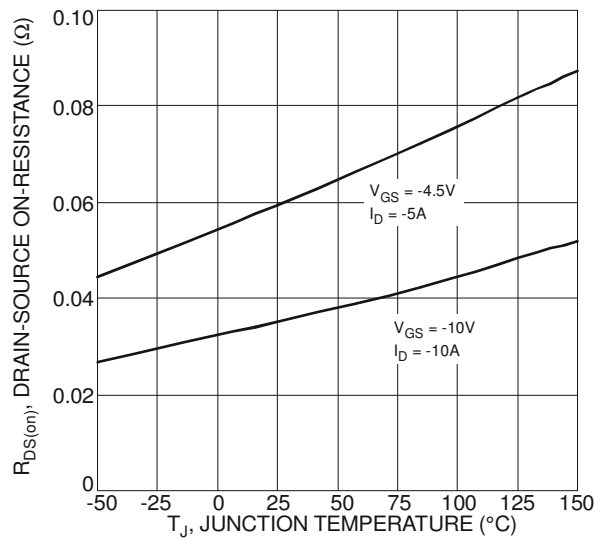


Figure 19. On-Resistance Variation with Temperature

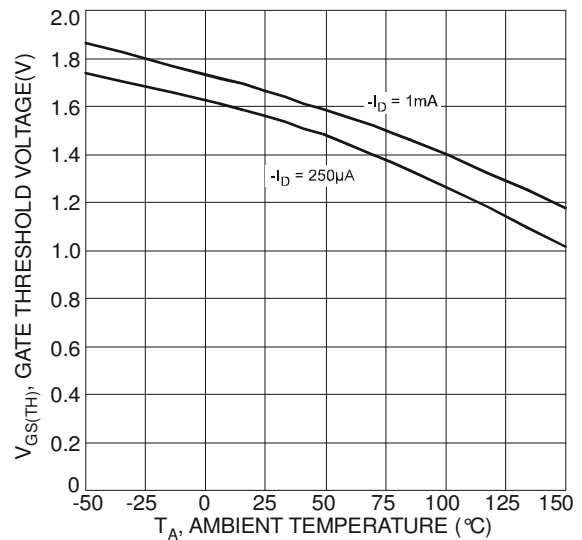


Figure 20. Gate Threshold Variation vs. Ambient Temperature

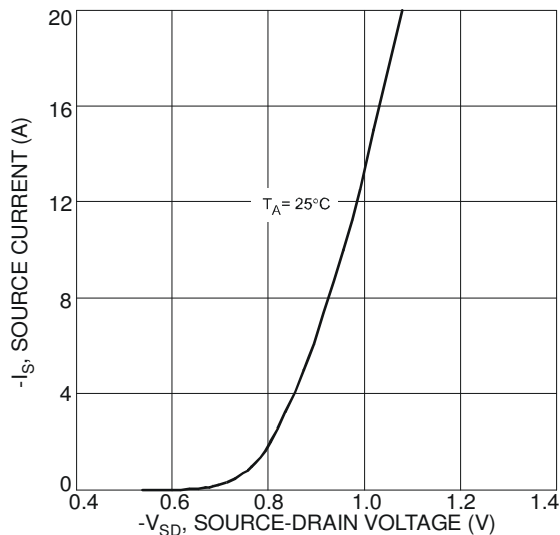


Figure 21. Diode Forward Voltage vs. Current

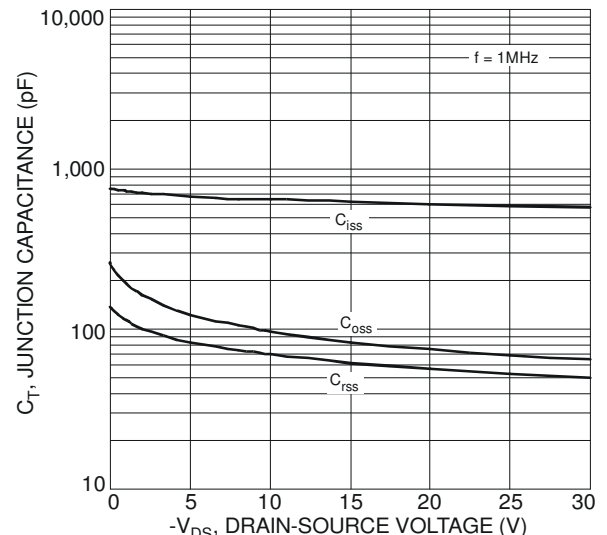


Figure 22. Typical Junction Capacitance

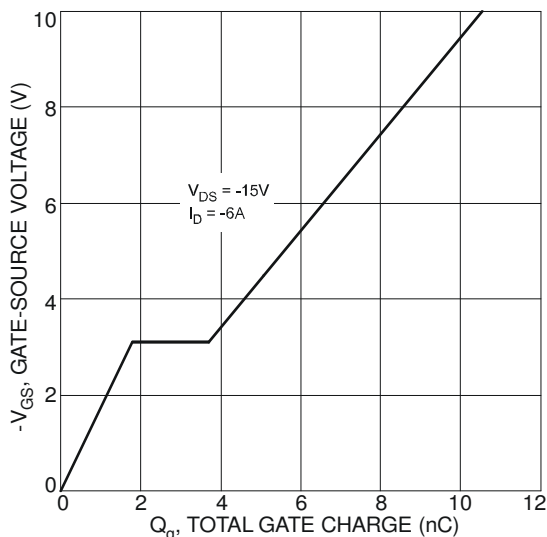


Figure 23. Gate-Charge Characteristics

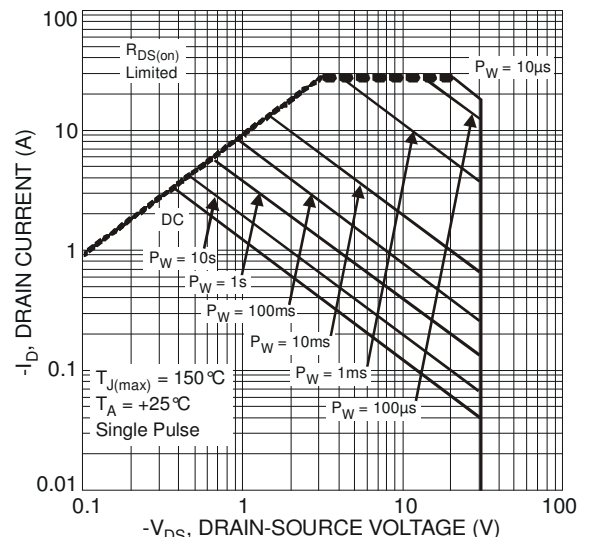
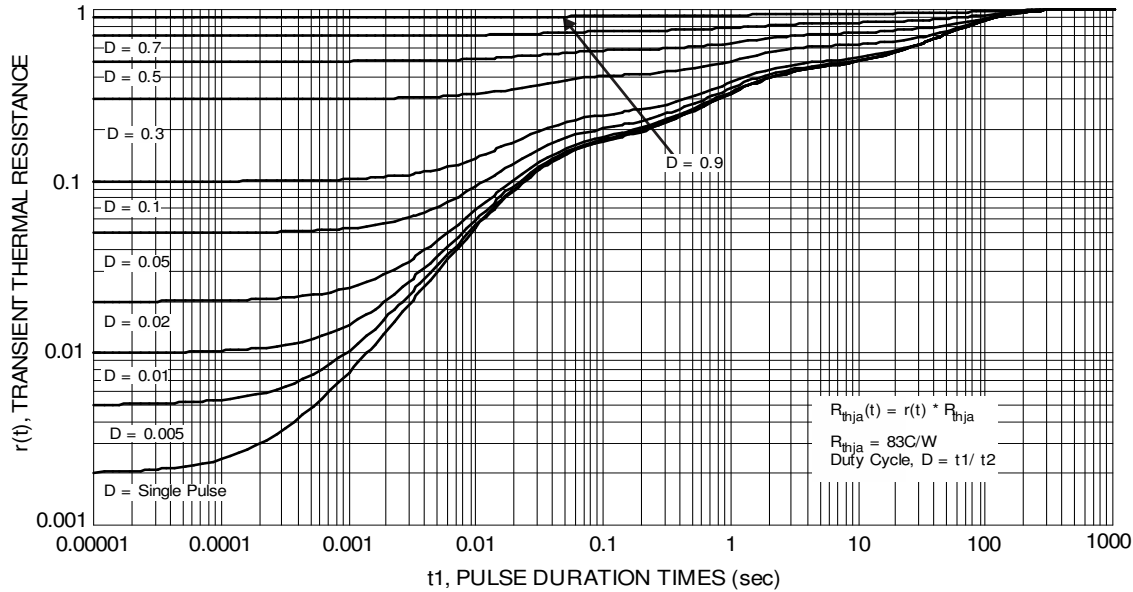
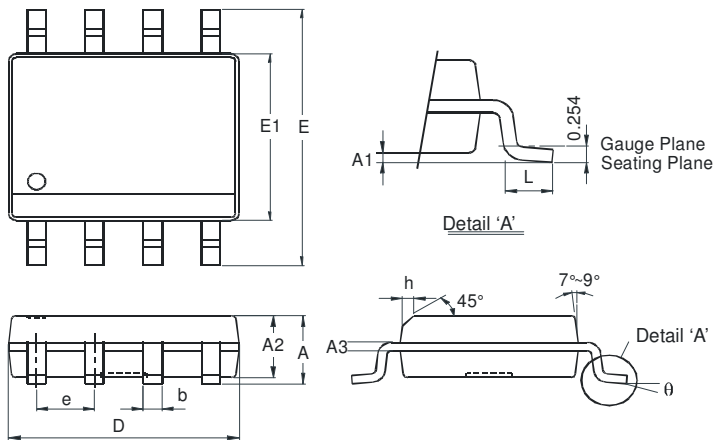


Figure 24. SOA, Safe Operation Area



Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

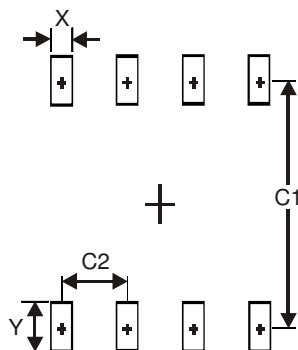


SO-8		
Dim	Min	Max
A	-	1.75
A1	0.10	0.20
A2	1.30	1.50
A3	0.15	0.25
b	0.3	0.5
D	4.85	4.95
E	5.90	6.10
E1	3.85	3.95
e	1.27 Typ	
h	-	0.35
L	0.62	0.82
θ	0°	8°
All Dimensions in mm		

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

SO-8



Dimensions	Value (in mm)
X	0.60
Y	1.55
C1	5.4
C2	1.27

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