

BUL44D2

Designer's™ Data Sheet

High Speed, High Gain Bipolar NPN Power Transistor with Integrated Collector-Emitter Diode and Built-in Efficient Antisaturation Network

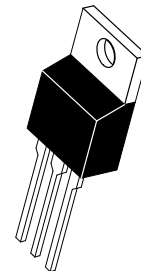
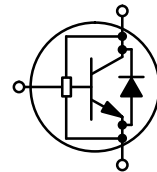
The BUL44D2 is state-of-art High Speed High gain BIPolar transistor (H2BIP). High dynamic characteristics and lot to lot minimum spread (± 150 ns on storage time) make it ideally suitable for light ballast applications. Therefore, there is no need to guarantee an h_{FE} window.

Main features:

- Low Base Drive Requirement
- High Peak DC Current Gain (55 Typical) @ $I_C = 100$ mA
- **Extremely Low Storage Time Min/Max Guarantees Due to the H2BIP Structure which Minimizes the Spread**
- Integrated Collector-Emitter Free Wheeling Diode
- Fully Characterized and Guaranteed Dynamic $V_{CE(sat)}$
- "6 Sigma" Process Providing Tight and Reproducible Parameter Spreads

It's characteristics make it also suitable for PFC application.

POWER TRANSISTORS
2 AMPERES
700 VOLTS
50 WATTS



CASE 221A-06
TO-220AB

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Sustaining Voltage	V_{CEO}	400	Vdc
Collector-Base Breakdown Voltage	V_{CBO}	700	Vdc
Collector-Emitter Breakdown Voltage	V_{CES}	700	Vdc
Emitter-Base Voltage	V_{EBO}	12	Vdc
Collector Current — Continuous	I_C	2	Adc
— Peak (1)	I_{CM}	5	
Base Current — Continuous	I_B	1	Adc
— Peak (1)	I_{BM}	2	
*Total Device Dissipation @ $T_C = 25^\circ\text{C}$	P_D	50	Watt
*Derate above 25°C		0.4	W/ $^\circ\text{C}$
Operating and Storage Temperature	T_J, T_{stg}	-65 to 150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Thermal Resistance	$R_{\theta JC}$ $R_{\theta JA}$	2.5 62.5	$^\circ\text{C/W}$
— Junction to Case			
— Junction to Ambient			
Maximum Lead Temperature for Soldering Purposes: 1/8" from case for 5 seconds	T_L	260	$^\circ\text{C}$

(1) Pulse Test: Pulse Width = 5 ms, Duty Cycle $\leq 10\%$.

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Designer's Data for "Worst Case" Conditions — The Designer's Data Sheet permits the design of most circuits entirely from the information presented. SOA Limit curves — representing boundaries on device characteristics — are given to facilitate "worst case" design.

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Sustaining Voltage ($I_C = 100\text{ mA}$, $L = 25\text{ mH}$)	$V_{CEO(sus)}$	400	470		Vdc
Collector–Base Breakdown Voltage ($I_{CBO} = 1\text{ mA}$)	V_{CBO}	700	920		Vdc
Emitter–Base Breakdown Voltage ($I_{EBO} = 1\text{ mA}$)	V_{EBO}	12	14.5		Vdc
Collector Cutoff Current ($V_{CE} = \text{Rated } V_{CEO}$, $I_B = 0$)	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$ I_{CEO}			50 500	μAdc
Collector Cutoff Current ($V_{CE} = \text{Rated } V_{CES}$, $V_{EB} = 0$) ($V_{CE} = 500\text{ V}$, $V_{EB} = 0$)	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$ I_{CES}			50 500 100	μAdc
Emitter–Cutoff Current ($V_{EB} = 10\text{ Vdc}$, $I_C = 0$)	I_{EBO}			100	μAdc

ON CHARACTERISTICS

Base–Emitter Saturation Voltage ($I_C = 0.4\text{ Adc}$, $I_B = 40\text{ mAdc}$) ($I_C = 1\text{ Adc}$, $I_B = 0.2\text{ Adc}$)	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$ @ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	$V_{BE(sat)}$		0.78 0.65 0.87 0.76	0.9 0.8 1 0.9	Vdc
Collector–Emitter Saturation Voltage ($I_C = 0.4\text{ Adc}$, $I_B = 40\text{ mAdc}$) ($I_C = 1\text{ Adc}$, $I_B = 0.2\text{ Adc}$) ($I_C = 0.4\text{ Adc}$, $I_B = 20\text{ mAdc}$)	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$ @ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$ @ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	$V_{CE(sat)}$		0.25 0.27 0.28 0.35 0.45 0.67	0.4 0.5 0.5 0.6 0.65 1	Vdc
DC Current Gain ($I_C = 0.4\text{ Adc}$, $V_{CE} = 1\text{ Vdc}$) ($I_C = 1\text{ Adc}$, $V_{CE} = 1\text{ Vdc}$)	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$ @ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	h_{FE}	20 18 10 7	32 26 14 9.5		—

DIODE CHARACTERISTICS

Forward Diode Voltage ($I_{EC} = 1\text{ Adc}$) ($I_{EC} = 0.4\text{ Adc}$) ($I_{EC} = 0.2\text{ Adc}$) ($I_{EC} = 0.2\text{ Adc}$)	@ $T_C = 25^\circ\text{C}$ @ $T_C = 25^\circ\text{C}$ @ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	V_{EC}		1.1 0.9 0.8 0.6	1.5 1.2 1	V
Forward Recovery Time (see Figure 22 bis) ($I_F = 0.2\text{ Adc}$, $di/dt = 10\text{ A}/\mu\text{s}$) ($I_F = 0.4\text{ Adc}$, $di/dt = 10\text{ A}/\mu\text{s}$) ($I_F = 1\text{ Adc}$, $di/dt = 10\text{ A}/\mu\text{s}$)	@ $T_C = 25^\circ\text{C}$ @ $T_C = 25^\circ\text{C}$ @ $T_C = 25^\circ\text{C}$	T_{fr}		415 390 340		ns

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic				Symbol	Min	Typ	Max	Unit
DYNAMIC SATURATION VOLTAGE								
Dynamic Saturation Voltage: Determined 1 μ s and 3 μ s respectively after rising I_{B1} reaches 90% of final I_{B1}	$I_C = 0.4\text{ A}$ $I_{B1} = 40\text{ mA}$ $V_{CC} = 300\text{ V}$	@ 1 μ s	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	$V_{CE(dsat)}$		3.3 6.8		V
		@ 3 μ s	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$			0.5 1.3		V
	$I_C = 1\text{ A}$ $I_{B1} = 0.2\text{ A}$ $V_{CC} = 300\text{ V}$	@ 1 μ s	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$			4.4 12.8		V
		@ 3 μ s	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$			0.5 1.8		V

DYNAMIC CHARACTERISTICS

Current Gain Bandwidth ($I_C = 0.5\text{ Adc}$, $V_{CE} = 10\text{ Vdc}$, $f = 1\text{ MHz}$)	f_T		13		MHz
Output Capacitance ($V_{CB} = 10\text{ Vdc}$, $I_E = 0$, $f = 1\text{ MHz}$)	C_{ob}		50	75	pF
Input Capacitance ($V_{EB} = 8\text{ Vdc}$)	C_{ib}		240	500	pF

SWITCHING CHARACTERISTICS: Resistive Load (D.C. $\leq 10\%$, Pulse Width = 40 μs)

Turn-on Time	$I_C = 0.5\text{ Adc}$, $I_{B1} = 50\text{ mAdc}$ $I_{B2} = 250\text{ mAdc}$ $V_{CC} = 300\text{ Vdc}$	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_{on}		450 600	600	ns
Turn-off Time		@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_{off}	700	1300	1000	ns
Turn-on Time	$I_C = 1\text{ Adc}$, $I_{B1} = 0.2\text{ Adc}$ $I_{B2} = 0.5\text{ Adc}$ $V_{CC} = 300\text{ Vdc}$	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_{on}		90 105	150	ns
Turn-off Time		@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_{off}		1.1 1.5	1.25	μs

SWITCHING CHARACTERISTICS: Inductive Load ($V_{clamp} = 300\text{ V}$, $V_{CC} = 15\text{ V}$, $L = 200\text{ }\mu\text{H}$)

Fall Time	$I_C = 0.4\text{ Adc}$ $I_{B1} = 40\text{ mAdc}$ $I_{B2} = 0.2\text{ Adc}$	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_f		110 105	150	ns
Storage Time		@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_s		0.55 0.70	0.75	μs
Crossover Time		@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_c		85 80	150	ns
Fall Time	$I_C = 1\text{ Adc}$ $I_{B1} = 0.2\text{ Adc}$ $I_{B2} = 0.5\text{ Adc}$	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_f		100 90	150	ns
Storage Time		@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_s		1.05 1.45	1.5	μs
Crossover Time		@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_c		100 100	175	ns
Fall Time	$I_C = 0.8\text{ Adc}$ $I_{B1} = 160\text{ mAdc}$ $I_{B2} = 160\text{ mAdc}$	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_f		110 180	150	ns
Storage Time		@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_s	2.05	2.8	2.35	μs
Crossover Time		@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_c		180 400	300	ns
Fall Time	$I_C = 0.4\text{ Adc}$ $I_{B1} = 40\text{ mAdc}$ $I_{B2} = 40\text{ mAdc}$	@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_f		150 175	225	ns
Storage Time		@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_s	1.65	2.2	1.95	μs
Crossover Time		@ $T_C = 25^\circ\text{C}$ @ $T_C = 125^\circ\text{C}$	t_c		150 330	250	ns

TYPICAL STATIC CHARACTERISTICS

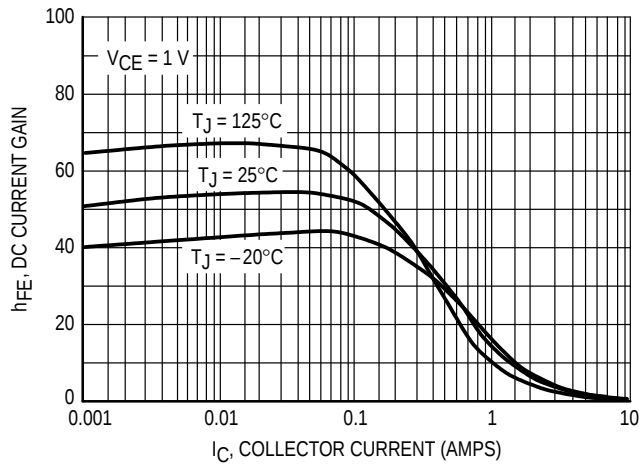


Figure 1. DC Current Gain @ 1 Volt

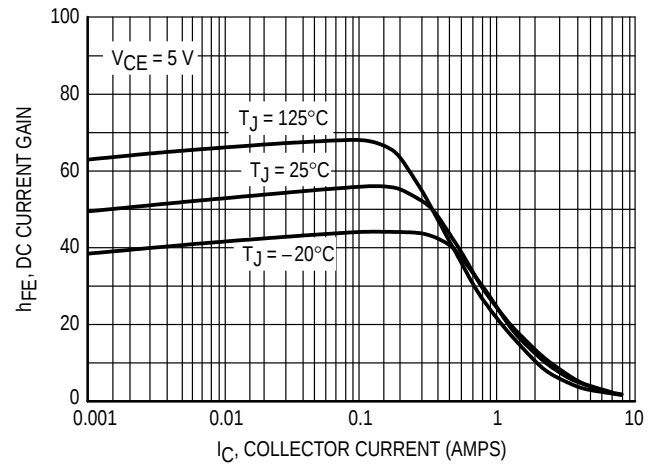


Figure 2. DC Current Gain @ 5 Volt

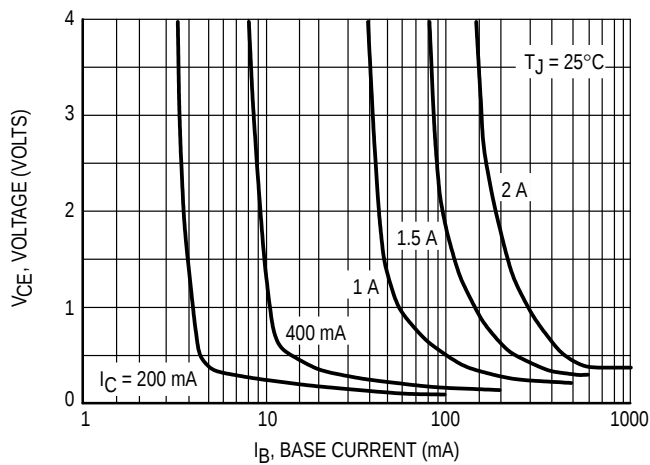


Figure 3. Collector Saturation Region

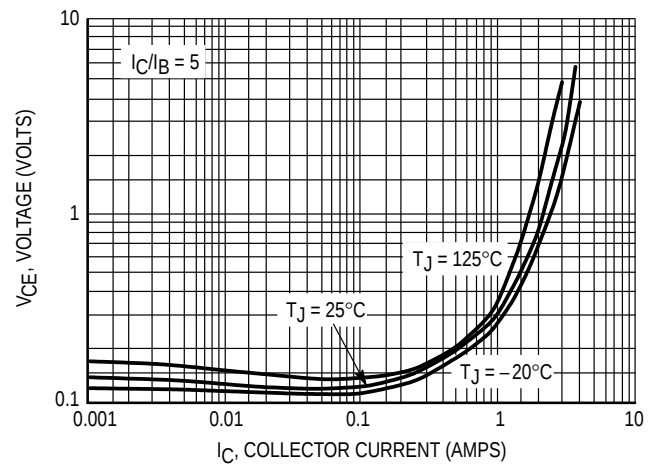


Figure 4. Collector-Emitter Saturation Voltage

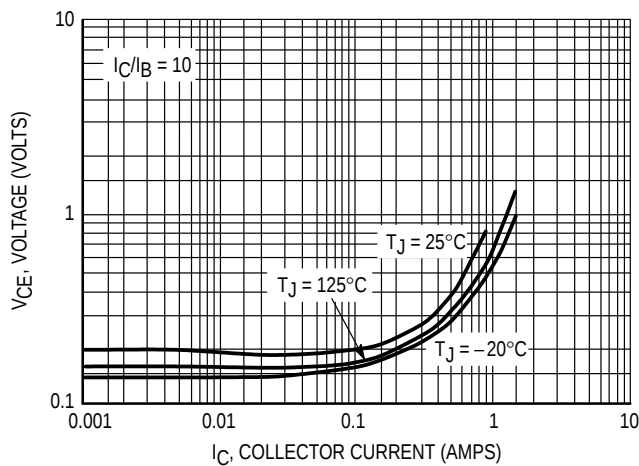


Figure 5. Collector-Emitter Saturation Voltage

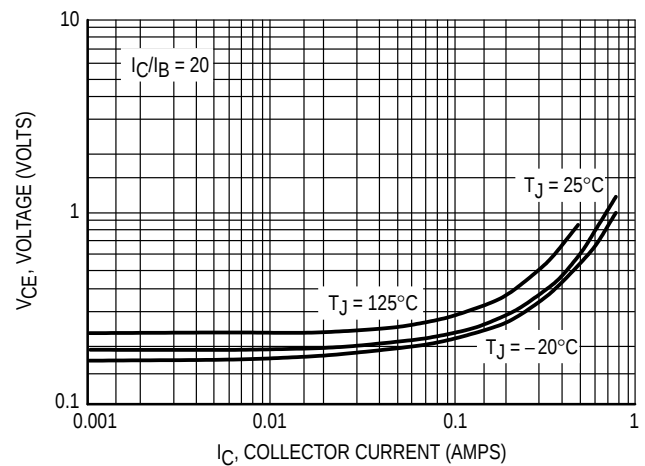


Figure 6. Collector-Emitter Saturation Voltage

TYPICAL STATIC CHARACTERISTICS

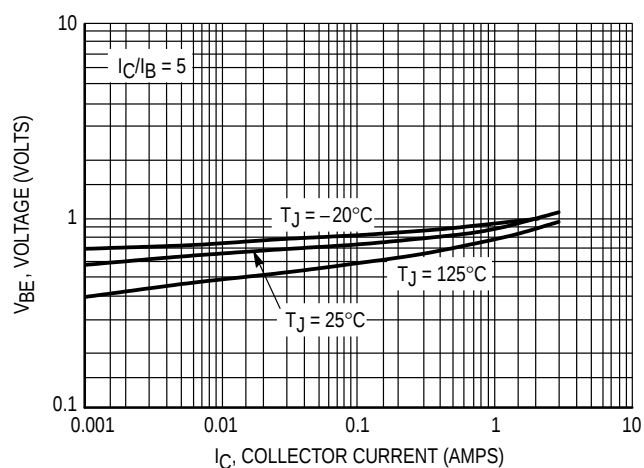


Figure 7A. Base-Emitter Saturation Region

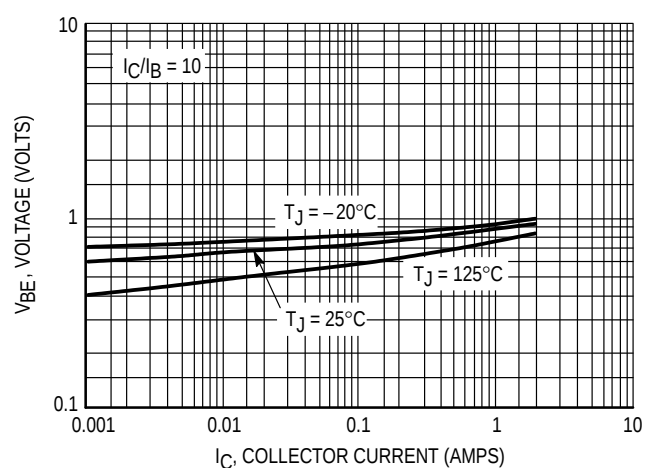


Figure 7B. Base-Emitter Saturation Region

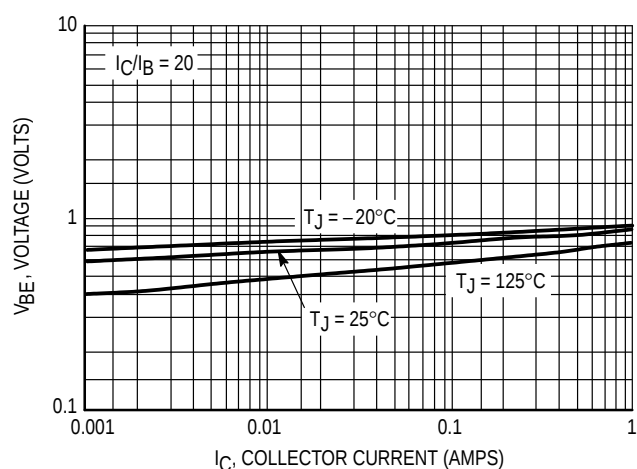


Figure 7C. Base-Emitter Saturation Region

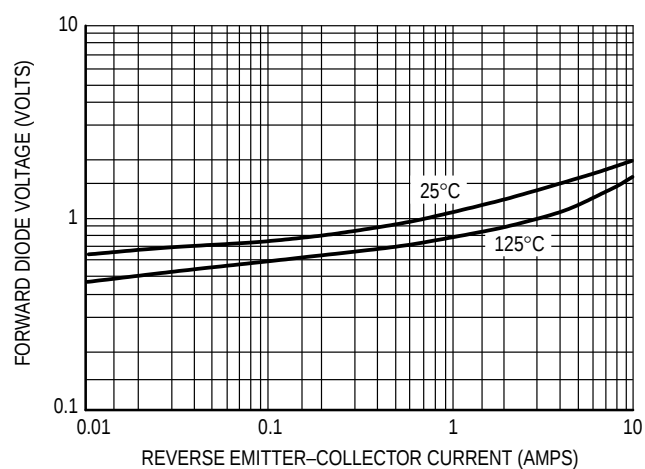


Figure 8. Forward Diode Voltage

TYPICAL SWITCHING CHARACTERISTICS

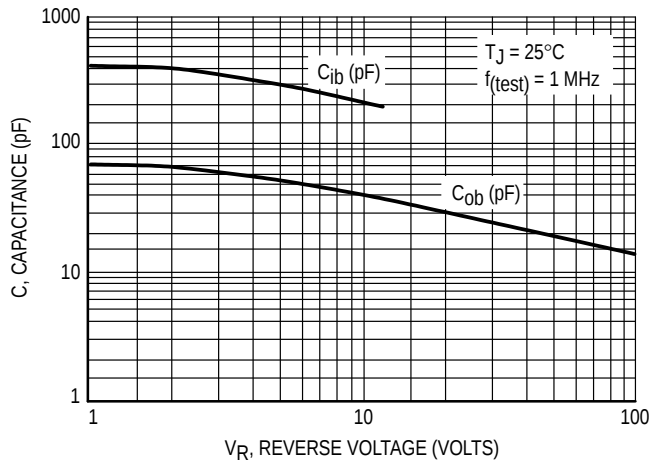


Figure 9. Capacitance

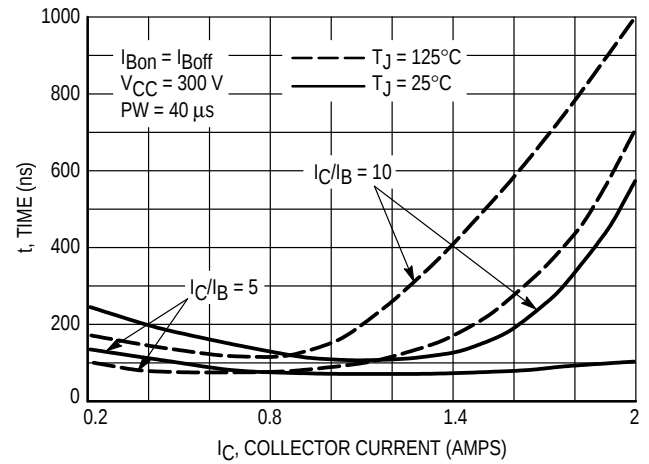
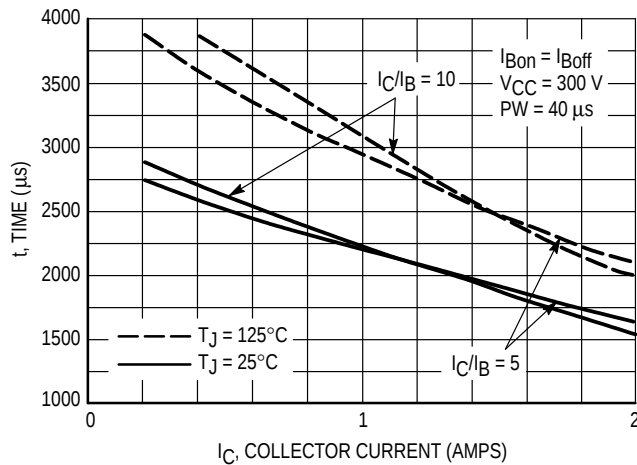
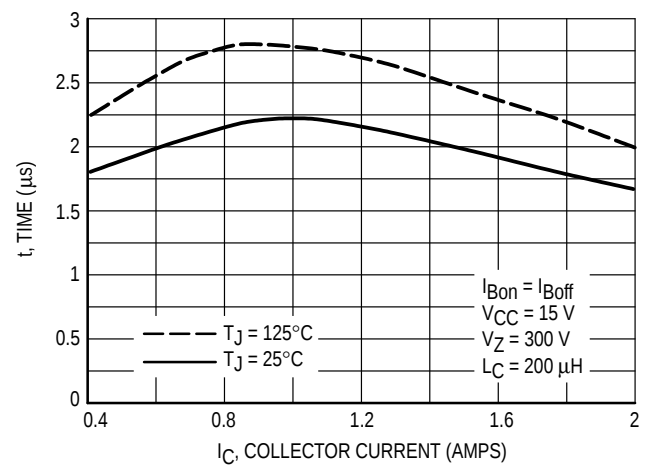
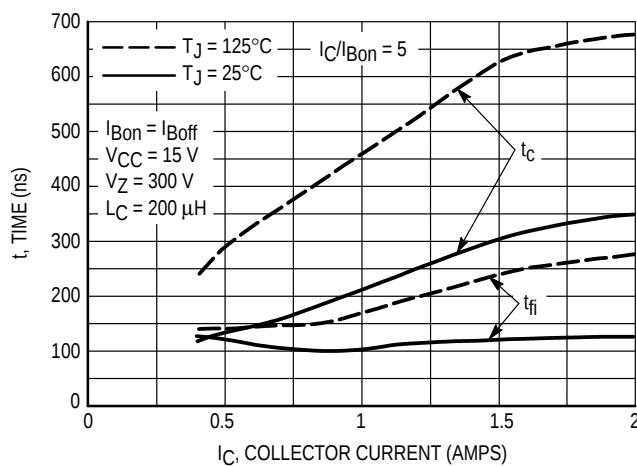
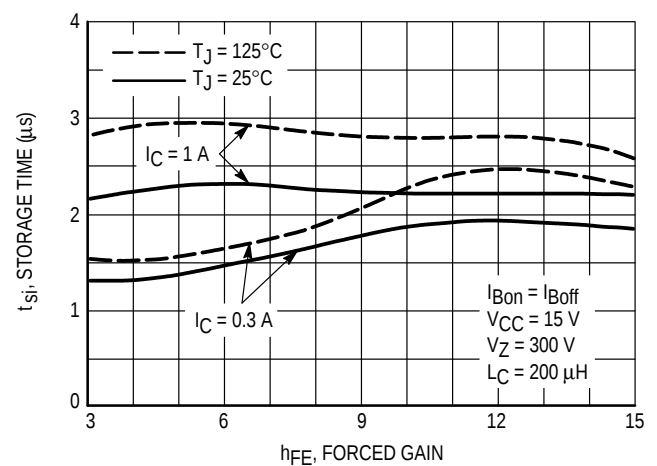
Figure 10. Resistive Switch Time, t_{on} Figure 11. Resistive Switch Time, t_{off} Figure 12. Inductive Storage Time, t_{si} @ $I_C/I_B = 5$ Figure 13. Inductive Switching, t_c & t_{fi} @ $I_C/I_B = 5$ 

Figure 14. Inductive Storage Time

TYPICAL SWITCHING CHARACTERISTICS

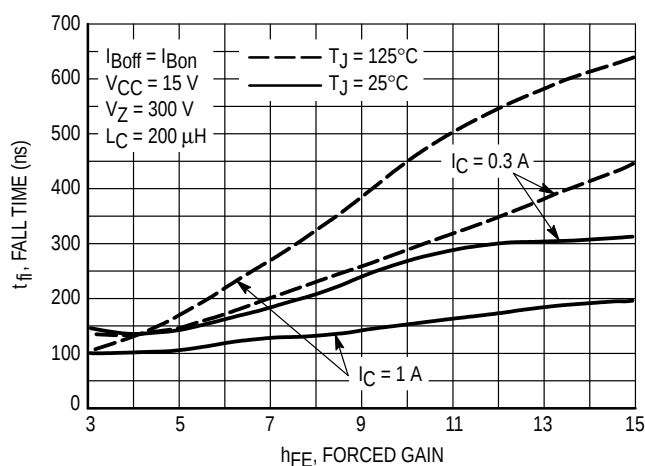


Figure 15. Inductive Fall Time

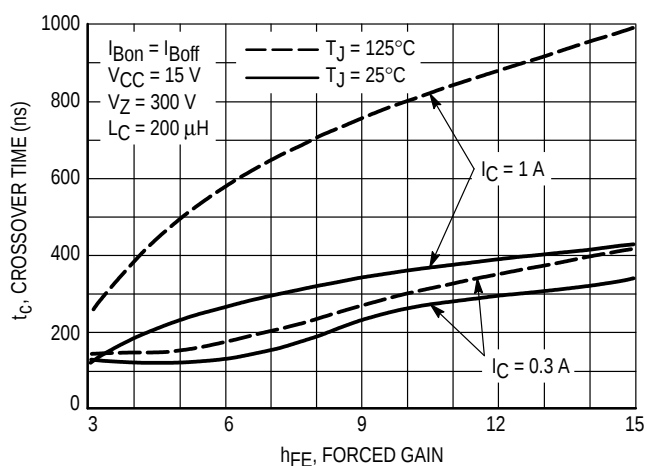
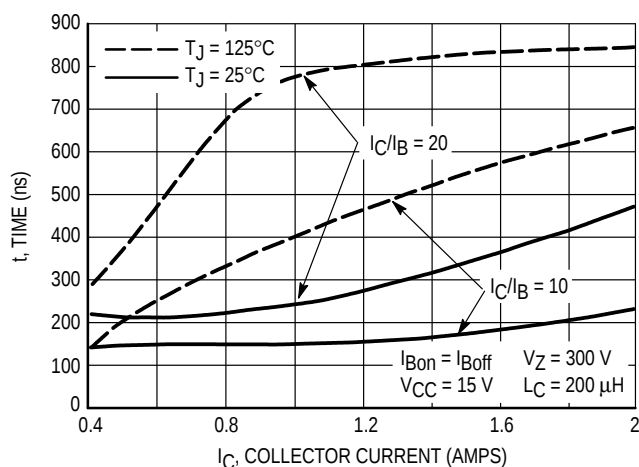
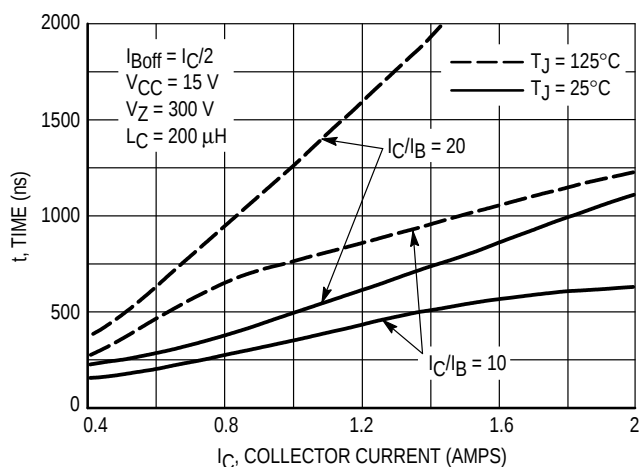
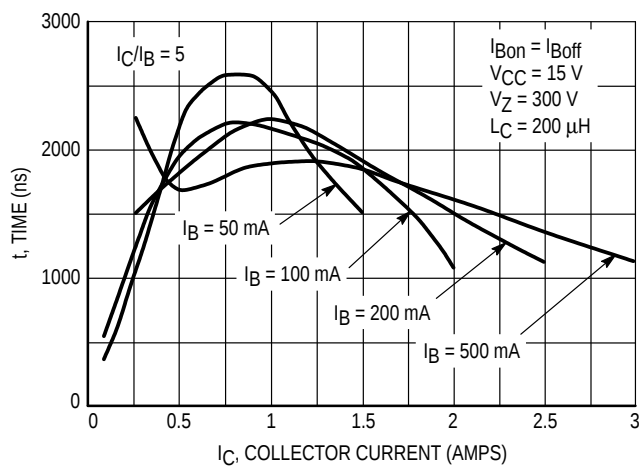
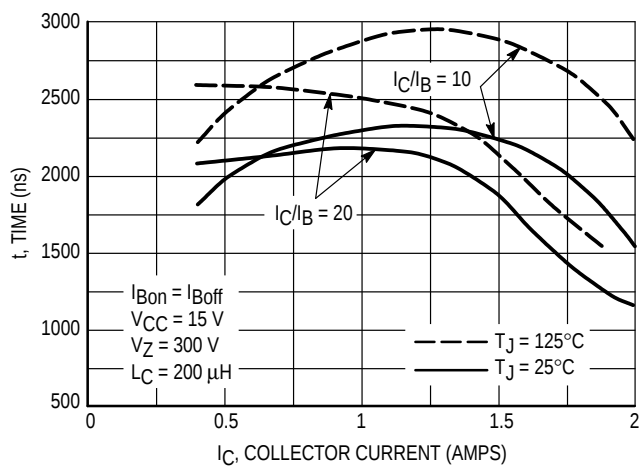


Figure 16. Inductive Crossover Time

Figure 17. Inductive Switching, t_{fi} Figure 18. Inductive Switching, t_c Figure 19. Inductive Storage Time, t_{si} Figure 20. Inductive Storage Time, t_{si}

TYPICAL SWITCHING CHARACTERISTICS

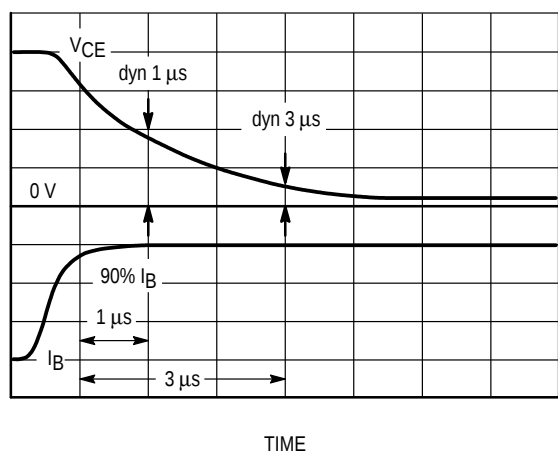


Figure 21. Dynamic Saturation Voltage Measurements

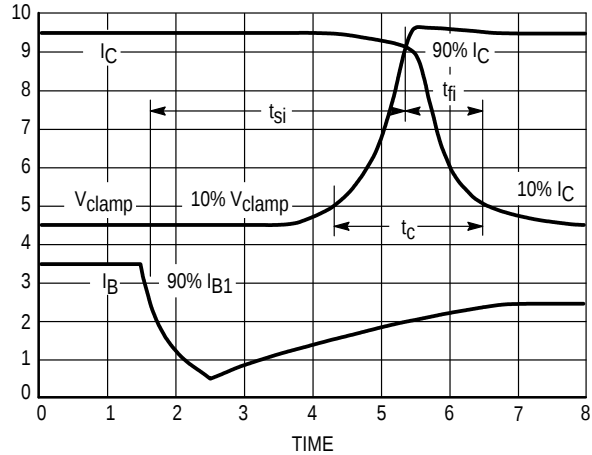


Figure 22. Inductive Switching Measurements

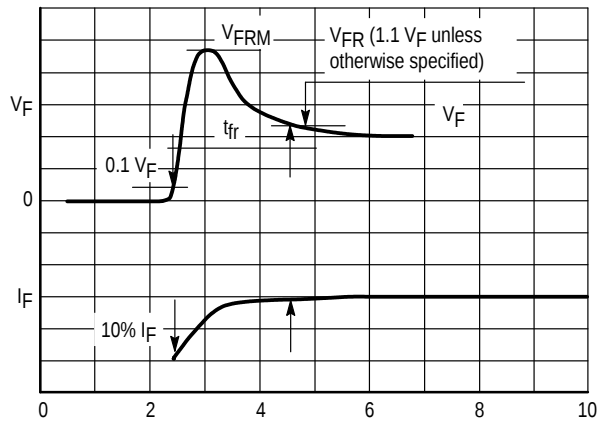
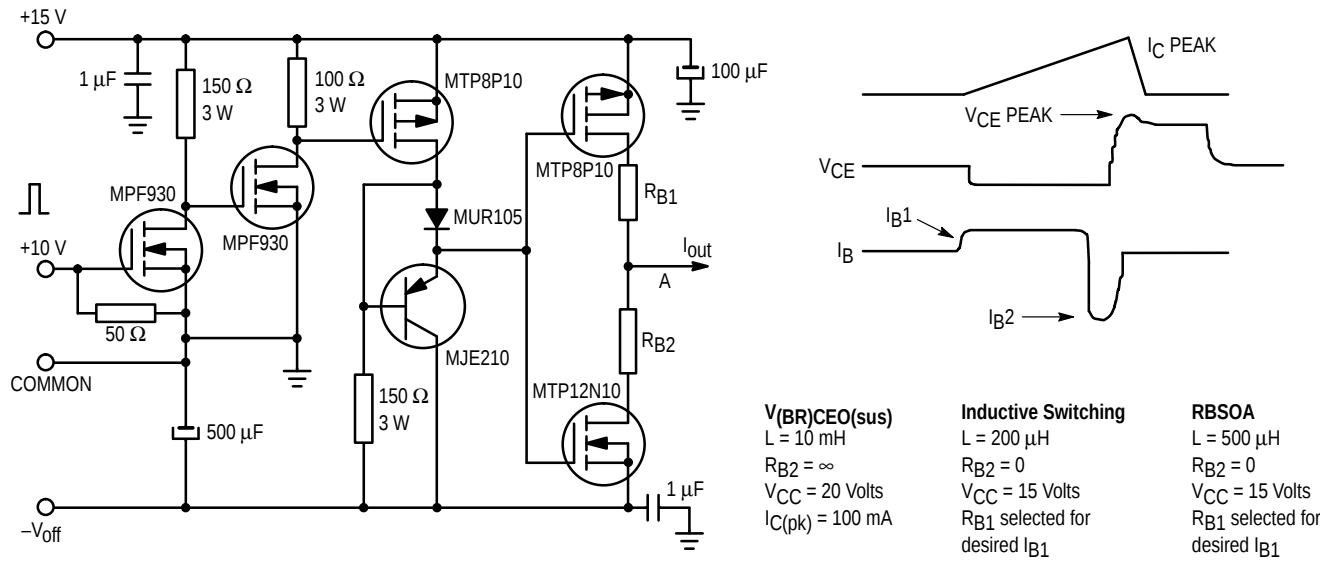


Figure 22 bis. t_{fr} Measurements

Table 1. Inductive Load Switching Drive Circuit



TYPICAL CHARACTERISTICS

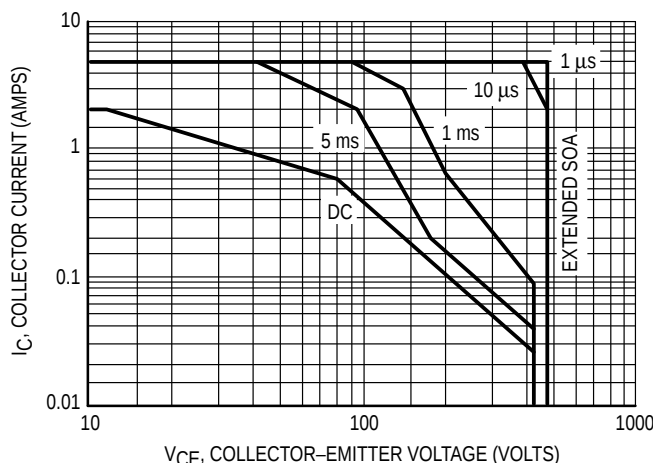


Figure 23. Forward Bias Safe Operating Area

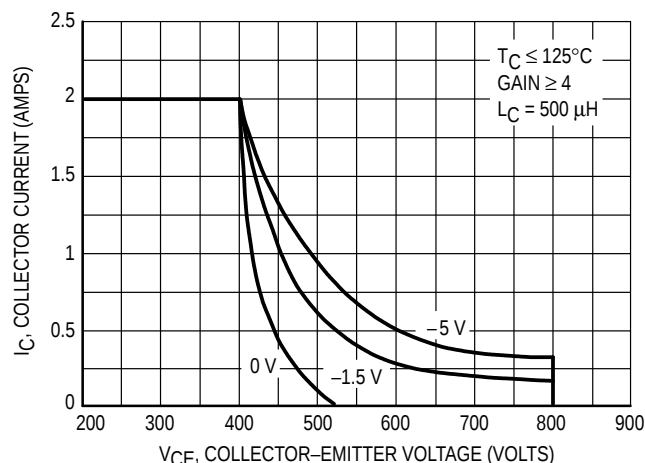


Figure 24. Reverse Bias Safe Operating Area

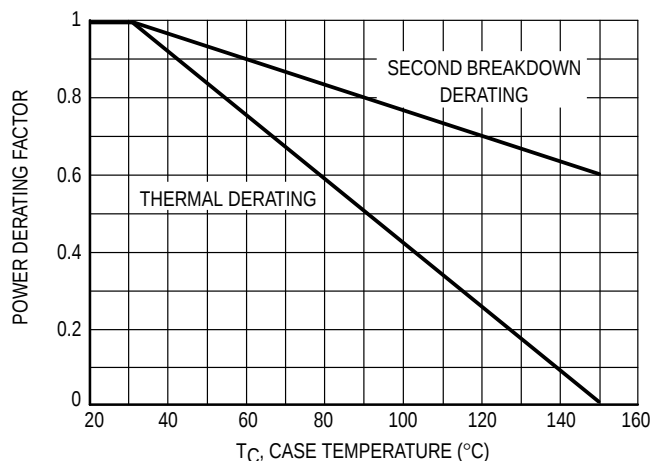
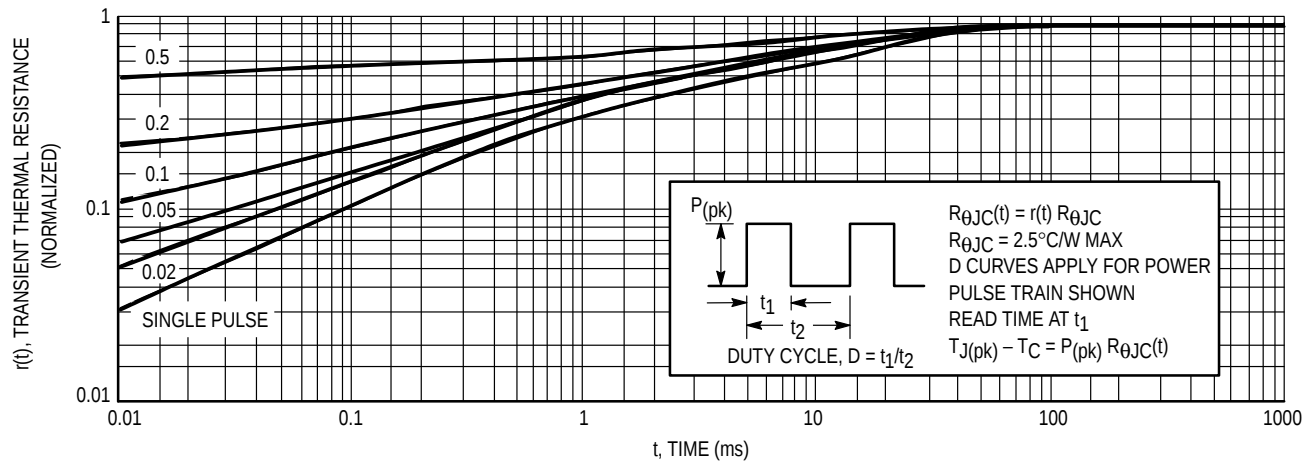


Figure 25. Forward Bias Power Derating

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_C - V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure 23 is based on $T_C = 25^\circ\text{C}$; $T_{J(pk)}$ is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% but must be derated when $T_C > 25^\circ\text{C}$. Second breakdown limitations do not derate the same as thermal limitations. Allowable current at the voltages shown on Figure 23 may be found at any case temperature by using the appropriate curve on Figure 25.

$T_{J(pk)}$ may be calculated from the data in Figure 26. At any case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown. For inductive loads, high voltage and current must be sustained simultaneously during turn-off with the base to emitter junction reverse biased. The safe level is specified as a reverse biased safe operating area (Figure 24). This rating is verified under clamped conditions so that the device is never subjected to an avalanche mode.

TYPICAL THERMAL RESPONSE

Figure 26. Typical Thermal Response ($Z_{\theta JC}(t)$) for BUL44D2

TYPICAL STATIC CHARACTERISTICS

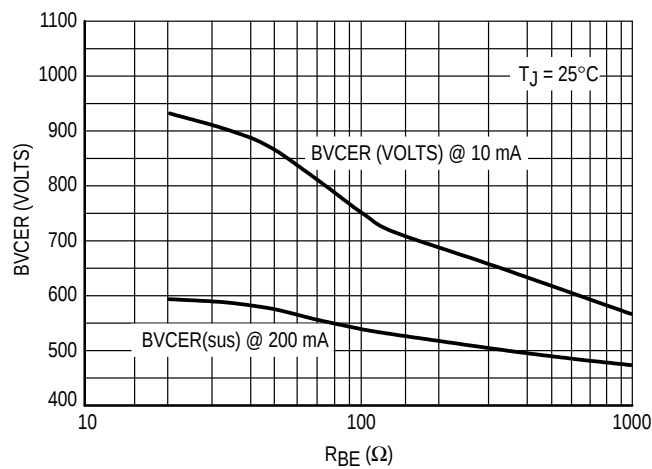
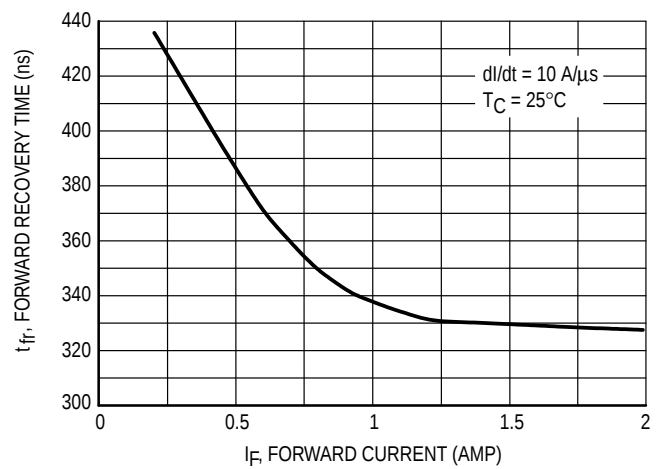
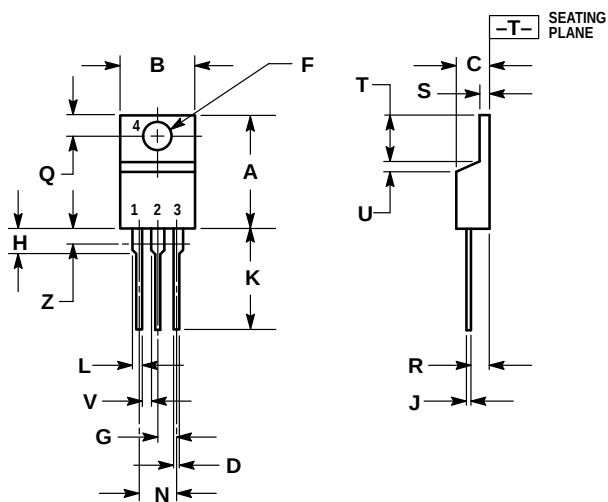


Figure 27. BVCER

Figure 28. Forward Recovery Time t_{fr}

PACKAGE DIMENSIONS



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.570	0.620	14.48	15.75
B	0.380	0.405	9.66	10.28
C	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
H	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	—	1.15	—
Z	—	0.080	—	2.04

STYLE 1:

- PIN 1: BASE
 2. COLLECTOR
 3. EMITTER
 4. COLLECTOR

CASE 221A-06
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