

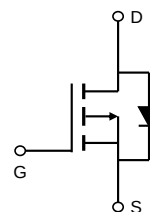
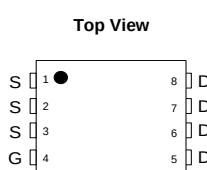
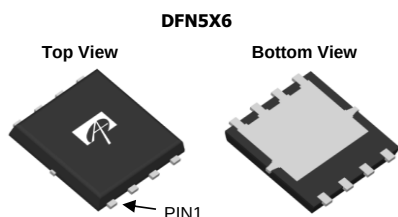
General Description

The AON6435 combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. This device is ideal for load switch and battery protection applications.

Product Summary

V_{DS}	-30V
I_D (at $V_{GS} = -10V$)	-34A
$R_{DS(ON)}$ (at $V_{GS} = -10V$)	< 17mΩ
$R_{DS(ON)}$ (at $V_{GS} = -5V$)	< 34mΩ

100% UIS Tested
 100% R_g Tested



Absolute Maximum Ratings $T_A = 25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 25	V
Continuous Drain Current	I_D	-34	A
$T_C = 25^\circ C$		-21.5	
$T_C = 100^\circ C$			
Pulsed Drain Current ^C	I_{DM}	-95	A
Continuous Drain Current	I_{DSM}	-12	A
$T_A = 25^\circ C$		-10	
$T_A = 70^\circ C$			
Avalanche Current ^C	I_{AS}	24	A
Avalanche energy $L = 0.1mH$ ^C	E_{AS}	29	mJ
Power Dissipation ^B	P_D	31	W
$T_C = 25^\circ C$		12.5	
$T_C = 100^\circ C$			
Power Dissipation ^A	P_{DSM}	4.1	W
$T_A = 25^\circ C$		2.6	
$T_A = 70^\circ C$			
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	24	30	$^\circ C/W$
$t \leq 10s$				
Maximum Junction-to-Ambient ^{A D}	$R_{\theta JA}$	53	64	$^\circ C/W$
Steady-State				
Maximum Junction-to-Case	$R_{\theta JC}$	3.4	4	$^\circ C/W$
Steady-State				

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =-250μA, V _{GS} =0V	-30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =-30V, V _{GS} =0V T _J =55°C			-1 -5	μA
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} =±25V			±100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250μA	-1.7	-2.3	-3	V
I _{D(ON)}	On state drain current	V _{GS} =-10V, V _{DS} =-5V	-95			A
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V, I _D =-20A T _J =125°C		13 19	17 25	mΩ
		V _{GS} =-5V, I _D =-15A		25	34	mΩ
g _{FS}	Forward Transconductance	V _{DS} =-5V, I _D =-20A		28		S
V _{SD}	Diode Forward Voltage	I _S =-1A, V _{GS} =0V		-0.73	-1	V
I _S	Maximum Body-Diode Continuous Current				-35	A
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =-15V, f=1MHz		1130	1400	pF
C _{oss}	Output Capacitance			240		pF
C _{rss}	Reverse Transfer Capacitance			155		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		5.8	8	Ω
SWITCHING PARAMETERS						
Q _{g(10V)}	Total Gate Charge	V _{GS} =-10V, V _{DS} =-15V, I _D =-20A		21		nC
Q _{g(4.5V)}	Total Gate Charge			10		nC
Q _{gs}	Gate Source Charge			4		nC
Q _{gd}	Gate Drain Charge			6		nC
t _{D(on)}	Turn-On DelayTime	V _{GS} =-10V, V _{DS} =-15V, R _L =0.75Ω, R _{GEN} =3Ω		10		ns
t _r	Turn-On Rise Time			8		ns
t _{D(off)}	Turn-Off DelayTime			15		ns
t _f	Turn-Off Fall Time			7		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =-20A, dI/dt=500A/μs		13.5		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =-20A, dI/dt=500A/μs		29		nC

A. The value of R_{0JA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25° C. The Power dissipation P_{DSM} is based on R_{0JA} and the maximum allowed junction temperature of 150° C. The value in any given application depends on the user's specific board design, and the maximum temperature of 150° C may be used if the PCB allows it.

B. The power dissipation P_D is based on T_{J(MAX)}=150° C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.

C. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150° C. Ratings are based on low frequency and duty cycles to keep initial T_J=25° C. Maximum UIS current limited by test equipment.

D. The R_{0JA} is the sum of the thermal impedance from junction to case R_{0JC} and case to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of T_{J(MAX)}=150° C. The SOA curve provides a single pulse rating.

G. The maximum current rating is package limited.

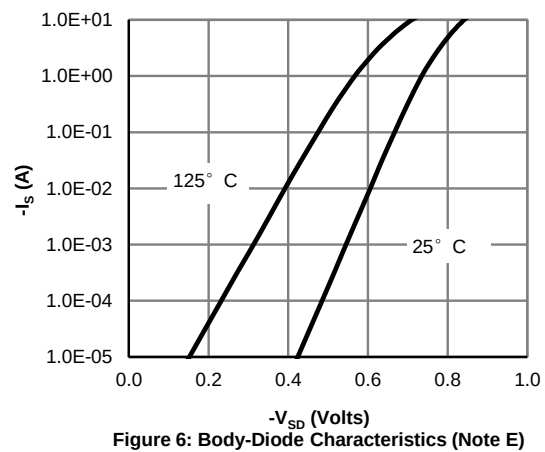
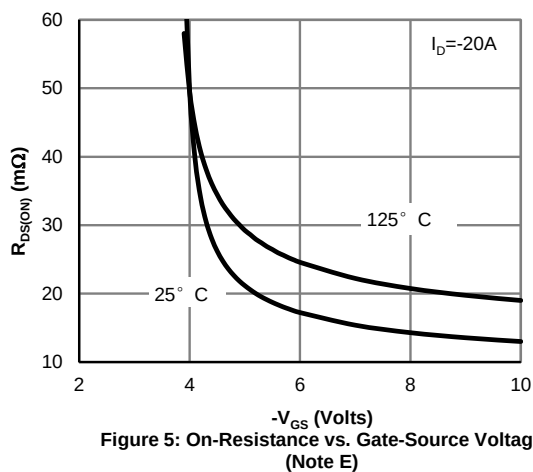
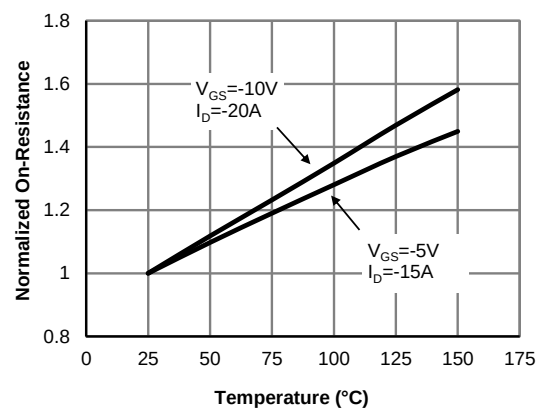
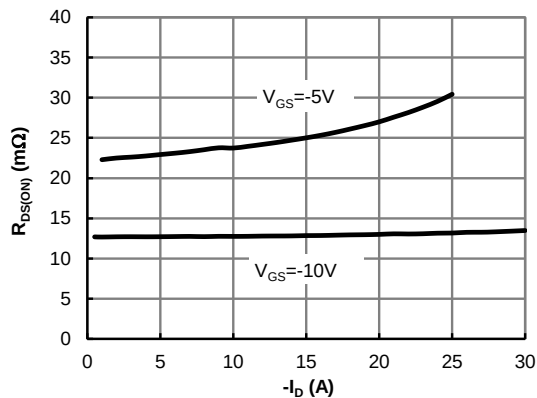
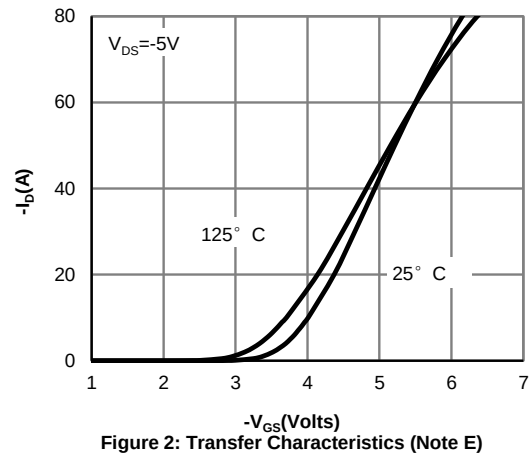
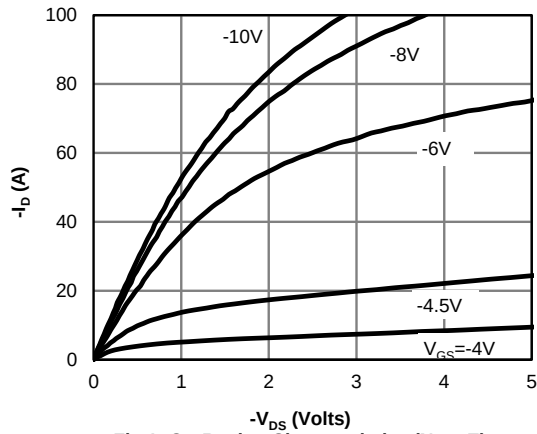
H. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25° C.

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



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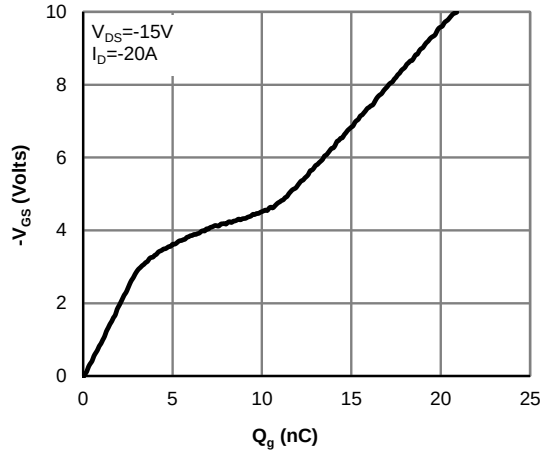


Figure 7: Gate-Charge Characteristics

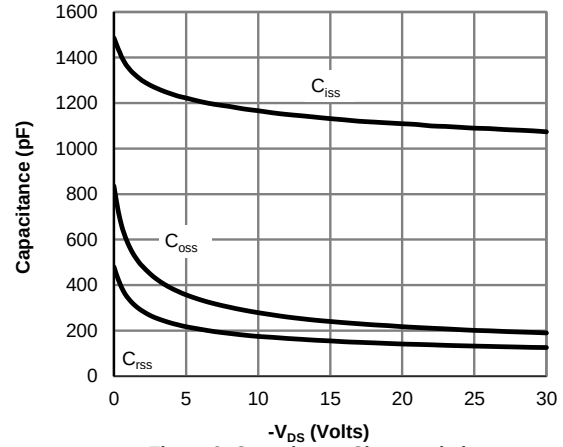


Figure 8: Capacitance Characteristics

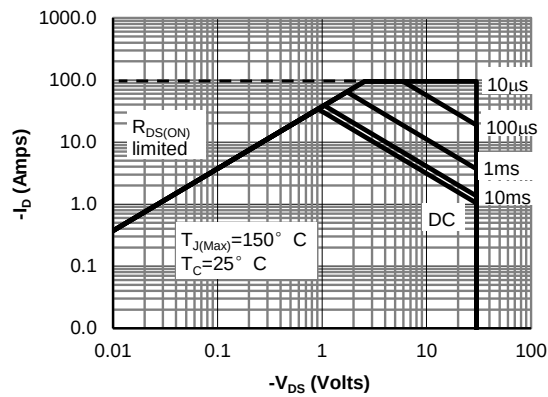


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

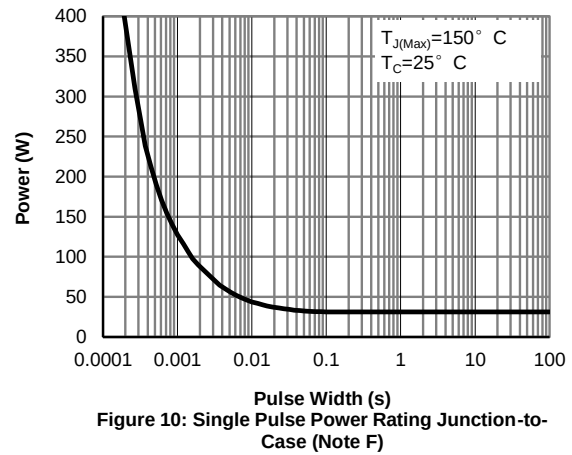


Figure 10: Single Pulse Power Rating Junction-to-Case (Note F)

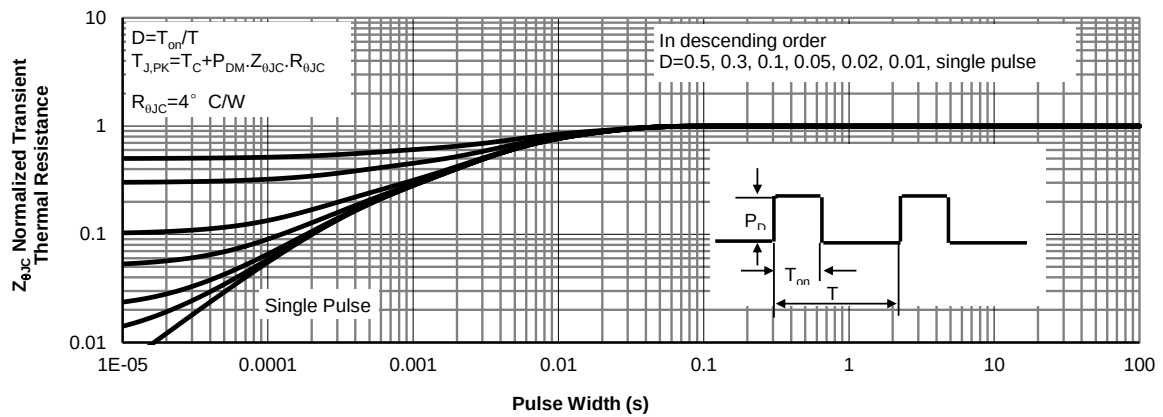


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

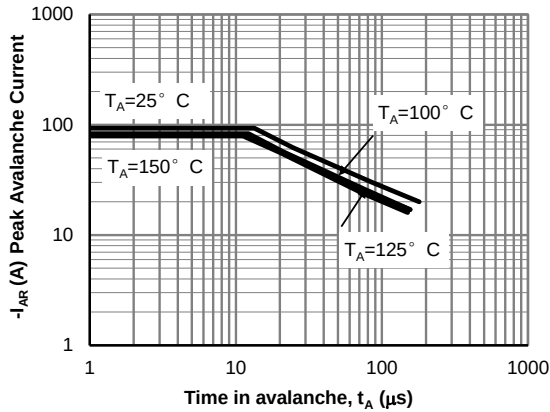


Figure 12: Single Pulse Avalanche capability (Note C)

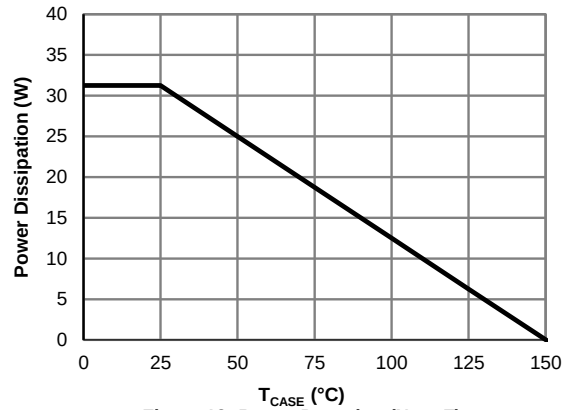


Figure 13: Power De-rating (Note F)

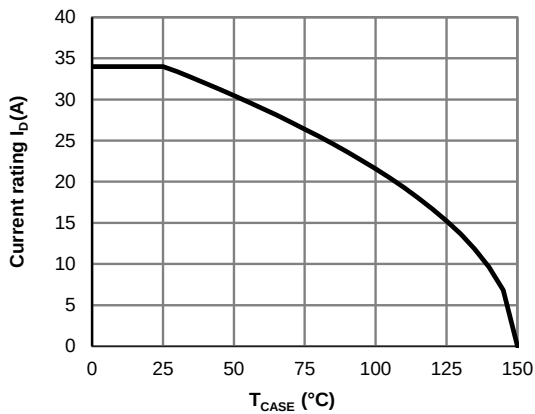


Figure 14: Current De-rating (Note F)

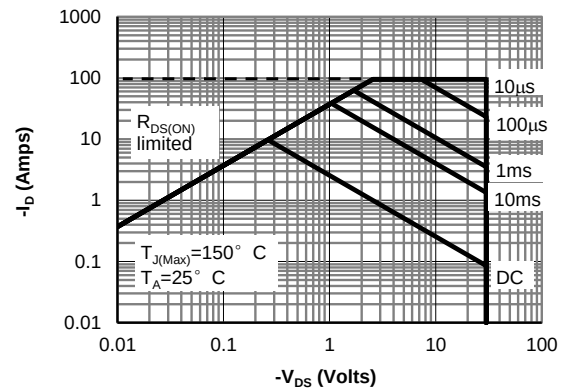


Figure 15: Maximum Forward Biased Safe Operating Area (Note H)

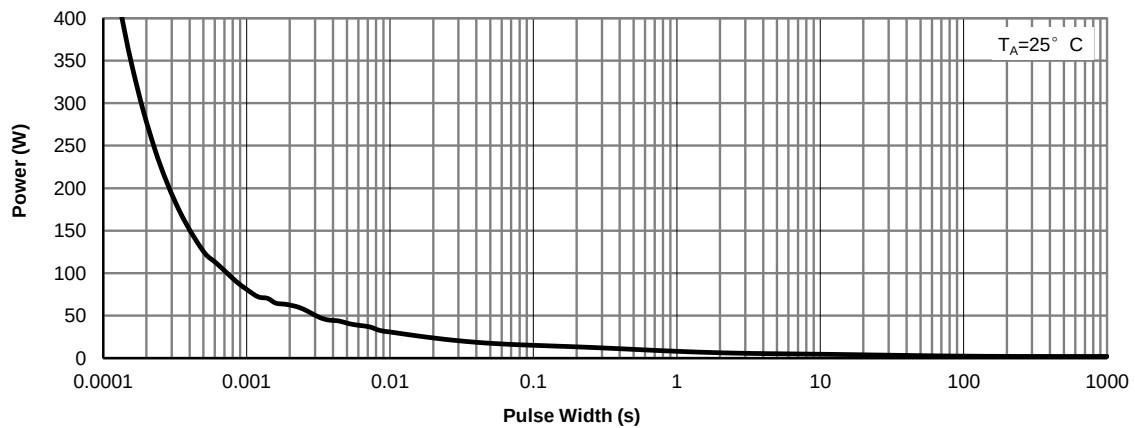


Figure 16: Single Pulse Power Rating Junction-to-Ambient (Note H)

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

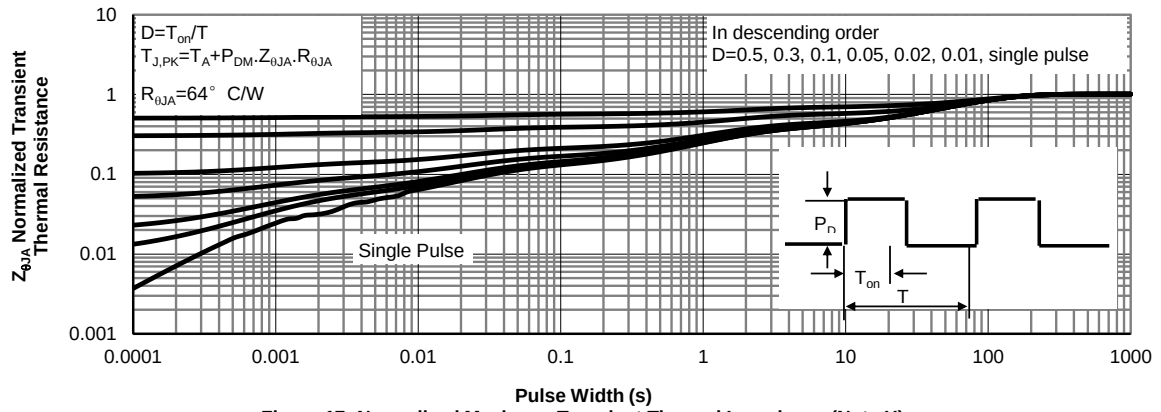
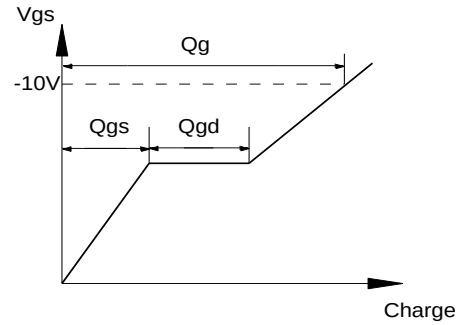
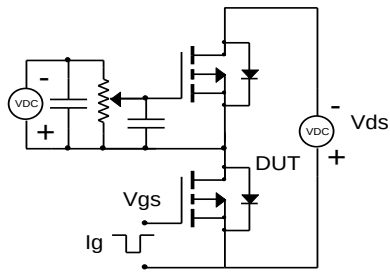
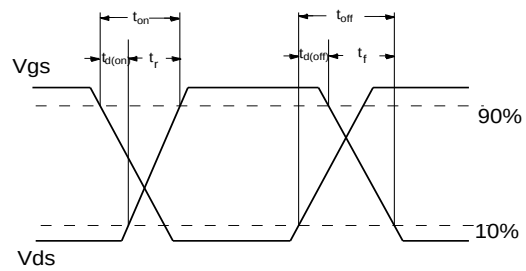
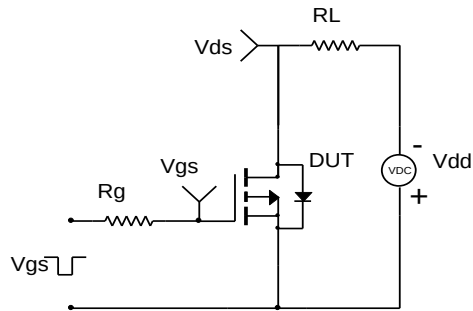


Figure 17: Normalized Maximum Transient Thermal Impedance (Note H)

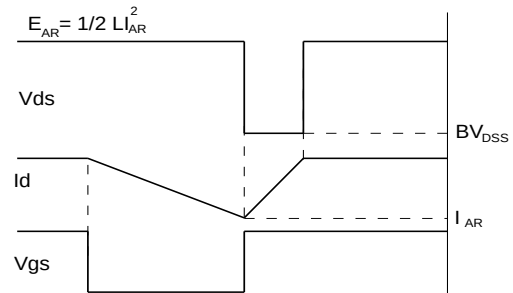
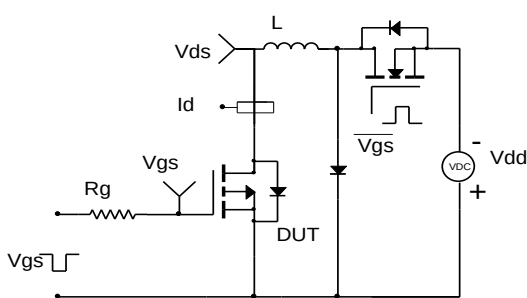
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms

