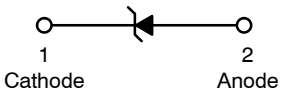
Zener Voltage Regulators

200 mW SOD-523 Surface Mount

NSZ5V6V2



This series of Zener diodes is packaged in a SOD-523 surface mount package. They are designed to provide voltage regulation protection and are especially attractive in situations where space is at a premium. They are well suited for applications such as cellular phones, hand held portables, and high density PC boards.

SOD-523
CASE 502
PLASTIC

Specification Features

- Standard Zener Breakdown Voltage of 5.6 V
- Steady State Power Rating of 200 mW
- Small Body Outline Dimensions:
  0.047″ x 0.032″ (1.20 mm x 0.80 mm)
- Low Body Height: 0.028″ (0.7 mm)
- ESD Rating of Class 3 (>16 kV) per Human Body Model
- Tight Tolerance V_Z
- These are Pb-Free Devices

Mechanical Characteristics

CASE: Void-free, transfer-molded, thermosetting plastic
Epoxy Meets UL 94, V-0
LEAD FINISH: 100% Matte Sn (Tin)
MOUNTING POSITION: Any
QUALIFIED MAX REFLOW TEMPERATURE: 260 °C
Device Meets MSL 1 Requirements

MARKING DIAGRAM

CT = Specific Device Code
M = Date Code*
▪ = Pb-Free Package

(Note: Microdot may be in either location)

* Date Code orientation may vary depending upon manufacturing location.

MAXIMUM RATINGS

Rating	Symbol	Max	Unit
Total Device Dissipation FR-5 Board, (Note 2) @ T_A = 25 °C Derate above 25 °C	P_D	200 1.5	mW mW/°C
Thermal Resistance from Junction-to-Ambient	$R_{\theta JA}$	635	°C/W
Junction and Storage Temperature Range	T_J, T_{stg}	−65 to +150	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.
2. FR-4 Minimum Pad.

ORDERING INFORMATION

Device	Package	Shipping†
NSZ5V6V2T5G	SOD-523 (Pb-Free)	3000/Tape & Reel

DISCONTINUED (Note 1)

NSZ5V6V2T1G	SOD-523 (Pb-Free)	3000/Tape & Reel

† For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

1. **DISCONTINUED:** This device is not available. Please contact your **onsemi** representative for information. The most current information on this device may be available on www.onsemi.com.

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ELECTRICAL CHARACTERISTICS

(T_A = 25 °C unless otherwise noted,
V_F = 0.9 V Max. @ I_F = 10 mA for all types)

Symbol	Parameter
V_Z	Reverse Zener Voltage @ I_{ZT}
I_{ZT}	Reverse Current
Z_{ZT}	Maximum Zener Impedance @ I_{ZT}
I_{ZK}	Reverse Current
Z_{ZK}	Maximum Zener Impedance @ I_{ZK}
I_R	Reverse Leakage Current @ V_R
V_R	Reverse Voltage
I_F	Forward Current
V_F	Forward Voltage @ I_F
ΘV_Z	Maximum Temperature Coefficient of V_Z
C	Max. Capacitance @V_R = 0 and f = 1 MHz

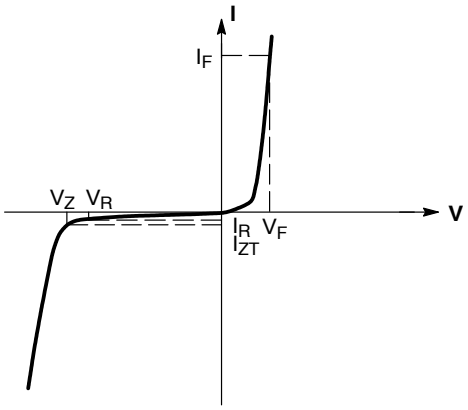


Figure 1. Zener Voltage Regulator

ELECTRICAL CHARACTERISTICS (V_F = 0.9 Max @ I_F = 10 mA for all types)

Device*	Test Current Izt mA	Zener Voltage VZ		Z_{ZK} I_Z = 1.0 mA Ω Max	Z_{ZT} I_Z = IZT @ 10% Mod Ω Max	Max IR @ VR		d_{VZ}/dt (mV/k) @ I_{ZT1} = 5 mA		C pF Max @ V_R = 0 f = 1 MHz
		Min	Max			μA	V	Min	Max	
NSZ5V6V2T1G	5.0	5.49	5.73	200	40	1.0	2.0	−2.0	2.5	200

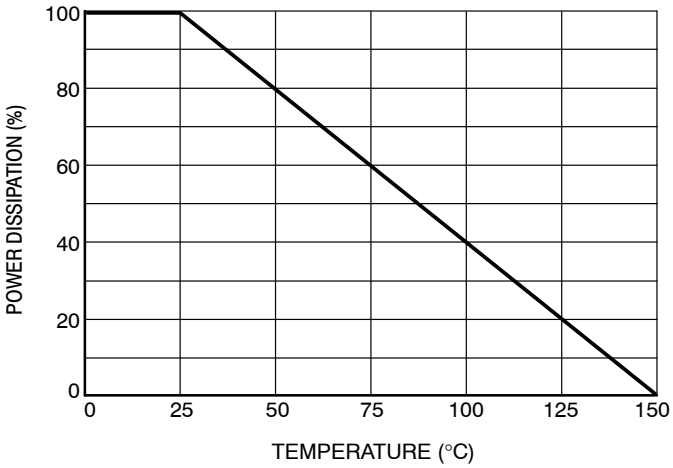


Figure 2. Steady State Power Derating

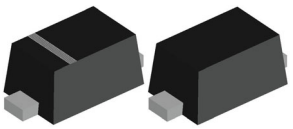
Share Feedback
Your Opinion Matters

REVISION HISTORY

Revision	Description of Changes	Date
2	Document rebranded to **onsemi** format. OPN NSZ5V6V2T1G marked as discontinued.	2/5/2026

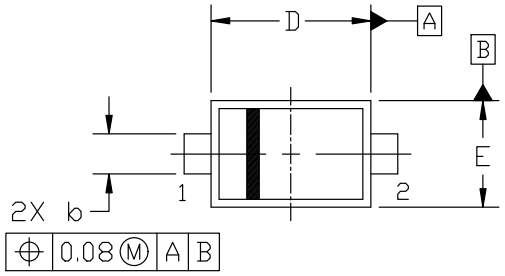
This document has undergone updates prior to the inclusion of this revision history table. The changes tracked here only reflect updates made on the noted approval dates.

Share Feedback
Your Opinion Matters

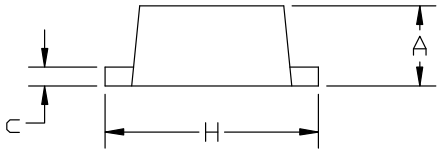


SOD−523 1.20x0.80x0.60
CASE 502
ISSUE F

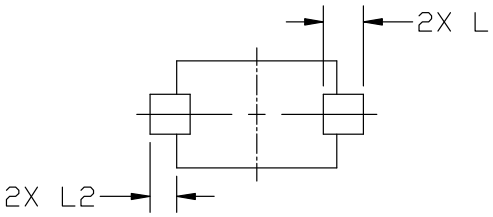
DATE 08 FEB 2024



TOP VIEW



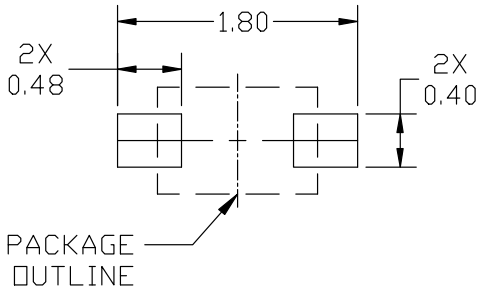
SIDE VIEW



BOTTOM VIEW

NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2018.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH, MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

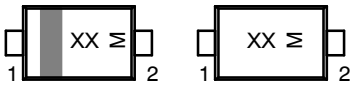
	MILLIMETERS		
DIM	MIN.	NOM.	MAX.
A	0.50	0.60	0.70
b	0.25	0.30	0.35
c	0.07	0.14	0.20
D	1.10	1.20	1.30
E	0.70	0.80	0.90
H	1.50	1.60	1.70
L	0.30 REF		
L2	0.15	0.20	0.25



RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



STYLE 1 STYLE 2

XX = Specific Device Code
M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb−Free indicator, “G” or microdot “▪”, may or may not be present. Some products may not follow the Generic Marking.

STYLE 1:
PIN 1. CATHODE (POLARITY BAND)
2. ANODE

STYLE 2:
NO POLARITY

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DESCRIPTION:	**SOD−523 1.20x0.80x0.60**		**PAGE 1 OF 1**

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